

# BIOS Revision History

Bios name	Remarks
FS50S022	Date code: 02/08/2002 checksum: 2F00 Initial release.
FS50S030	Date code: 03/26/2002 checksum: BF00 1. Fixed Spread Spectrum enabled can not boot. 2. Add Item 'Onboard Lan Boot ROM' select. 3. Disable Shutdown Temperature item. 4. Add CPU fan function. 5. Fixed CPU fan function. Enter S1,CPU fan still action. 6. Fixed CPU Shutdown Temperature over 100'c.
FS50S036	Date code: 04/18/2002 checksum: 7300 1. Fixed BIOS can't detect slave H.D when P.O.S.T 2. Fixed slave H.D can't boot.
FS50S037	Date code: 05/09/2002 checksum: 7400 1. Fix P4-Celeron shown P4-Willamet 2. Update CPU Microcode.
FS50S039	Date code: 07/05/2002 checksum: 7500 1.Fix plugging pci vga card and then disable onboard vga. 2.Fix onboard lan boot rom. 3.set cpu fan AutoGuardian default value 'enable' 4.set cpu fan default value 'fan3'
FS50S042	Date code: 06/02/2003 checksum: 8200 1.Fixed Intel celeron 2.0 GHz sign-on message show 2.0A GHz (100X2.0). 2.Fixed use Dmicfg.exe to detect Intel celeron 2.0 GHz L2 catch show error. 3.Changed value of SafeRatio for Intel sample P4 CPU with Hyper-Threading. 4.Take off S3 item selection, cause by Sis chipset (650&651A&648A) can't support Hyper-Threading CPU resume from S3. 5.Support boot from USB flash ROM. 6.Update CPU code.

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