

BIOS Revision History

Bios name	Remarks
FN45S00B	Checksum: F400H Date: 05/02/03 Initial release.
FN45S00D	Checksum: 3200H Date: 05/28/03 Fixed system sometimes cannot boot.
FN45S00F	Checksum: 4800H Date: 05/30/03 Improve USB devices compatibility
FN45S00I	Checksum: 8500H Date: 06/09/03 1. Added the multiplier setting (x11.5, x12.0, and x12.5) in BIOS. 2. Added Spread Spectrum table when FSB200 CPU installed.
FN45S00J	Checksum: 8F00H Date: 10/03/03 Support 0.13 micron AMD Duron (Applebred) CPU.
FN45S00M	Checksum: A100H Date: 12/05/03 1. Added "C1 Disconnect" item in BIOS setup menu. 2. Added "IDE DMA transfer access" item in BIOS setup menu. 3. Improve DDR compatibility. 4. Support 0.18 micron AMD Duron (Palomino) CPU.
FN45S00O	Checksum: A200H Date: 04/14/03 1. Show "Dual Channel Mode Enable" string on POST screen. 2. Resolve a potential issue that may affect users that set Vcore to 2.7V.
FN45S00Q	Checksum: 5C00H Date: 08/04/04 Support 0.13 micron AMD K7 Sempron CPU.
FN45S00R	Checksum: 3E00H Date: 08/23/04 Increase AMD K7 Sempron 2200+ CPU compatibility.
FN45S00W	Checksum: AD00H Date: 04/22/05 Increase Barton low power CPU compatibility.

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