

BIOS Revision History

Bios name	Remarks
FN41SI0G	Date code: 12/05/2003 Initial release. Checksum:9000
FN41SI0H	Date code: 01/16/2004 Fixed S3 resume fail if enable FSB200 spread spectrum function. Checksum: F700
FN41SI0J	Date code: 04/14/2004 1. Show "Dual Channel Mode Enable" string on POST screen. 2. Resolve a potential issue that may affect users that set Vcore to 2.7V. Checksum: 8B00
FN41SI0K	Date code: 05/21/2004 Improve memory compatibility Checksum: 1F00
FN41SI0M	Date code: 08/04/2004 Support 0.13 micron AMD K7 Sempron CPU. Checksum: 5500
FN41SI0N	Date code: 08/23/2004 Increase AMD K7 Sempron 2200+ CPU compatibility. Checksum: B500
FN41SI0Q	Date code: 04/22/2005 Low power Barton CPU now showing correctly. Checksum: 7600

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