

BIOS Revision History

Bios name	Remarks
FB52S00D	Date code: 01/20/2003 Checksum: C900H Initial release.
FB52S00G	Date code: 06/06/2003 Checksum: E100H 1. Support P4 D1 Core Stepping CPU. (Northwood) 2. Add Bios code to support DDR333. 3. Set memory frequency default to Auto. Note: DDR333 is supported since FB52 Bom12 and later.

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