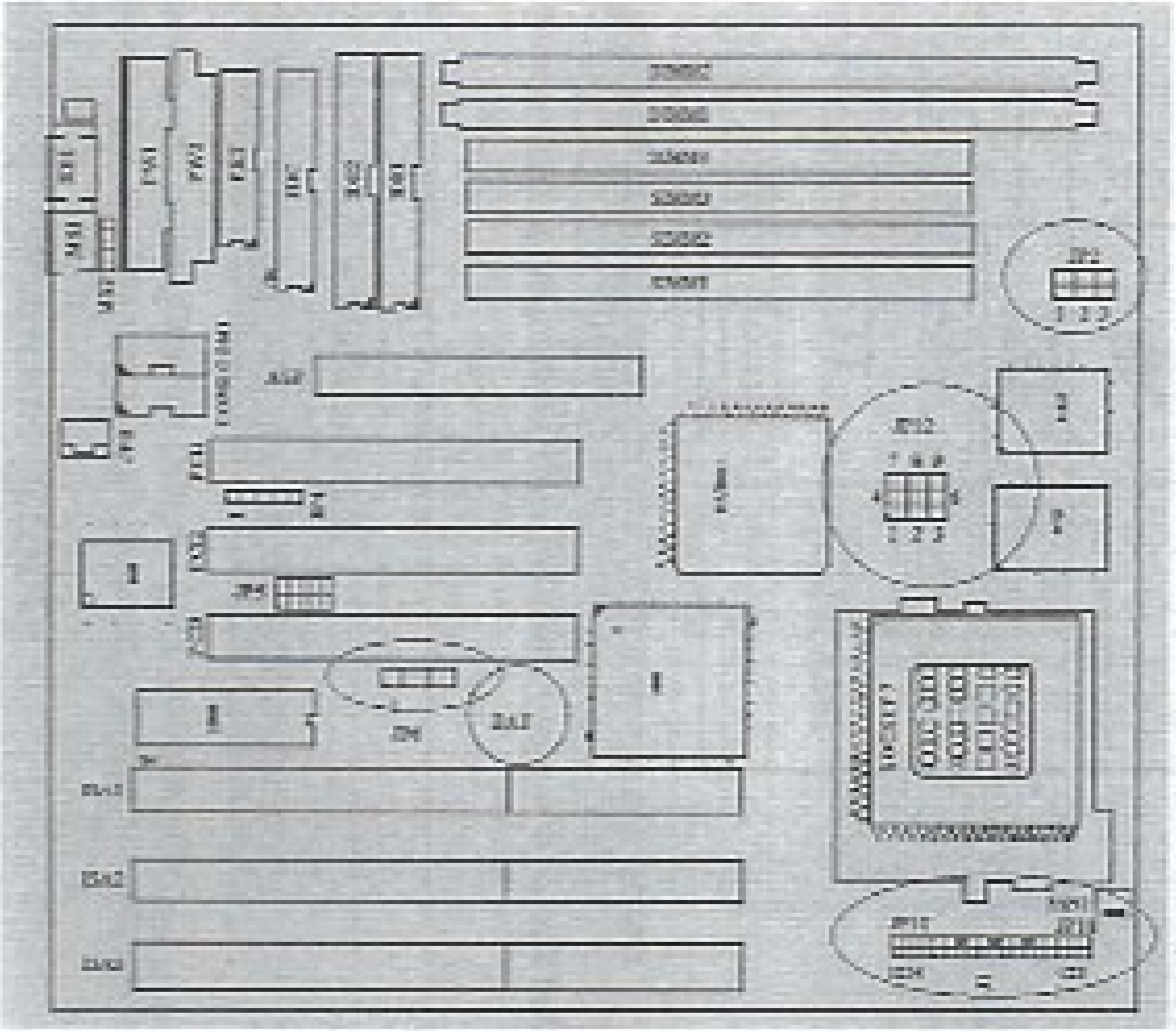


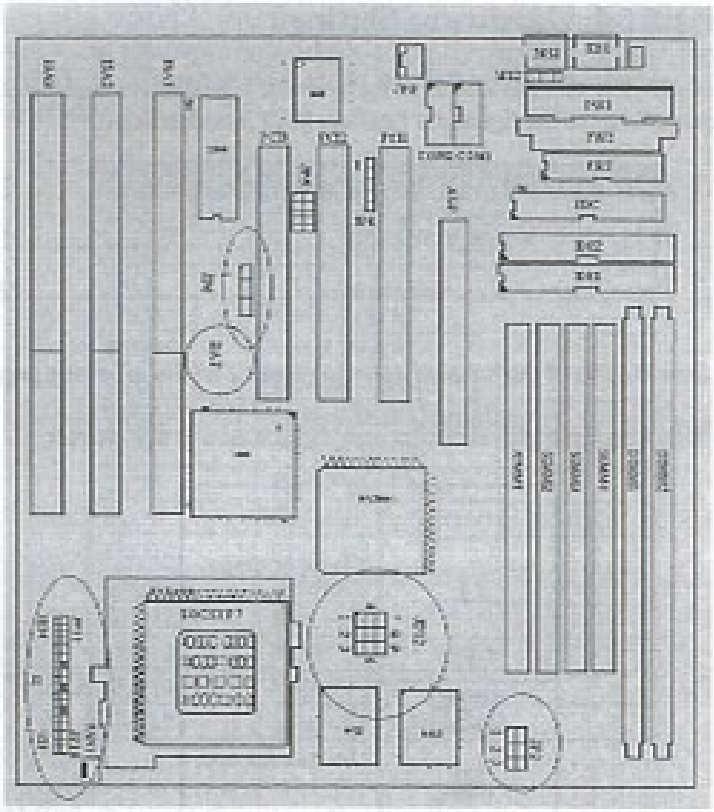
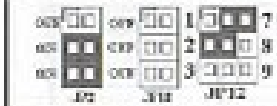
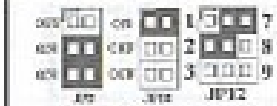
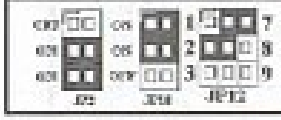


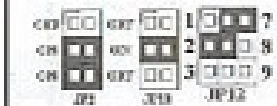
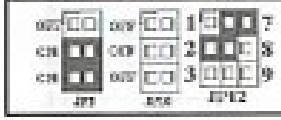
Figure 2: Jumper Locations of the Pentium MVP3 motherboard

4.1 JP2+JP10+JP12: CPU Frequency Selector (ZP-5V1 :JP2+JP10 ONLY)

For Intel Pentium CPU

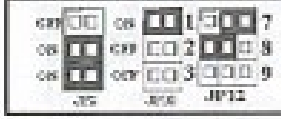
JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	1.5x	P54C-100
	66MHz	2x	P54C-133
	66MHz	2.5x	P54C/P55C-166

For Intel Pentium CPU

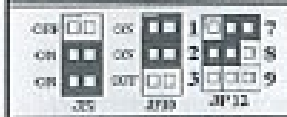
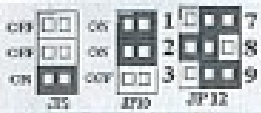
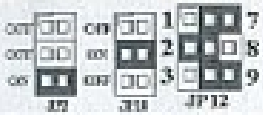
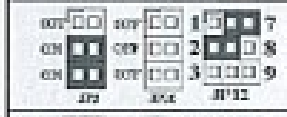
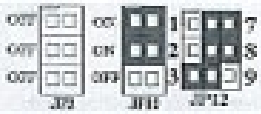
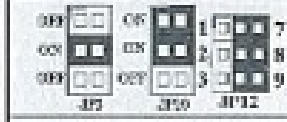
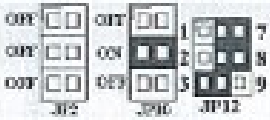
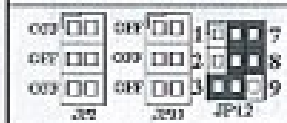
JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	3x	P54C/P55C-200
	66MHz	3.5x	P55C-233

**P55C = Pentium with MMX technology*

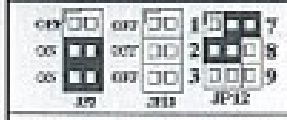
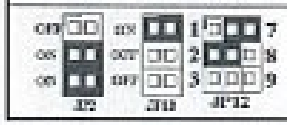
For Cyrix 6x86, 6x86L CPU

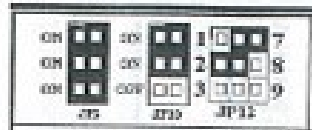
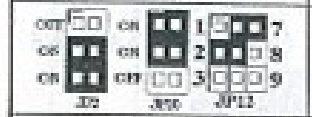
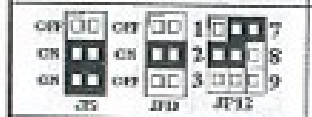
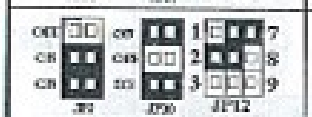
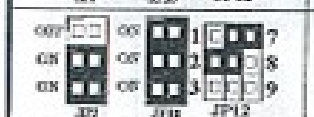
JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	2x	P166+ (133MHz)

For Cyrix 6x86MX CPU

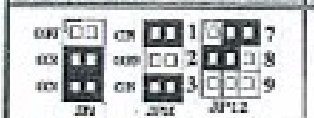
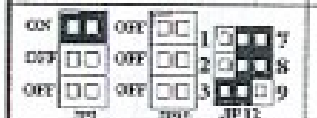
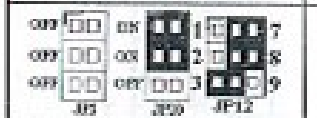
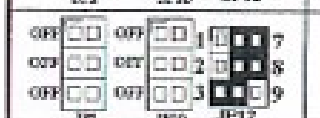
JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	2.5x	PR200 (166MHz)
	75MHz	2.5x	PR233 (188MHz)
	75MHz	3x	PR266 (225MHz)
	66MHz	3.5x	M II-300 (233MHz)
	100MHz	2.5x	M II-333 (233MHz)
	83MHz	2.5x	M II-266 (127MHz)
	100MHz	3x	M II-300 (300MHz)
	100MHz	3.5x	M II-350 (350MHz)

For AMD K5, K6 CPU

JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	1.5x	PR100
	66MHz	2x	PR133

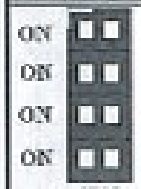
	60MHz	2.5x	PR150
	66MHz	2.5x	PR166 / K6-166
	66MHz	3x	K6-200
	66MHz	3.5x	K6-233
	66MHz	4x	K6-266
	66MHz	4.5x	K6-300

For AMD K6 3D CPU

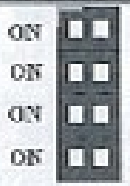
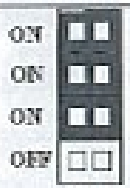
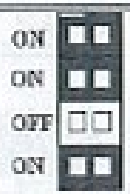
JP2+JP10+JP12	Bus Clock	Mutiplier	CPU FREQ.
	66MHz	4x	K6-2/266
	95MHz	3.5x	K6-2/333
	100MHz	2.5x	K6-2/250
	100MHz	3.0x	K6-2/300
	100MHz	3.5x	K6-2/350

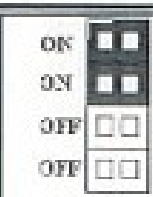
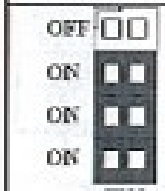
4.2 JP11: CPU Voltage Selector

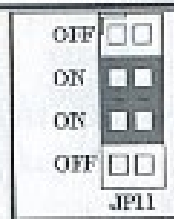
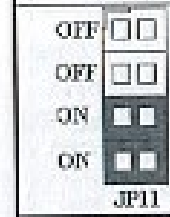
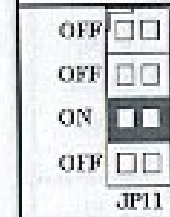
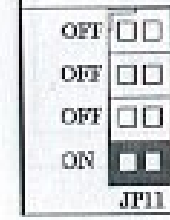
For Single Voltage CPU: Intel P54C, Cyrix 6x86, AMD K5

JP11	V_{IO}	V_{CORE}
	3.5V	3.5V

For Dual Voltage CPU: Intel P55C, Cyrix 6x86L/MX/MII, AMD K6/K6-2D

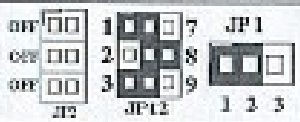
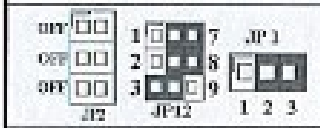
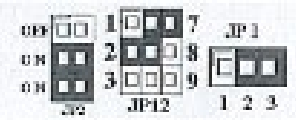
JP11	V_{IO}	V_{CORE}	CPU
	3.3V	3.5V	
	3.3V	3.4V	
	3.3V	3.3V	

	3.3V	3.2V	K6-233 (0.35µ)
	3.3V	3.1V	
	3.3V	3.0V	
	3.3V	2.9V	K6-166/200 6x86MX M II-266/300 333/350
	3.3V	2.8V	P55C 6x86L
	3.3V	2.7V	

 JP11	3.3V	2.6V	
 JP11	3.3V	2.5V	
 JP11	3.3V	2.4V	
 JP11	3.3V	2.3V	
 JP11	3.3V	2.2V	K6-233 /266/300 K6-2D 250/266/300 /333/350
 JP11	3.3V	2.1V	K6-233 (0.25μ)

 JP11	3.3V	2.0V	

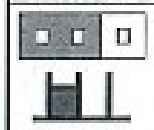
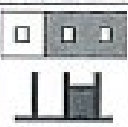
4.3 JP2+JP12+JP1: CPU CLOCK+DRAM CLOCK

JP2+JP12+JP1	CPU Clock	DRAM Clock
	100MHz	100MHZ
	100MHz	66MHZ
	66MHz	66MHZ

****** *(Note: When CPU bus is running at 100 MHz, the SDRAM must be PC-100 compliant DIMMs)*

4.4 JP6: Clear CMOS Selection

Use JP6, a 3-pin header, to clear the contents of the CMOS RAM. Do not clear the CMOS RAM unless it is absolutely necessary. You will lose your password, etc.

JP6	Function	JP6	Function
 1 2 3	Normal	 1 2 3	Clear CMOS

NOTE: If you are using an ATX power supply, the ATX-power connector should be disconnected from the motherboard to be able to clear CMOS.