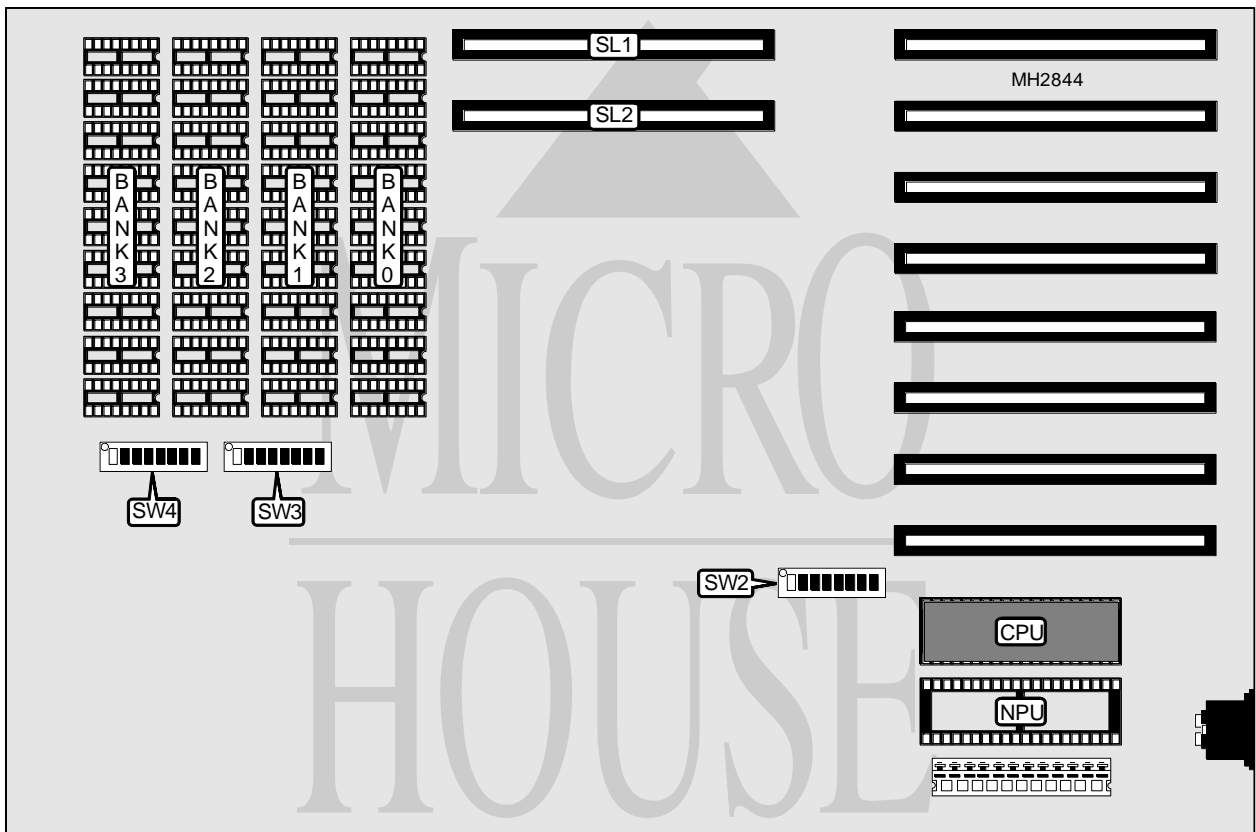


# OMICRON X TURBO

<b>Processor</b>	8088
<b>Processor Speed</b>	4/6MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	640KB
<b>Cache</b>	None
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	Proprietary slots (2)
<b>NPU Options</b>	8087



CONNECTIONS	
Purpose	Location
Proprietary slots	SL1 - SL2

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# AMERICAN RESEARCH CORPORATION X TURBO

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USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Turbo enabled	SW2/1	Off
Turbo disabled	SW2/1	On
í NPU disabled	SW2/2	On
NPU enabled	SW2/2	Off

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
64KB	(9) 4164	NONE	NONE	NONE
128KB	(9) 4164	(9) 4164	NONE	NONE
192KB	(9) 4164	(9) 4164	(9) 4164	NONE
256KB	(9) 4164	(9) 4164	(9) 4164	(9) 4164
256KB	(9) 41256	NONE	NONE	NONE
640KB	(9) 41256	(9) 41256	(9) 4164	(9) 4164

DRAM SWITCH CONFIGURATION		
Size	SW2/3	SW2/4
64KB	On	On
128KB	Off	On
192KB	On	Off
256KB	Off	Off
640KB	Off	Off

DRAM SWITCH CONFIGURATION								
Size	SW3/1	SW3/2	SW3/3	SW3/4	SW3/5	SW3/6	SW3/7	SW3/8
Bank 0 & 1 = 41256	Off	On	Off	On	Off	On	Off	On
Bank 2 & 3 = 4164	Off	On	Off	On	Off	On	Off	On

DRAM SWITCH CONFIGURATION (CON'T)								
Size	SW4/1	SW4/2	SW4/3	SW4/4	SW4/5	SW4/6	SW4/7	SW4/8
Bank 0 & 1 = 41256	Off	On	Off	On	Off	On	Off	On
Bank 2 & 3 = 4164	Off	On	Off	On	Off	On	Off	On

DRAM SWITCH CONFIGURATION								
Size	SW3/1	SW3/2	SW3/3	SW3/4	SW3/5	SW3/6	SW3/7	SW3/8
Bank 0 & 1 = 4164	On	Off	On	Off	On	Off	On	Off
Bank 2 & 3 = 4164	On	Off	On	Off	On	Off	On	Off

DRAM SWITCH CONFIGURATION (CON'T)								
Size	SW4/1	SW4/2	SW4/3	SW4/4	SW4/5	SW4/6	SW4/7	SW4/8
Bank 0 & 1 = 4164	On	Off	On	Off	On	Off	On	Off
Bank 2 & 3 = 4164	On	Off	On	Off	On	Off	On	Off

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# AMERICAN RESEARCH CORPORATION

## X TURBO

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MONITOR CONFIGURATION		
Type	SW2/5	SW2/6
Monochrome	Off	Off
40 x 25 color	Off	On
80 x 25 color	On	Off

FLOPPY DRIVE CONFIGURATION		
Type	SW2/7	SW2/8
1	On	On
2	Off	On
3	On	Off
4	Off	Off