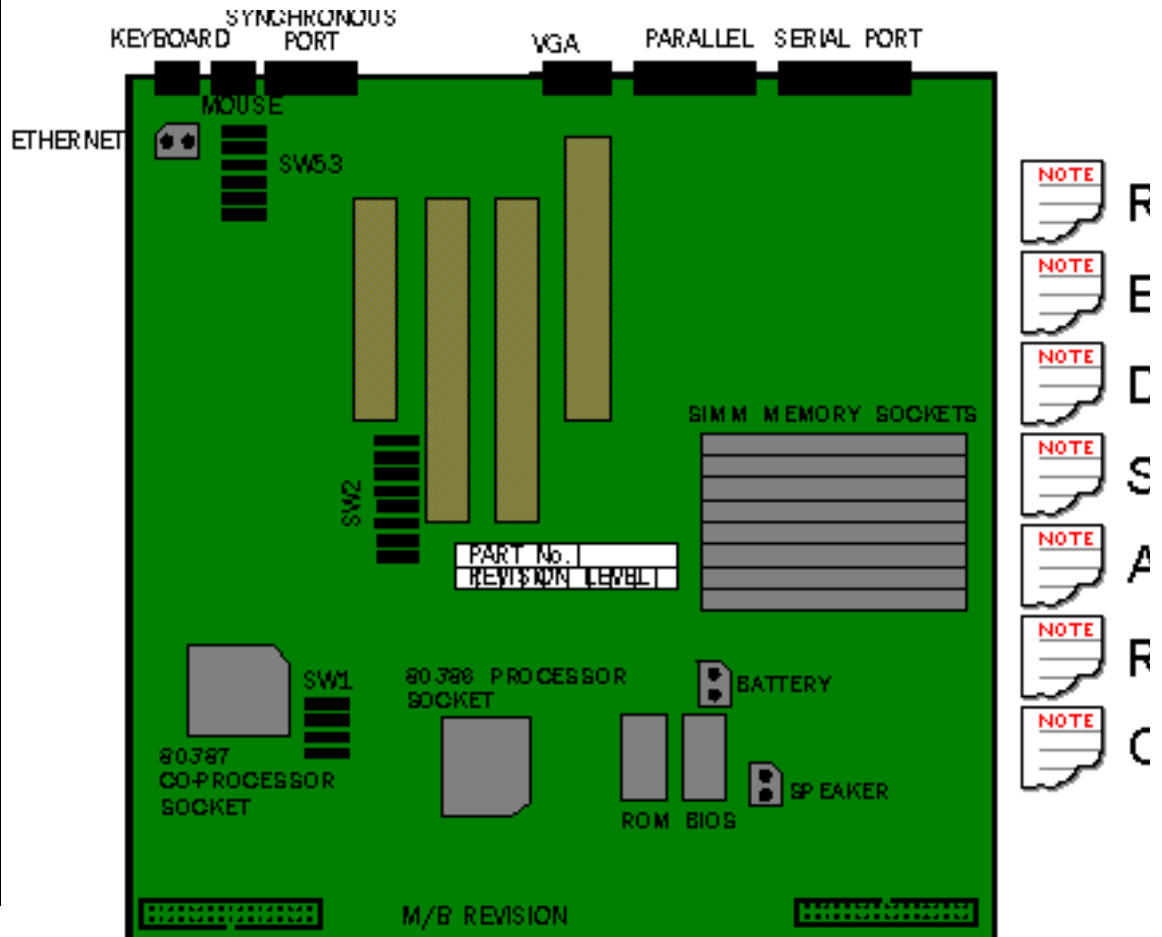


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VX FT 386 MkI (Titan Motherboard) (Rev 1.1.12)



RAM Configuration

Memory Fitted	Switches			SIMM Capa Bank 0
	1	2	3	
1 MByte	Off	Off	Off	256k
1 MByte	On	Off	Off	256k
2 MByte	Off	On	Off	256k
4 MByte	Off	Off	On	1 M

5 MByte	On	On	Off	256k
8 MByte	On	Off	On	1 M
12 MByte	Off	On	On	2 M
16 MByte	On	On	On	2 M



Hard Drives

MB	Make	Model	Type	Part Nu
100	Quantum	LPS105S	SCSI	TI47
380	IBM	0661	SCSI	TI47
510	Maxtor	LXT-535SY	SCSI	TI48:
1,000	Seagate	ST41200N	SCSI	TI43:
1,300	Seagate	ST41600N	SCSI	TI48:
2,000	Seagate	ST42400N	SCSI	TI49
Tape				
150	Archive	Viper	SCSI	TI43:
525	Archive	Viper	SCSI	TI49:
1.3GB	Archive	Python	SCSI	TI49:



Ethernet Options

On Board Thin and Thick Ethernet.



Switch Settings

SW1

Switch	On	Off
1	Pipeline 386 enable	Pipeline 386 disabl
2	Pipeline 385 enable	Pipeline 385 disabl
3	ROM's disabled	ROM's enabled
4	Refresh enabled	Refresh disabled

SW2

Switch	On	Off
1	RAM configuration	
2	RAM configuration	
3	RAM configuration	
4	CPU speed select	
5	CPU speed select	
6	Do not alter from ON	
7	Weitek present *	Weitek not present
8	Do not alter from ON	
* PAL required to enable		

RAM

Memory Fitted	Switches			SIMM Capacity	
	1	2	3	Bank 0	Bank 1
1 MByte	Off	Off	Off	256k	-
1 MByte	On	Off	Off	256k	-
2 MByte	Off	On	Off	256k	256k
4 MByte	Off	Off	On	1 M	-
5 MByte	On	On	Off	256k	1 M
8 MByte	On	Off	On	1 M	1 M
12 MByte	Off	On	On	2 M	1 M
16 MByte	On	On	On	2 M	2 M

Clock speed Switches

CPU speed MHz	Switches	
	4	5
25 MHz	Off	On

SW3

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No function	Either setting acceptable.
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SW4

All ON	BNC Connector Active (Thin Ethernet)
All OFF	D-Shell Connector Active (Thick Etherne

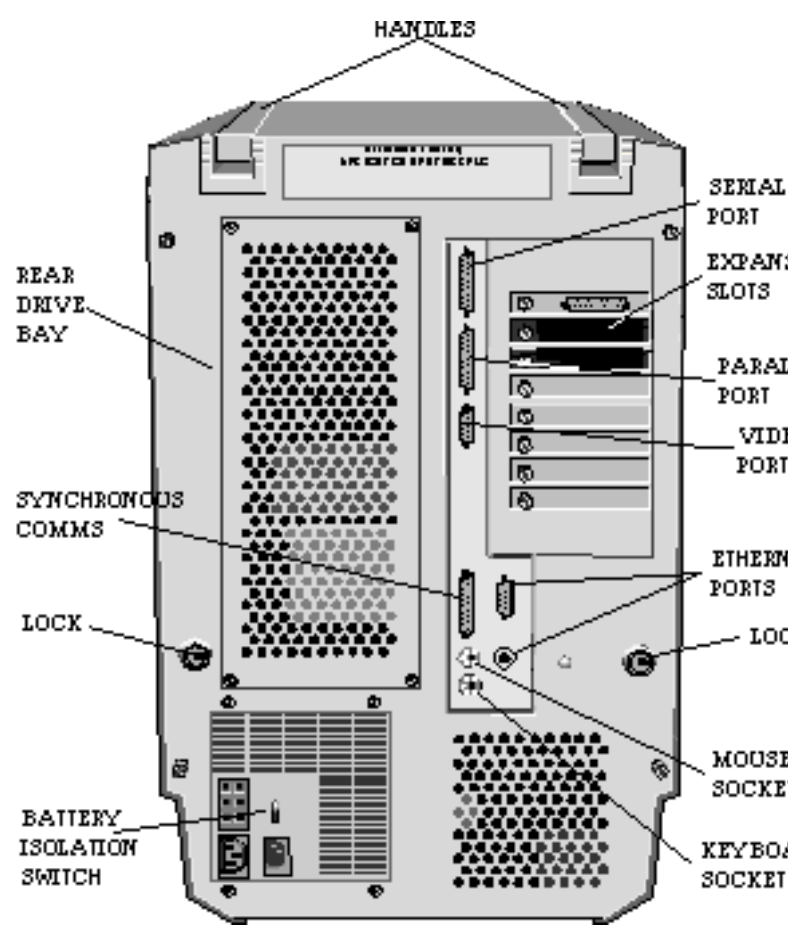


Additional Options

Apricot LOC Technology security.



Rear Panel



CMOS Discharge

To drain CMOS disconnect the lead running from the battery mounted on the board and allow approx. 20 minutes to drain.



C O M P U T I N G F O R A C O N N E C T E D I

