

VB7007

Mini-ITX Mainboard Manual

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Battery Recycling and Disposal



Only use the appropriate battery specified for this product. Do not re-use, recharge, or reheat an old battery. Do not attempt to force open the battery. Do not discard used batteries with regular trash. Discard used batteries according to local regulations.



Regulatory Compliance

FCC-B Radio Frequency Interference Statement

This equipment has been tested and found to comply with the limits for a class B digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his personal expense.

Notice 1

The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Notice 2

Shielded interface cables and A.C. power cord, if any, must be used in order to comply with the emission limits.





KC Korean Certificate Statement

KCC-REM-VNT-VB7007 (B) VIA Technologies, Inc. VB7007

Global Brands MFR LTD PCBA Business Unit 2009/02

Made in China

B급 기기 (가정용 방송통신기자재)

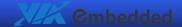
Class B Equipment (For Home Use Broadcasting & Communication Equipment) 이 기기는 가정용(B급) 전자파적합기기로서 주로 가정에서 사용하는 것을 목적으로 하며,모든 지역에서 사용할 수 있습니다.

This electromagnetic wave equipment is suitable for home use (Class B) and may be used mainly at home and in other areas.



Safety Precautions

ш	Aiways	read the safety instructions carefully.				
	Keep this User's Manual for future reference.					
	Keep this equipment away from humidity.					
	Lay this equipment on a reliable flat surface before setting it up					
	The op	enings on the enclosure are for air convection hence				
	protect	ts the equipment from overheating. Do not cover the				
	openin	gs.				
	Make s	sure the voltage of the power source and adjust properly				
	110/22	0V before connecting the equipment to the power inlet.				
	Place tl	he power cord in such a way that people cannot step on it				
	Do not place anything over the power cord.					
☐ Always unplug the power cord before inserting any add-c						
	or mod	dule.				
	All cau	tions and warnings on the equipment should be noted.				
☐ Never pour any liquid into the opening. Liquid can cause						
	damag	e or electrical shock.				
	If any c	of the following situations arises, get the equipment				
	checke	d by authorized service personnel:				
	0	The power cord or plug is damaged.				
	0	Liquid has penetrated into the equipment.				
	0	The equipment has been exposed to moisture.				
	0	The equipment has not worked well or you cannot get it				
		work according to User's Manual.				
	0	The equipment has dropped and damaged.				
	0	The equipment has obvious sign of breakage.				
	Do not	leave this equipment in an environment unconditioned or				
	in a sto	orage temperature above 60°C (140°F). The equipment				
	may be	e damaged.				



Box Contents

- □ 1 x VB7007 mainboard
- \square 1 x ATA-66/100/133 IDE ribbon cable
- ☐ 1 x I/O bracket



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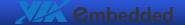
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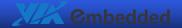
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1:

Specifications

The compact and highly integrated VIA VB7007 mainboard comes with an integrated VIA ${\rm C7}^{\rm \$}$ NanoBGA2 processor, boasting of ultra-low power consumption and cool operation.



Mainboard Specifications

CPU

- VIA C7[®]-D 1.6 GHz NanoBGA2 processor with fansink
- VIA C7[®] 1.0 GHz NanoBGA2 processor (fanless)

Chipset

- VIA CN700 North Bridge
- VIA VT8237S South Bridge

Graphics

 Integrated VIA UniChrome™ Pro II AGP graphics with 2D/3D and video acceleration

Memory

One DDR2 667¹/533 DIMM slots (up to 1 GB)

Expansion Slot

One PCI slot

IDE

One UltraDMA 133/100/66/33 connector

LAN

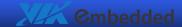
- One VIA VT6103L 10/100 Mbps Fast Ethernet PHY
- One VIA VT6107 10/100 Mbps Fast Ethernet controller or one VIA VT6122 Gigabit LAN controller²

Audio Codec

VIA VT1708B High Definition audio codec

¹ DDR2 667 MHz memory modules can be used but the effective speed with be limited to 533 MHz.

² The VT6122 Gigabit LAN controller is a manufacturing option.



Back Panel I/O Ports

- One VGA port
- One COM port
- One parallel port
- Two RJ-45 LAN ports
- Four USB 2.0 ports
- Three audio jacks: Line-in, Line-out, and MIC-in
- Two PS/2 ports: mouse and keyboard

Onboard I/O Connectors

- Three COM pin headers (with 5V/12V select)
- One digital I/O pin header (GPI x 4, GPO x 4)
- Two USB 2.0 pin headers (up to four additional ports)
- Two SATA connectors
- One SMBus pin header
- One S/PDIF-out connector
- One dual-channel LVDS panel connector (18/24-bit)
- One LVDS panel power selector (5V/3.3V)
- One LVDS inverter pin header
- One LVDS inverter power selector (5V/12V)
- One FIR pin header
- One chassis front panel pin header
- One front audio pin header
- Two fan connectors (CPU and SYS)
- One ATX power connector



System Monitoring and Management

- AC power failure recovery
- RTC timer
- Wake-on LAN, keyboard, and mouse

Supported OS

- Windows 2000
- Windows XP
- Windows CE
- Windows XPe
- Linux

BIOS

- Award BIOS
- SPI 4/8Mbit flash memory

Operating Environment

- Temperature range: 0°C 50°C
- Humidity range: 0% 95%³

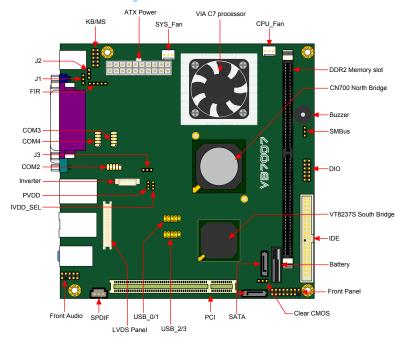
Form Factor

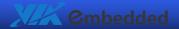
• Mini-ITX (17 cm x 17 cm)

³ Relative humidity and non-condensing.

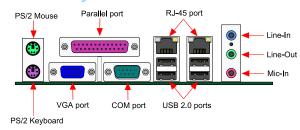


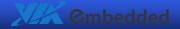
Mainboard Layout





Back Panel Layout





2:

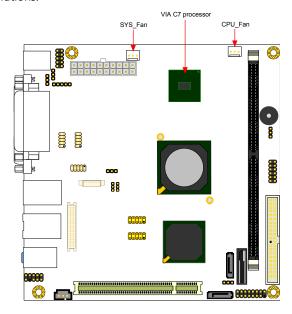
Installation

This chapter provides you with information about hardware installation procedures. It is recommended to use a grounded wrist strap before handling computer components. Electrostatic discharge (ESD) can damage some components.



CPU

The VIA VB7007 mainboard is packaged with either the VIA C7[®]-D 1.6 GHz or VIA C7[®] 1.0 GHz NanoBGA2 processor. The VIA C7[®]-D 1.6 GHz processor requires a heatsink with fan to provide sufficient cooling. The VIA C7[®] 1.0 GHz processor is for fanless solutions.

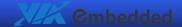


CPU Fan and System Fan: CPUFAN and SYSFAN

The CPU_FAN (CPU fan) and SYS_FAN (system fan) run on +12V and maintain system cooling. When connecting the wire to the connectors, always be aware that the red wire (positive wire) should be connected to the +12V. The black wire is Ground and should always be connected to GND.

Signal	
FANIO	
+12V	
GND	
	FANIO +12V

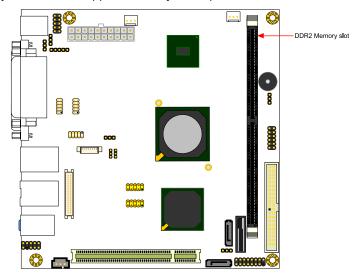




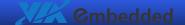
Memory Module Installation

Memory Slot: DDR2 DIMM

The VIA VB7007 mainboard has one 240-DIMM slot for DDR2 667/533 SDRAM memory modules and supports memory sizes up to 1 GB. 4

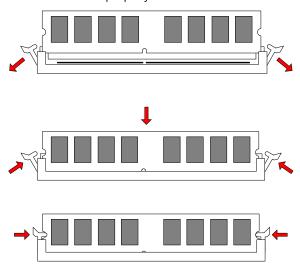


 $^{^{4}}$ 1 GB SDRAM only supported with 64 MB x 8-bit x 16 configuration.



DDR2 SDRAM Module Installation Procedures

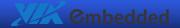
- Locate the DIMM slot in the motherboard.
- Unlock a DIMM slot by pressing the retaining clips outward.
- Align a DIMM on the socket such that the notch on the DIMM matches the break on the slot.
- Firmly insert the DIMM into the slot until the retaining clips snap back in place and the DIMM is properly seated.



Available DDR2 SDRAM Configurations

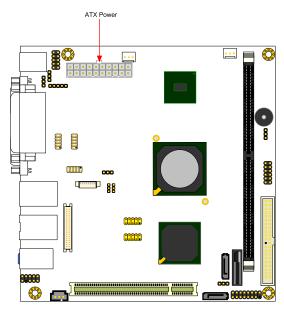
Refer to the table below for available DDR2 SDRAM configurations on the mainboard.

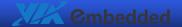
Slot	Module Size	Total
DIMM1	64 MB, 128 MB, 256 MB, 512 MB, 1 GB	64 MB - 1 GB
Maximum	64 MB - 1 GB	



Connecting the Power Supply

The VIA VB7007 mainboard supports a conventional ATX power supply for the power system. Before inserting the power supply connector, always make sure that all components are installed correctly to ensure that no damage will be caused.

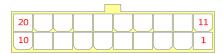




ATX 20-Pin Power Connector

To connect the power supply, make sure the power plug is inserted in the proper orientation and the pins are aligned. Then push down the plug firmly into the connector.

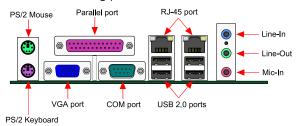
Pin	Signal	Pin	Signal
1	+3.3V	11	+3.3V
2	+3.3V	12	-12V
3	GND	13	GND
4	+5V	14	Power Supply On
5	GND	15	GND
6	+5V	16	GND
7	GND	17	GND
8	Power Good	18	-5V
9	+5V Standby	19	+5V
10	+12V	20	+5V





Back Panel Ports

The back panel has the following ports:



PS/2 ports: mouse and keyboard

The green PS/2 port is used to connect a mouse. The purple PS/2 port is used to connect a keyboard.

VGA port

The 15-pin VGA port is for connecting to analog displays.

Serial port: COM1

The 9-pin COM port is for pointing devices or other serial devices.

Parallel port

The parallel port is for connecting to LPT devices.

USB Ports

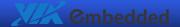
Four standard USB 2.0 ports are provided on the back panel.

LAN Ports

The mainboard provides two Fast Ethernet ports controlled with separate VIA Fast Ethernet controllers. One of the controllers can be substituted for a Gigabit LAN controller as a manufacturing option.

Audio jacks: Line-in, Line-out, and MIC-in

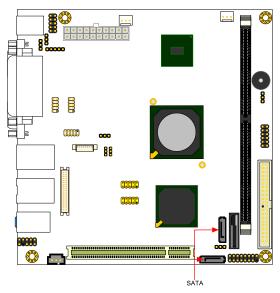
Three audio jacks are provided on the back panel.



Connectors

Serial ATA Connectors: SATA1 and SATA2

The current SATA interface allows up to 300 MB/s data transfer rate, faster than the standard parallel ATA with 133 MB/s (Ultra DMA).

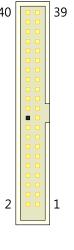




IDE Connector: IDE1

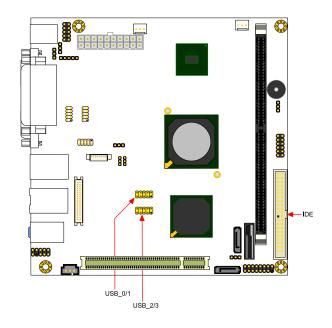
The mainboard has an Ultra DMA 133/100/66/33 controller. You can connect up to two IDE devices in any combination.

Pin	Signal	Pin	Signal	
1	-IDERST1	2	GND	_
3	PD_7	4	PD_8	_
5	PD_6	6	PD_9	_
7	PD_5	8	PD_10	
9	PD_4	10	PD_11	
11	PD_3	12	PD_12	
13	PD_2	14	PD_13	
15	PD_1	16	PD_14	_
17	PD_0	18	PD_15	
19	GND	20	KEY	
21	PDREQ	22	GND	
23	-PIOW	24	GND	
25	-PIOR	26	GND	
27	PDRDY	28	GND	
29	-PDACK	30	GND	
31	IRQ14_R	32	NC	
33	PD_A1	34	P_ATA66	
35	PD_A0	36	PD_A2	
37	-PCS_1	38	-PCS_3	
39	-HD_LED1	40	GND	



If two drives are connected to a single cable, the jumper on the second drive must be set to slave mode. Refer to the drive documentation supplied by the vendor for the jumper settings.





USB Pin Connector: USB_0/1 and USB_2/3

The mainboard provides two 10-pin USB pin connectors (allowing up to four additional USB 2.0 ports).

USB_0/1

Pin	Signal	Pin	Signal
1	VUSB0	2	VUSB0
3	USBD_T0-	4	USBD_T1-
5	USBD_T0+	6	USBD_T1+
7	GND	8	GND
9	KEY	10	GND



USB_2/3

Pin Signal		Pin	Signal
1	VUSB0	2	VUSB0
3	USBD_T2-	4	USBD_T3-
5	USBD_T2+	6	USBD_T3+
7	GND	8	GND
9	KEY	10	GND

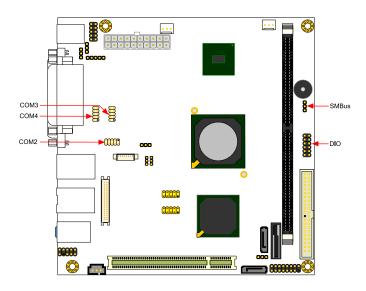


Digital I/O Connector: DIO

General purpose digital input and output.

Pin Signal		Pin	Signal
1	+5V	2	+12V
3	GPO1	4	GPI4
5	GPO7	6	GPI13
7	GPO26	8	GPI14
9	GPO27	10	GPI15
11	GND	12	GND







System Management Bus Connector: SMBUS

This pin header allows you to connect SMBus (System Management Bus) devices. Devices communicate with a SMBus host and/or other SMBus devices using the SMBus interface.

Pin	Signal	
1	SMBCK	
2	SMBDT	
3	GND	



Serial Port Connector: COM2/3/4

COM pin headers can be used to attach an additional port for serial devices.

Pin	Signal	Pin	Signal
1	COM_DCD	2	COM_SIN
3	COM_SOUT	4	COM_DTR
5	GND	6	COM_DSR
7	COM_RTS	8	COM_CTS
9	COM_RI	10	KEY





Case Connector: F_PANEL

The F_PANEL pin header allows you to connect the power switch, reset switch, power LED, sleep LED, HDD LED and the case speaker.

Pin	Signal	Pin	Signal
1	+5VDUAL	2	+5V
3	+5VDUAL	4	HD_LED
5	-PLED_2	6	PW_BN
7	+5V	8	GND
9	NC	10	RST_SW
11	NC	12	GND
13	SPEAK	14	+5V
15	KEY	16	-SLEEP_LED



Power LED (PLED)

The LED will light when the system is on. If the system is in S1 (POS - Power On Suspend) or S3 (STR - Suspend To RAM) state, the LED will blink.

HDD LED (HD_LED)

HDD LED shows the activity of a hard disk drive. Avoid turning the power off when the HDD LED is still on. Connect the HDD LED from the system case to this pin.

Power Switch (PW_BN)

Connect to a 2-pin power button switch. Pressing this button will turn the system power on or off.

Speaker (SPEAK)

This pin header is for connecting to the system buzzer.

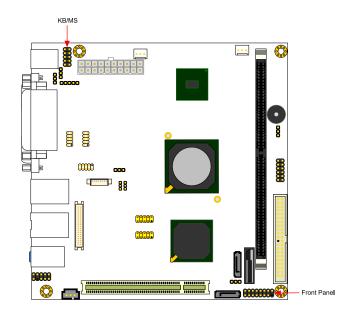
Reset Switch (RST_SW)

The reset switch is used to reboot the system rather than turning the power ON/OFF. Avoid rebooting the system, if the HDD is still working. Connect the reset switch from the system case to this pin.

Sleep LED (SLEEP_LED)

The SLEEP LED is lit when the system is in the S1 (POW-Power On Suspend)



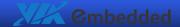


KBMS Connector: KB/MS

The mainboard provides a PS2 pin header to attach a PS2 keyboard and mouse.

Pin	Signal	Pin	Signal
1	+5VDUAL	2	GND
3	KB_CLK	4	KB_DATA
5	EKBCLK	6	EKBDATA
7	MS_CLK	8	MS_DATA
9	EMSCLK	10	EMSDATA

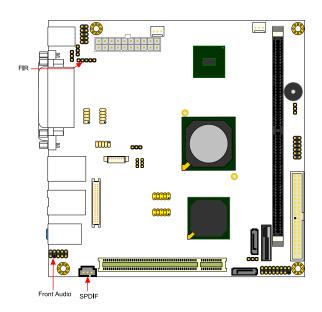




Fast Infrared Connector: FIR

Pin	Signal
1	+5V
2	IRRX1
3	IRRX
4	GND
5	IRTX

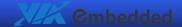




S/PDIF Connector: SPDIF

Pin	Signal	
1	+5V	
3	SPDIF_OUT	
5	GND	



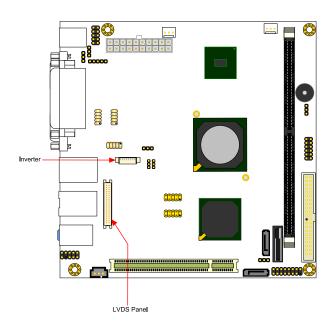


Front Audio: F_AUDIO

This connector allows you to connect a front audio panel to the mainboard.

Pin	Signal	Pin	Signal
1	MIC2_IN_L	2	AGND
3	MIC2_IN_R	4	+3.3V
5	HP_OUT_R	6	NC
7	NC	8	KEY
9	HP_OUT_L	10	NC

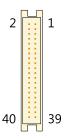






LVDS Connector: PANEL_CONN

Pin	Signal	Pin	Signal
1	-A4_L	2	PVDD
3	A4_L	4	PVDD
5	GND	6	GND
7	-A5_L	8	GND
9	A5_L	10	-A0_L
11	GND	12	A0_L
13	-A6_L	14	GND
15	A6_L	16	-A1_L
17	GND	18	A1_L
19	-CLK2_L	20	GND
21	CLK2_L	22	-A2_L
23	GND	24	A2_L
25	-A7_L	26	GND
27	A7_L	28	-CLK1_L
29	NC	30	CLK1_L
31	NC	32	GND
33	NC	34	-A3_L
35	NC	36	A3_L
37	NC	38	SPCLK1
39	NC	40	SPD1



LVDS Inverter: INVERTER

Pin	Signal
1	VCC
2	VCC
3	BAKLITE
4	NC
5	BAKLITE
6	SMBUS OUT
7	GND
8	GND





Jumpers

The mainboard provides jumpers for setting some mainboard functions. This section will explain how to change the settings of the mainboard functions using the jumpers.

Clear CMOS Connector: CLEAR_CMOS

The onboard CMOS RAM stores system configuration data and has an onboard battery power supply. To reset the CMOS settings, set the jumper on pins 1 and 2 while the system is off. Return the jumper to pins 2 and 3 afterwards. Setting the jumper while the system is on will damage the mainboard.

Setting	1	2	3
Normal Operation	ON	ON	OFF
Clear CMOS setting	OFF	ON	ON



Caution:

Except when clearing the RTC RAM, never remove the cap on CLEAR_CMOS jumper default position. Removing the cap will cause system boot failure. Avoid clearing the CMOS while the system is on; it will damage the mainboard.

Voltage Selector for COM2/3/4: J3/2/1

This VCC selector is to determine the input voltage of each COM connector.

Setting	1	2	3
+5V	ON	ON	OFF
+12V	OFF	ON	ON

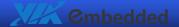


Panel Power Selector: PVDD

Setting	1	2	3
5V	ON	ON	OFF
3.3V	OFF	ON	ON

Inverter Power Selector: IVDD_SEL

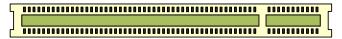
Setting	1	2	3
5V	ON	ON	OFF
12V	OFF	ON	ON



Slots

Peripheral Component Interconnect: PCI_SLOT

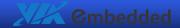
The PCI slot allows you to insert a PCI expansion card. First unplug the power supply before adding or removing expansion cards. Read the documentation for the expansion card to see if any changes to the system are necessary.



PCI Interrupt Request Routing

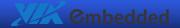
The IRQ (interrupt request line) are hardware lines over which devices can send interrupt signals to the microprocessor. The "PCI & LAN" IRQ pins are typically connected to the PCI bus –INT_A ~ -INT_D pins as follows:

	Order 1	Order 2	Order 3	Order 4
PCI slot	-INT_B	-INT_C	-INT_D	-INT_A



3: BIOS Setup

This chapter gives a detailed explanation of the BIOS setup functions.



Entering the BIOS Setup Menu

Power on the computer and press < **Delete**> during the beginning of the boot sequence to enter the BIOS setup menu. If you missed the BIOS setup entry point, restart the system and try again.

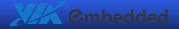


Control Keys

Keys	Description
\uparrow	Move to the previous item
1	Move to the next item
<u>-</u>	Move to the item in the left side
$\boxed{\rightarrow}$	Move to the item in the right side
Enter	Select the item
Esc	Jumps to the Exit menu or returns to the main menu from a submenu
Page Up	Increase the numeric value or make changes
Page Down	Decrease the numeric value or make changes
+	Increase the numeric value or make changes
	Decrease the numeric value or make changes



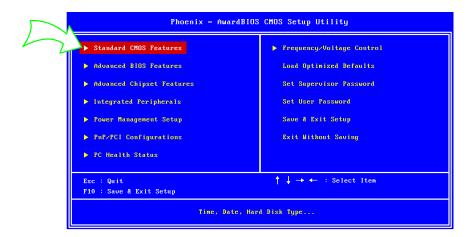
Keys	Description
F1	General help, only for Status Page Setup Menu and Option Page Setup Menu
F5	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
F6	Load the default CMOS value from Fail-Safe default table, only for Option Page Setup Menu
F7	Load Optimized defaults
F10	Save all the CMOS changes and exit

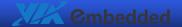


Navigating the BIOS Menus

The main menu displays all the BIOS setup categories. Use the **Left**>/**Right**> and **Up**>/**Down**> arrow keys to select any item or sub-menu. Descriptions of the selected/highlighted category are displayed at the bottom of the screen.

The small triangular arrowhead symbol next to a field indicates that a sub-menu is available (see figure below). Press **<Enter>** to display the sub-menu. To exit the sub-menu, press **<Esc>**.





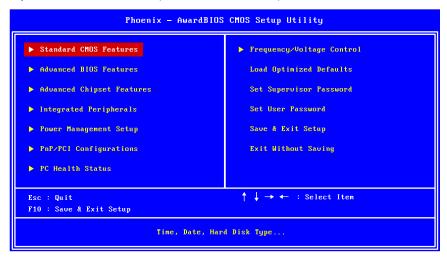
Getting Help

The BIOS setup program provides a "**General Help**" screen. You can display this screen from any menu/sub-menu by pressing **F1**>. The help screen displays the keys for using and navigating the BIOS setup. Press **F2**> to exit the help screen.



Main Menu

The Main Menu contains thirteen setup functions and two exit choices. Use arrow keys to select the items and press **Enter**> to accept or enter Sub-menu.



Standard CMOS Features

Use this menu to set basic system configurations.

Advanced BIOS Features

Use this menu to set the advanced features available on your system.

Advanced Chipset Features

Use this menu to set chipset specific features and optimize system performance.

Integrated Peripherals

Use this menu to set onboard peripherals features.

Power Management Setup

Use this menu to set onboard power management functions.



PnP/PCI Configurations

Use this menu to set the PnP and PCI configurations.

PC Health Status

This menu shows the PC health status.

Frequency/Voltage Control

Use this menu to set the system frequency and voltage control.

Load Optimized Defaults

Use this menu option to load BIOS default settings for optimal and high performance system operations.

Set Supervisor Password

Use this menu option to set the BIOS supervisor password.

Set User Password

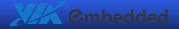
Use this menu option to set the BIOS user password.

Save & Exit Setup

Save BIOS setting changes and exit setup.

Exit Without Saving

Discard all BIOS setting changes and exit setup.



Standard CMOS Features

Date (mm:dd:yy)	Thu, Jul 1 2008	Item Help
Time (hh:mm:ss) IDE Channel 0 Haster IDE Channel 0 Slave IDE Channel 1 Haster IDE Channel 1 Slave Halt On	3 : 17 : 8 [None] [None] [None] [None] [None]	Menu Level ▶ Change the day, month, yea and century
Base Memory Extended Memory Total Memory	640K 456784K 457728K	

Date

The date format is [Day, Month Date, Year]

Time

The time format is [Hour: Minute: Second]

Halt On

Set the system's response to specific boot errors. Below is a table that details the possible settings.

Settings	Description
All Errors	System halts when any error is detected
No Errors	System does not halt for any error
All, But Keyboard	System halts for all non-key errors



IDE Channels

IDE HDD Auto-Detection	[Press Enter]	Item Help
IDE Channel 0 Master	[Auto]	Menu Level ▶
Access Mode	[Auto]	To auto-detect the
Capacity	0 MB	HDD's size, head on this channel
Cylinder		
Head		
Precomp		
Landing Zone		
Sector		
PIO Mode	[Auto]	
Ultra DMA Mode	[Auto]	

The specifications of your drive must match with the drive table. The hard disk will not work properly if you enter incorrect information in this category. Select "**Auto**" whenever possible. If you select "**Manual**", make sure the information is from your hard disk vendor or system manufacturer.

Below is a table that details required hard drive information when using the "Manual" mode.

Settings	Description
IDE Channel	The name of this match the name of the menu. Settings:
	[None, Auto, Manual]
Access Mode	Settings: [CHS, LBA, Large, Auto]
Capacity	Formatted size of the storage device
Cylinder	Number of cylinders
Head	Number of heads
Precomp	Write precompensation
Landing Zone	Cylinder location of the landing zone
Sector	Number of sectors
Primary PIO	Settings: [Auto, Mode 1, Mode 2, Mode 3, Mode 4]
Primary UDMA	Settings: [Disabled, Auto]



Advanced BIOS Features

CPU Feature	[Press Enter]	Item Help
▶ Hard Disk Boot Priority	[Press Enter]	
Virus Warning CPU L1 & L2 Cache	[Disabled] [Enabled]	Menu Level ▶
CPU L2 Cache ECC Checking		
Quick Power On Self Test		
First Boot Device	[USB-HDD]	
Second Boot Device	[CDROM]	
	[Hard Disk]	
	[Enabled]	
	[On]	
•	[Disabled]	
Typematic Rate (Chars/Sec)		
	250	
Security Option	[Setup]	
MPS Version Countrol For OS		
	[Disabled]	
Summary Screen Show	[Disabled]	

Virus Warning

Allows you to choose the VIRUS warning feature for IDE Hard Disk boot sector protection.

Settings	Description
Enabled	Turns on hard disk boot sector virus protection
Disabled	Turns off hard disk boot sector virus protection



Note:

If this function is enabled and someone attempt to write data into this area, BIOS will show a warning message on the screen and alarm beep.

CPU L1 & L2 Cache



CPU L2 Cache ECC Checking

This feature facilitates error detection/correction when data passes through Level 2 cache.

Settings: [Disabled, Enabled]

Quick Power On Self-Test

Shortens Power On Self-Test (POST) cycle to enable shorter boot up time.

Settings	Description
Disabled	Standard Power On Self Test (POST)
Enabled	Shorten Power On Self Test (POST) cycle and boot up time

First/Second/Third Boot Device

Set the boot device sequence as BIOS attempts to load the disk operating system.

Settings	Description
Removable	Boot from external drive
Hard Disk	Boot from the HDD
CDROM	Boot from CDROM
USB-CDROM	Boot from USB CDROM
Network	Boot from network drive
Disabled	Disable the boot device sequence

Boot Other Device

Enables the system to boot from alternate devices if the system fails to boot from the "First/Second/Third Boot Device" lists.

Settings	Description
Disabled	No alternate boot device allowed
Enabled	Enable alternate boot device

Boot Up NumLock Status

Set the NumLock status when the system is powered on.

Settings	Description
Off	Forces keypad to behave as arrow keys
On	Forces keypad to behave as 10-key

Typematic Rate Setting

 ${\bf Enable\ ``Type matic\ Rate''\ function.}$



Typematic Rate (Chars/Sec)

This item sets the rate (characters/second) at which the system retrieves a signal from a depressed key.

Settings: [6, 8, 10, 12, 15, 20, 24, 30]

Typematic Delay (Msec)

This item sets the delay between, when the key was first pressed and when the system begins to repeat the signal from the depressed key.

Settings: [250, 500, 750, 1000]

Security Option

Selects whether the password is required every time the System boots, or only when you enter Setup.

Settings	Description
Setup	Password prompt appears only when end users try to run BIOS
	Setup
System	Password prompt appears every time when the computer is
	powered on and when end users try to run BIOS Setup

MPS Version Control for OS

Settings: [1.1, 1.4]

Display Full Screen Logo

Show full screen logo during BIOS boot up process.

Settings: [Disabled, Enabled]

Summary Screen Show

Show summary screen.



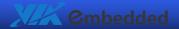
CPU Feature



Thermal Management

This item sets CPU's thermal control rule to protect CPU from overheat.

Settings	Description
Thermal Monitor 1	On-die throttling



Hard Disk Boot Priority



This is for setting the priority of the hard disk boot order when the "Hard Disk" option is selected in the "[First/Second/Third] Boot Device" menu item.



Advanced Chipset Features

Display Card Priority ▶ AGP & P2P Bridge Control	[PCI Slot]	Item Help
► CPU & PCI Bus Control Video RAM Cacheable AGP Driving Control AGP Driving Value Select Display Device Panel Type	[Press Enter] [Press Enter] [Disabled] [Auto] DA [CRT] [02]	Henu Level If there is display card on PCI slot, configure this item for BIOS to select which one to boot.



Caution:

The Advanced Chipset Features menu is used for optimizing the chipset functions. Do not change these settings unless you are familiar with the chipset.

Display Card Priority

Settings: [PCI Slot, AGP]

Video RAM Cacheable

Settings: [Disabled, Enabled]

AGP Driving Control

Settings: [Auto, Manual]



AGP Driving Value

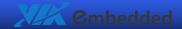
This option can only be altered if "AGP Driving Control" is set to "Manual". Input is expected as hexidecimal

Select Display Device

Settings: [CRT, LCD, CRT&LCD]

Panel Type

Settings	Description
00	640 x 480
01	800 x 600
02	1024 x 768
03	1280 x 768
04	1280 x 1024



AGP & P2P Bridge Control

AGP Aperture Size	[128M]	I	tem Help
AGP3.0 Mode	[8X]		
AGP Fast Write	[Auto]	Menu Level	
AGP 3.0 Calibration Cycle VGA Share Memory Size	[Enabled] [64M]		
Direct Frame Buffer	[Enabled]		
Outport Port	[D10]		
Dithering	[Enabled]		

AGP Aperture Size

This setting controls how much memory space can be allocated to AGP for video purposes. The aperture is a portion of the PCI memory address range dedicated to graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation.

Settings: [32MB, 64MB, 128MB, 256MB, 512MB, 1GB]

AGP3.0 Mode

This mainboard supports the AGP 8x interface. When the AGP 8x video card is used, it can transfer video data at 2133MB/s. AGP 8x is backward compatible, leave the default 4x mode on. AGP 4x mode can be detected automatically once you plug in the AGP 4x card.

Settings: [8x, 4x]

AGP Fast Write



AGP 3.0 Calibration Cycle

Settings: [Disabled, Enabled]

VGA Share Memory Size

This setting allows you to select the amount of system memory that is allocated to the integrated graphics processor.

Settings: [Disabled, 16M, 32M, 64M]

Direct Frame Buffer

Settings: [Disabled, Enabled]

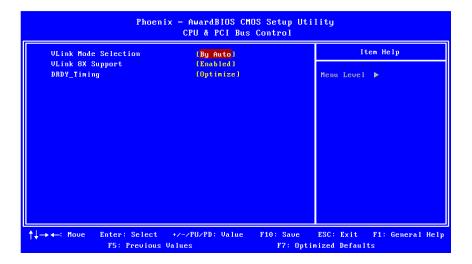
Outport Port

Settings: [DI0, DI1]

Dithering



CPU & PCI Bus Control



VLink Mode Selection

Settings: [By Auto, Mode 0, Mode 1]

VLink 8X Support

Settings: [Disabled, Enabled]

DRDY_Timing

Settings: [Slowest, Default, Optimize]



Integrated Peripherals

▶ SuperIO Device	[Press Enter]	Item Help
Onboard IDE Channel 1	[Enabled]	
Onboard IDE Channel 2	[Enabled]	Menu Level ▶
IDE Prefetch Mode	[Enabled]	
IDE HDD Block Mode	[Disabled]	
▶ VIA OnChip IDE Device	[Press Enter]	
SATA Controller	[Enabled]	
SATA Controller Mode	[IDE]	
Azalia HDA Controller	[Auto]	
LAN Controller	[Enabled]	
OnChip LAN Boot ROM		
Giga LAN Boot ROM	[Disabled]	
WatchDog Support	[Disabled]	
WatchDog Timer Select	Second	
WatchDog Count value		
▶ USB Device Setting	[Press Enter]	

Onboard IDE Channel 1 and 2

The integrated peripheral controller contains an IDE interface with support for two IDE channels.

Settings: [Disabled, Enabled]

IDE Prefetch Mode

This allows your hard disk controller to use the fast block mode to transfer data to and from the hard disk drive. Block mode is also called block transfer, multiple commands or multiple sector read/write.

Settings: [Disabled, Enabled]

IDE HDD Block Mode

Automatic detection of the optimal number of block read/writes per sector the drive can support.



SATA Controller

Settings: [Disabled, Enabled]

SATA Controller Mode

Controls the features of the Serial ATA controller within the South Bridge. Serial ATA is the latest generation of the ATA interface. Serial ATA hard drives deliver transfer speeds of up to 300MB/sec.

Settings	Description
IDE	Supports two PATA hard disk drives. Disables RAID and AHCI
	function.
RAID	Only SATA supports RAID and AHCI function
AHCI	Enable the AHCI function such as Native Command Queuing and
	Hot Plug function

Azalia HDA Controller

Settings: [Auto, Disabled]

LAN Controller

Settings: [Enabled, Disabled]

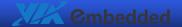
OnChip LAN Boot ROM

Decide whether to invoke the boot ROM of the onboard LAN chip.

Settings: [Enabled, Disabled]

Giga LAN Boot ROM

Decide whether to invoke the boot ROM of the onboard GigaLAN chip. Settings: [Enabled, Disabled]



WatchDog Support

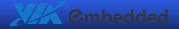
Settings: [Enabled, Disabled]

Watch Dog Timer Select

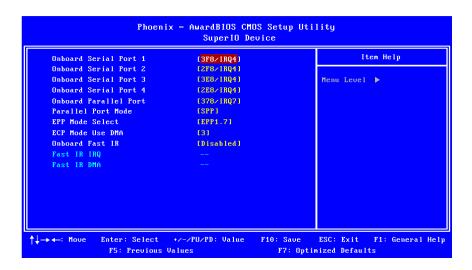
This option can only be altered if "WatchDog Support" is enabled. Settings: [Minute, Second]

Watch Dog Count Value

This option can only be altered if "WatchDog Support" is enabled. Settings: [any integer from 0 to 255]



SuperIO Device



Onboard Serial Port 1/2/3/4

Set the base I/O port address and IRQ for the onboard serial port A / serial port B. Selecting *Auto* allows BIOS to automatically determine the correct base I/O port address. Settings:

Port			Settin	igs		
1	Disabled	3F8/IRQ4	2F8/IRQ3	3E8/IRQ4	2E8/IRQ3	Auto
2	Disabled	3F8/IRQ4	2F8/IRQ3	3E8/IRQ4	2E8/IRQ3	Auto
3	Disabled	3F8/IRQ5	2F8/IRQ10	3E8/IRQ5	2E8/IRQ10	
4	Disabled	3F8/IRQ5	2F8/IRQ10	3E8/IRQ5	2E8/IRQ10	

Onboard Parallel Port

This specifies the I/O port address and IRQ of the onboard parallel port. Settings: [Disabled, 378/IRQ7, 278/IRQ5, 3BC/IRQ7]



Parallel Port Mode

Set the parallel port mode. To operate the onboard parallel port as Standard Parallel Port, choose SPP. To operate the onboard parallel port in the EPP mode, choose EPP. By choosing ECP, the onboard parallel port will operate in ECP mode. Choosing ECP + EPP will allow the onboard parallel port to support both the ECP and EPP modes simultaneously.

Settings: [SPP, EPP, ECP, ECP + EPP]

EPP Mode Select

EPP (Enhanced Parallel Port) comes in two modes: 1.9 and 1.7. EPP 1.9 is the newer version of the protocol and is backwards compatible with most EPP devices. If your EPP device does not work with the EPP 1.9 setting, try changing the setting to EPP 1.7.

Settings: [EPP 1.9, EPP 1.7]

ECP Mode Use DMA

ECP (Extended Capabilities Port) has two DMA channels that it can use. The default channel is 3. However, some expansion cards may use channel 3 as well. To solve this conflict, change the ECP channel to 1. Select a DMA channel for the port.

Settings: [1, 3]

Onboard Fast IR

Enables or disables the infrared port. Settings: [Enabled and Disabled]

Fast IR IRQ

Set this field to reserve an IRQ for the Fast IR port. This field is only available if Onboard Fast IR is enabled.

Settings: [3, 4]

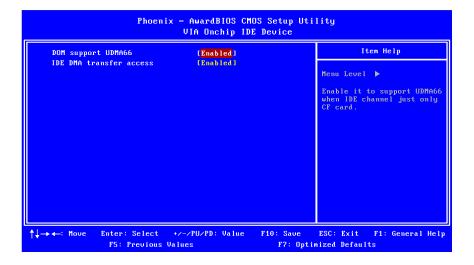
Fast IR DMA

Set this field to choose the DMA channel. This field is only available if Onboard Fast IR is enabled.

Settings: [6, 5]



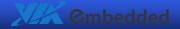
VIA OnChip IDE Device



DOM support UDMA66

Settings: [Disabled, Enabled]

IDE DMA Transfer Access



USB Device Setting

Phoeni	x - AwardBIOS CMO USB Device Se		lity
USB 1.0 Controller USB 2.0 Controller USB Operation Mode USB Keyboard Function USB Mouse Function USB Storage Function	[Enabled] [Enabled] [High Speed] [Enabled] [Enabled] [Enabled]		Item Help Henu Level ▶ [Enabled] or [Disabled] Universal Host Controller Interface for Universal Serial Bus.
↑↓→←: Move Enter: Select F5: Previous	+/-/PU/PD: Value Jalues	F10: Save F7: Optim	ESC: Exit F1: General Help nized Defaults

USB 1.0 Controller

Enable or disable Universal Host Controller Interface for Universal Serial Bus. Settings: [Disabled, Enabled]

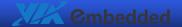
USB 2.0 Controller

Enable or disable Enhanced Host Controller Interface for Universal Serial Bus. Settings: [Disabled, Enabled]

USB Operation Mode

Auto decide USB device operation mode.

Settings	Description
Full/Low Speed	All of USB Device operated on full/low speed mode
High Speed	If USB device was high speed device, then it operated on high speed mode.



USB Keyboard Function

Enable or disable Legacy support of USB Keyboard. Settings: [Disabled, Enabled]

USB Mouse Function

Enable or disable Legacy support of USB Mouse. Settings: [Disabled, Enabled]

USB Storage Function

Enable or disable Legacy support of USB Mass Storage. Settings: [Disabled, Enabled]



Power Management Setup

ACPI Suspend Type	[S1&S3]	Item Help
HDD Power Down Power Management Timer Video Off Option Power Off by PWRBIN Run UGABIOS if S3 Resume AC Loss Auto Restart Peripherals Activities	(Disabled) (Disabled) (Suspend -> Off) (Instant-Off) (Auto) (Off) (Press Enter)	Henu Level ► This item allows you to select how the BIOS put system into power saving mode. S1(POS): System in low power mode S3(STR): All components are powered off except memory S18S3: Depends on OS to select S1 or S3

ACPI Suspend Type

Settings	Description
S1(POS)	S1/Power On Suspend (POS) is a low power state. In this state, no system context (CPU or chipset) is lost and hardware maintains all system contexts.
S3(STR)	S3/Suspend To RAM (STR) is a power-down state. In this state, power is supplied only to essential components such as main memory and wakeup-capable devices. The system context is saved to main memory, and context is restored from the memory when a "wakeup" event occurs.
S1 & S3	Depends on the OS to select S1 or S3.

HDD Power Down

Set the length of time for a period of inactivity before powering down the hard disk.

Settings: [Disable, 1 Min, 2 Min, 3 Min, 4 Min, 5 Min, 6 Min, 7 Min, 8 Min, 9 Min, 10 Min, 11 Min, 12 Min, 13 Min, 14 Min, 15 Min]



Power Management Timer

Settings: [Disable, 1 Min, 2 Min, 4 Min, 6 Min, 8 Min, 10 Min, 20 Min, 30 Min, 40 Min, 1 Hour]

Video Off Option

Select whether or not to turn off the screen when system enters power saving mode, ACPI OS such as Windows XP will override this option.

Settings	Description
Always On	Screen is always on even when system enters power saving mode
Suspend -> Off	Screen is turned off when system enters power saving mode

Power Off by PWRBTN

Settings	Description
Delay 4 Sec	System is turned off if power button is pressed for more than four
	seconds.
Instant-Off	Power button functions as a normal power-on/-off button.

Run VGABIOS if S3 Resume

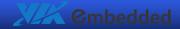
Select whether to run VGA BIOS if resuming from S3 state. This is only necessary for older VGA drivers.

Settings: [Auto, Yes, No]

AC Loss Auto Restart

The field defines how the system will respond after an AC power loss during system operation.

Settings	Description
Off	Keeps the system in an off state until the power button is pressed
On	Restarts the system when the power is back
Former-Sts	Former-Sts



Peripherals Activities



PS2KB Wakeup Select

This feature has two settings: Hot Key and Password. To select the Password option, press <**Page Up>** or <**Page Down>**. To set the password, enter up to eight digits and press <**Enter>**.

Settings: [Hot Key, Password]

PS2KB Wakeup Key Select

This feature is only available when "Hot Key" is chosen in "PS2KB Wakeup Select". Settings: [Ctrl+F1, Ctrl+F2, Ctrl+F3, Ctrl+F4, Ctrl+F5, Ctrl+F6, Ctrl+F7, Ctrl+F8, Ctrl+F9, Ctrl+F10, Ctrl+F11, Ctrl+F12, Power, Wake, Any Key]

PS2MS Wakeup Key Select

Settings: [Any Button, Left Button, Right Button]

PS2 Keyboard Power ON



PS2 Mouse Power ON

Settings: [Disabled, Enabled]

USB Resume

Settings: [Disabled, Enabled]

PowerOn by PCI Card

Enables activity detected from any PCI card to power up the system or resume from a suspended state. Such PCI cards include LAN, onboard USB ports, etc. Settings: [By OS, Enabled]

RTC Alarm Resume

Set a scheduled time and/or date to automatically power on the system.
Settings: [Disabled, Enabled]

Date (of Month)

This field can only be set if "RTC Alarm Resume" is enabled. The field specifies the date for "RTC Alarm Resume".

Resume Time (hh:mm:ss)

This field can only be set if "RTC Alarm Resume" is enabled. The field specifies the time for "RTC Alarm Resume".



PnP/PCI Configurations

Phoenix	- AwardBIOS CMOS PnP/PCI Configur	 ity
PNP OS Installed Reset Configuration Data Resources Controlled By IRQ Resources Assign IRQ For USA Assign IRQ For USB	(No) [Disabled] [Auto(ESCD)] Press Enter [Enabled] [Enabled]	Item Help Menu Level Select Yes if you are using a Plug and Play capable operating system. Select No if you need the BIOS to configure non-boot devices.
↑↓→←: Move Enter: Select F5: Previous Va		ESC: Exit F1: General Help ized Defaults



Note

This section covers some very technical items and it is strongly recommended to leave the default settings as is unless you are an experienced user.

PNP OS Installed

Settings	Description
No	BIOS will initialize all the PnP cards
Yes	BIOS will only initialize the PnP cards used for booting (VGA,
	IDE, SCSI). The rest of the cards will be initialized by the PnP
	operating system



Reset Configuration Data

Settings	Description
Disabled	Default setting
Enabled	Resets the ESCD (Extended System Configuration Data) after exiting BIOS Setup if a newly installed PCI card or the system
	configuration prevents the operating system from loading

Resources Controlled By

Enables the BIOS to automatically configure all the Plug-and-Play compatible devices.

Settings	Description
Auto(ESCD)	BIOS will automatically assign IRQ, DMA and memory base address fields
Manual	Unlocks "IRQ Resources" for manual configuration

Assign IRQ for VGA

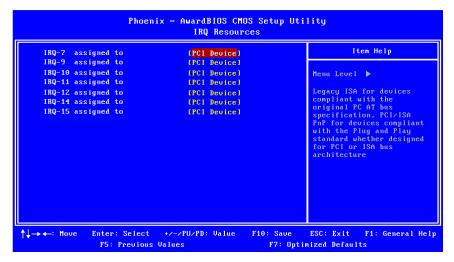
Assign IRQ for VGA devices. Settings: [Disabled, Enabled]

Assign IRQ for USB

Assign IRQ for USB devices. Settings: [Disabled, Enabled]



IRQ Resources

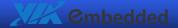


IRQ Resources list IRQ 7/9/10/11/12/14/15 for users to set each IRQ a type depending on the type of device using the IRQ. Settings:

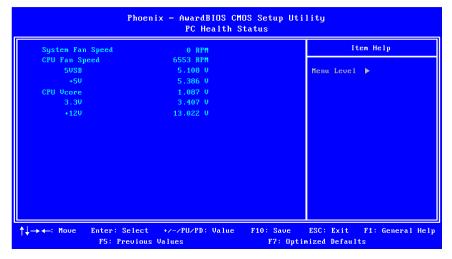
PCI Device For Plug-and-Play compatible devices designed for PCI bus

architecture

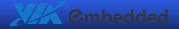
Reserved The IRQ will be reserved for further requests



PC Health Status



The PC Health Status displays the current status of all of the monitored hardware devices/components such as CPU voltages, temperatures and fan speeds.



Frequency/Voltage Control

DRAM Clock	[By SPD]	Item Help
DRAM Timing	[Auto By SPD]	
SDRAM CAS Latency [DDR/DDR2]	2.5 / 4	Menu Level
Bank Interleave	Disabled	
Precharge to Active(Trp)	41	
Active to Precharge(Tras)	07T	
Active to CMD(Trcd)	41	
REF to ACT/REF (Trfc)	25 T	
ACT(0) to ACT(1) (TRRD)	3 T	
Read to Precharge (Trtp)	[21]	
Write to Read CMD (Twtr)	[11/21]	
Write Recovery Time (Twr)	[41]	
RDSAIT mode	[Auto]	
RDSAIT selection	03	
Auto Detect PCI Clk	[Enabled]	
Spread Spectrum	[0.20×1	

DRAM Clock

The chipset supports synchronous and asynchronous mode between host clock and DRAM clock frequency.

Settings: [By SPD, 200 MHz, 266 MHz]

DRAM Timing

The value in this field depends on the memory modules installed in your system. Changing the value from the factory setting is not recommended unless you install new memory that has a different performance rating than the original modules. Settings: [Manual, By SPD]



SDRAM CAS Latency

This item adjusts the speed it takes for the memory module to complete a command. Generally, a lower setting will improve the performance of your system. However, if your system becomes less stable, you should change it to a higher setting. This field is only available when "DRAM Timing" is set to "Manual". Settings: [1.5/2, 2/3, 2.5/4, 3/5]

Bank Interleave

Set the interleave mode of the SDRAM interface. Interleaving allows banks of SDRAM to alternate their refresh and access cycles. One bank will undergo its refresh cycle while another is being accessed. This improves performance of the SDRAM by masking the refresh time of each bank. This field is only available when "DRAM Timing" is set to "Manual".

Settings: [Disabled, 2 Bank, 4 Bank, 8 Bank]

Precharge to Active (Trp)

This field controls the length of time it takes to precharge a row in the memory module before the row becomes active. Longer values are safer but may not offer the best performance. This field is only available when "DRAM Timing" is set to "Manual".

Settings: [2T - 5T]

Active to Precharge (Tras)

This field controls the length of time it a row stays active before precharging. Longer values are safer but may not offer the best performance. This field is only available when "DRAM Timing" is set to "Manual".

Settings: [5T - 20T]

Active to CMD (Trcd)

This field is only available when "DRAM Timing" is set to "Manual".

Settings: [2T - 5T]



REF to ACT/REF (Trfc)

This field is only available when "DRAM Timing" is set to "Manual".

Settings: [08T - 71T]

ACT(0) to ACT(1) (TRRD)

This field is only available when "DRAM Timing" is set to "Manual".

Settings: [2T - 5T]

Read to Precharge (Trtp)

Settings: [2T, 3T]

Write to Read CMD (Twtr)

Settings: [1T/2T, 2T/3T]

Write Recovery Time (Twr)

Settings: [2T - 5T]

RSAIT Mode

Settings: [Auto, Manual]

RSAIT Selection

This field is only available when "RSAIT Mode" is set to "Manual". Input should be a hexadecimal number.

Auto Detect PCI Clk

Settings: [Enabled, Disabled]

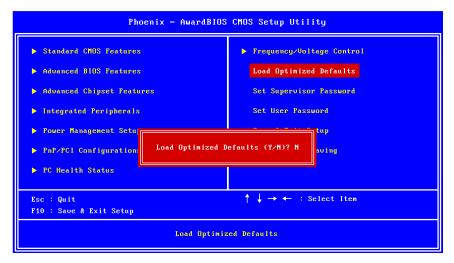
Spread Spectrum

When the mainboard's clock generator pulses, the extreme values (spikes) of the pulses create EMI (Electromagnetic Interference). The Spread Spectrum function reduces the EMI generated by modulating the pulses so that the spikes of the pulses are reduced to flatter curves.

Settings: [Disabled, 0.20%, 0.25%, 0.35%]



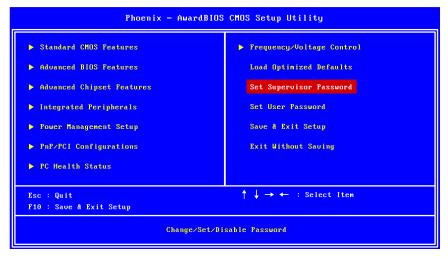
Load Optimized Defaults



This option is for restoring all the default optimized BIOS settings. The default optimized values are set by the mainboard manufacturer to provide a stable system with optimized performance. Entering "Y" and press < Enter> to load the default optimized BIOS values. Entering "N" will cancel the load optimized defaults request.



Set Supervisor/User Password



This option is for setting a password for entering BIOS Setup. When a password has been set, a password prompt will be displayed whenever BIOS Setup is run. This prevents an unauthorized person from changing any part of your system configuration.

There are two types of passwords you can set. A supervisor password and a user password. When a supervisor password is used, the BIOS Setup program can be accessed and the BIOS settings can be changed. When a user password is used, the BIOS Setup program can be accessed but the BIOS settings cannot be changed.

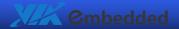
To set the password, type the password (up to eight characters in length) and press **<Enter>**. The password typed now will clear any previously set password from CMOS memory. The new password will need to be reentered to be confirmed. To cancel the process press **<Esc>**.

To disable the password, press **<Enter>** when prompted to enter a new password. A message will show up to confirm disabling the password. To cancel the process press **<Esc>**.

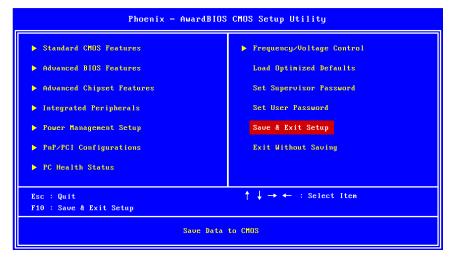


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Additionally, when a password is enabled, the BIOS can be set to request the password each time the system is booted. This would prevent unauthorized use of the system. See "Security Option" in the "Advanced BIOS Features" section for more details.



Save & Exit Setup



Entering "Y" saves any changes made, and exits the program.

Entering "N" will cancel the exit request.

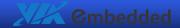


Exit Without Saving



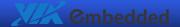
Entering "Y' discards any changes made, and exits the program.

Entering "N" will cancel the exit request.



4:

Driver Installation



Microsoft Driver Support

The VIA VB7007 mainboard is compatible with Microsoft operating systems. The latest Windows drivers can be downloaded from the VEPD website at www.viaembedded.com.

For embedded operating systems, the related drivers can be found in the VIA Embedded website at www.viaembedded.com.



Linux Driver Support

The VIA VB7007 mainboard is highly compatible with many Linux distributions. Support and drivers are provided through various methods including:

- 1. Drivers provided by VIA
- 2. Using a driver built into a distribution package
- 3. Visiting www.viaembedded.com for the latest updated drivers
- 4. Installing a third party driver (such as the ALSA driver from the Advanced Linux Sound Architecture project for integrated audio)

For OEM clients and system integrators developing a product for long term production, other code and resources may also be made available. Contact VEPD to submit a request



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