

CPU RT	BF0	BF1	BF2
X1.5/X3.5	0	0	X
X2.0	S	0	X
X2.5	S	S	X
X3.0	0	S	X
X4.0	S	0	S
X4.5	S	S	S
X5.0	0	S	S
X5.5	0	0	S

SD	FS2	FS1	FS0	CPU	SDRAM	PCI
0	S	S	S	66.8	66.8	33.4
0	S	S	0	75	75	30
0	S	0	S	83.3	83.3	33.3
0	S	0	0	95	95	31.7
0	0	S	S	100	100	33.3
0	0	S	0	112	112	37.3
0	0	0	S	124	124	41.3
0	0	0	0	133.3	133.3	33.3

JP2

SD	FS2	FS1	FS0	CPU	SDRAM	PCI
S	S	S	S	66.8	89	33.4
S	S	S	0	83.3	66.8	33.4
S	S	0	S	95	76	31.7
S	S	0	0	100	66.6	33.3
S	0	S	S	100	75	33.3
S	0	S	0	112	84	37.3
S	0	0	S	124	93	41.3
S	0	0	0	103	103	34.3

JPS	VID4	VID3	VID2	VID1	VID0
1.8V	S	S	0	S	0
2.0V	S	S	S	S	0
2.2V	0	0	0	S	0
2.5V	0	0	S	0	S
2.8V	0	S	0	0	0
2.9V	0	S	0	0	S
3.1V	0	S	0	S	S
3.2V	0	S	S	0	0
3.3V	0	S	S	0	S
3.5V	0	S	S	S	S