

## FEATURE

- SUPPORT CPU PROCESSORS 486SX/DX/DX2/DX4.P24T, AND 5X86 CPU FOR CYRIX/AMD.
- INTEGRATED DIRECT MAPPED L2 CACHE CONTROLLER FOR WRITE BACK/THROUGH.
- PCI/HOST BRIDGE COMPLIANT TO PCI SPEC. V 2.0
- STAGGERED CAS BEFORE RAS HIDDEN DRAM REFRESH.
- HIGH PERFORMANCE ARBITRATION BETWEEN CPU/PCI/ISA BUS MASTERS, DMA CHANNELS AND REFRESH REQUESTS.
- PROVIDES GREEN PC POWER MANAGEMENT : ON, DOZE, STANDBY, INACTIVE AND OFF.
- BUILT-IN TWO-CHANNEL MASTER/SLAVE MODE IDE CONTROLLER :  
2CHANNEL-4 IDE DRIVERS
- A USER FRIENDLY SUPER AT I/O CHIP 8663 INCLUDES : \*  
SIX LOGICAL DEVICES  
HIGH PERFORMANCE 1.44MB FLOPPY DISK CONTROLLER  
STANDARD AND ENHANCED PARALLEL PORT--SPP, EPP, ECP.  
TWO SLECTABLE IRQS BY TWO 16C550.  
ADDRESS DECODING AND STROBE GENERATOR FOR GAME PORT.

**\*\* SPECIFICATION**

MICROPROCESSOR : INTEL DX, DX2, DX4, P24D, P24T  
 : AMD DX, DX2, DX4, P133, P166.  
 CYRIX M6, M7, M1-SC 5X86  
 UMC U5SX

CHIPSET : UMC PCI 8881 CPU TO PCI BRIDGE AND MEMORY/CACHE  
 UMC PCI 8886 PCI TO ISA BRIDGE CONTROLLER  
 UMC ISA 8663 ENHANCED SUPER AT I/O  
 UMC TTL 8667 I/O TTL INTEGRATION

LAYOUT : 4 PCS AT SLOT, 3 PCI SLOT, 2 PCS OF SIMM SOCKET

PCE SIZE : 22 X 18 CM

I/O ON BOARD : DUAL CHANNEL PCI IDE CONTROLLER - 4 DEVICES  
 SINGLE CHANNEL FLOPPY CONTROLLER FOR 2 DRIVERS  
 16550 SERIAL PORT FUNCTION  
 ONE GAME PORT CONNECTOR

**JUMPER SETTING**  
**A. INTEL CPU**

	DX2-66	DX4-100	
J12	OPEN	OPEN	
J13	1-2, 3-4	1-2, 3-4	
J20	OPEN	OPEN	
J14	OPEN	OPEN	
J18	OPEN	OPEN	
J23	OPEN	OPEN	
J17	OPEN	OPEN	
J15	3-4	3-4	
J16	1-2	1-2	
J24	OPEN	OPEN	
J25	OPEN	OPEN	
J26	OPEN	OPEN	
J19	2-3	2-3	
JP21	OPEN	OPEN	
JP27	OPEN	OPEN	
J6	1-2	1-2	
J7	1-2	1-2	
JP28	1-2	CLOSE	CLOSE
	3-4	CLOSE	CLOSE
	5-6	CLOSE	CLOSE

B. AMD CPU

V.3.3 V3.43

	DX2 66	DX2 80	DX4 100	DX4 120	586 P-133	586 P-160
J12	OPEN	OPEN	OPEN	OPEN	2-3 4-5	2-3 4-5
J13	1-2 3-4	1-2 3-4	1-2 3-4	1-2 3-4	1-2 3-4	1-2 3-4
J20	OPEN	OPEN	OPEN	OPEN	1-2	1-2
J14	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
J18	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
J23	OPEN	OPEN	OPEN	OPEN	1-2	1-2
J17	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
J15	3-4	3-4	3-4	3-4	3-4	3-4
J16	1-2	1-2	1-2	1-2	1-2	1-2
J24	OPEN	OPEN	OPEN	OPEN	CLOSE	CLOSE
J25	OPEN	OPEN	OPEN	OPEN	CLOSE	CLOSE
J26	OPEN	OPEN	OPEN	OPEN	CLOSE	CLOSE
J19	2-3	2-3	2-3	2-3	2-3	2-3
JP21	OPEN	OPEN	OPEN	OPEN	1-2	1-2
JP27	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
J6	1-2	1-2	1-2	1-2	1-2	1-2
J7	1-2	1-2	1-2	1-2	1-2	1-2
JP28	1-2	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE
	3-4	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE
	5-6	CLOSE	OPEN	CLOSE	OPEN	CLOSE

CPU  
CHECK

CLOSE

C. CYRIX CPU

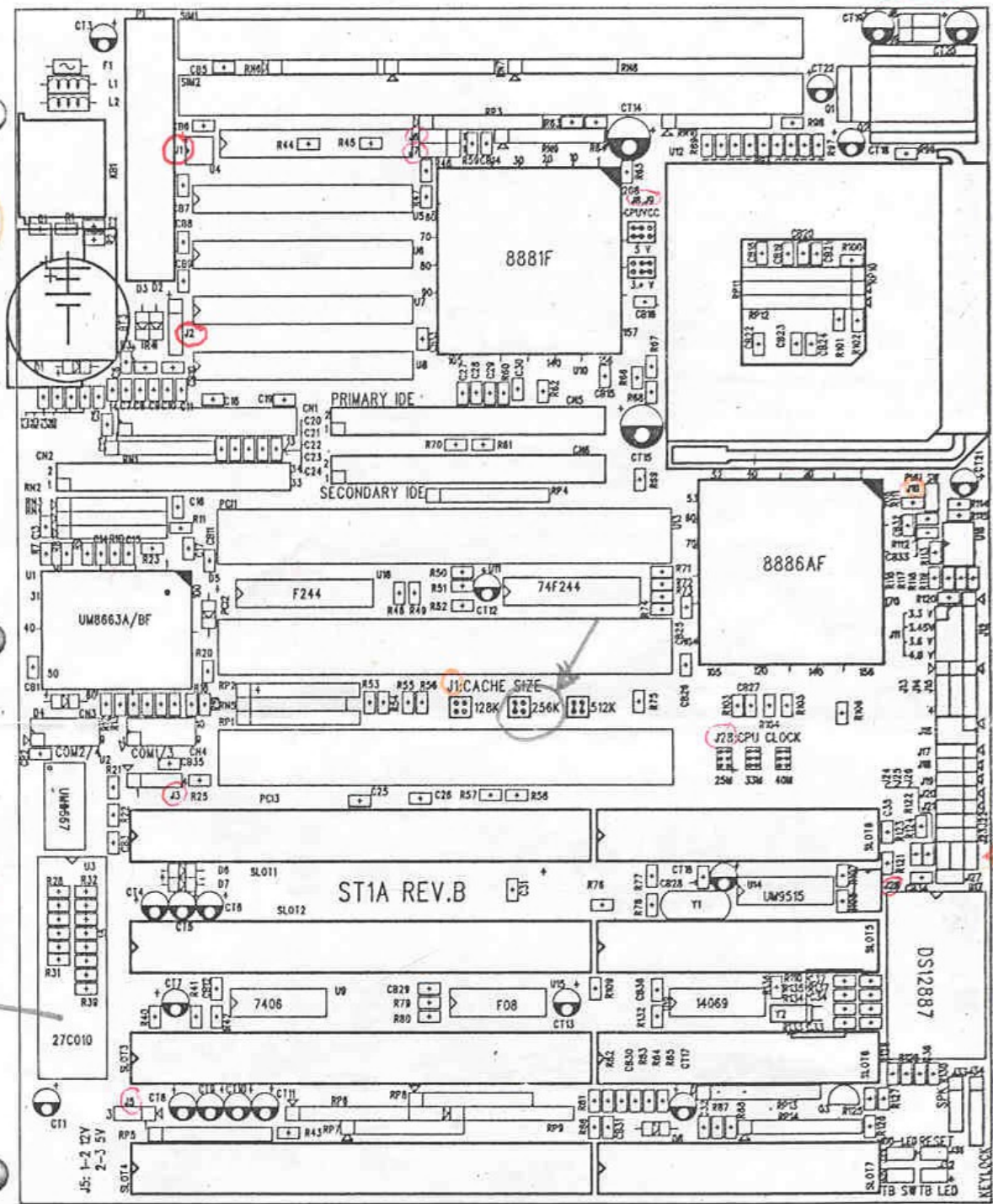
	DX2-80	5X86-100	5X86-120
J12	1-2 3-4	2-3 4-5	2-3, 4-5
J13	1-2 3-4	1-2 3-4	1-2 3-4
J20	2-3	1-2	1-2
J14	2-3	OPEN	OPEN
J18	1-2	OPEN	OPEN
J23	1-2	1-2	1-2
J17	2-3	OPEN	OPEN
J15	3-4	3-4	3-4
J16	1-2	1-2	1-2
J24	OPEN	CLOSE	CLOSE
J25	OPEN	CLOSE	CLOSE
J26	OPEN	CLOSE	CLOSE
J19	2-3	2-3	2-3
J21	OPEN	OPEN	OPEN
JP27	OPEN	CLOSE	CLOSE
J6	2-2	1-2	1-2
J7	1-2	1-2	1-2
JP28	1-2	CLOSE	CLOSE
	3-4	CLOSE	CLOSE
	5-6	OPEN	CLOSE

J8 J9

- J1 - CACHE (256K)
- J2 - 1-2
- J3 - OPEN
- J4 - ?
- J5 - X
- J22 - 2-3
- J10 - OPEN

J11 - CPU VOLTAGE

BIOS 27C010



SILK SCREEN (TOP)