



# ***Terminator***



***SMT486/5x86***  
***PCI/ISA***  
***GREEN***  
***E.IDE+FDC+2S1P***

## **User's manual**

Plug & Play  
***WIN'95*** COMPATIBLE

# Chapter 1 NEW Terminator

## 1-1 Over view

The NEW Terminator M/B provides a highly integrated solution for fully compatible high performance PC/AT platforms.

The M/B supports 486 family & 486 S-serial /5x86 GREEN microprocessors in the most cost effective and power efficient designs.

## SYSTEM FEATURES

1. SUPPORTS CPU : 5V / 4V / 3.45V / 3.3V power supply
  - INTEL 486SX / DX / DX2 / DX4 ,P24D ,P24T
  - AMD 486DX / DX2 / DX4 & ENHANCED CPU /5x86
  - Cyrix M6 / M7 / DX4 / 5x86
  - TI 486DX / DX2
  - IBM 486DX / DX2/5x86
  - SGS 486DX / DX2/5x86
  - UMC U5SX / DX / DX2
- 2.Support 72pin simm\*3(4 bank SD / DD) up to 128MB  
(Standard or EDO DRAM)
- 3.Supports 1 bank W/B cache SRAM up to 512KB
- 4.Supports plug and play interface card
- 5.Supports flash ROM
6. Buld in
  - 4\*ISA MASTER BUS
  - 3\*PCI V2.0 MASTER BUS
  - 2\*E. IDE SUPPORTS 4 HIGH CAP. DRIVE
  - 2\*SERIAL PORT HIGH SPEED 16C550 UART
  - 1\*E.EDC SUPPORTS 2.88MB FDD
  - 1\*PARALLEL PORT MULTI MODE

# Chapter 2 Installation

## 2-1 SIMM MODULE INSTALLATION

The Terminator PCI system designed to use 72 pin SIMM module , supports 4 bank SD / DD module standard or EDO DRAM .

| SIMM1 | SIMM2 | SIMM3 | TOTAL  |
|-------|-------|-------|--------|
| 1MB   | 0     | 0     | 1 MB   |
| 1MB   | 1MB   | 0     | 2 MB   |
| 1MB   | 1MB   | 1MB   | 3 MB   |
| 4 MB  | 0     | 0     | 4 MB   |
| 4 MB  | 4 MB  | 0     | 8 MB   |
| 4 MB  | 4 MB  | 4 MB  | 12 MB  |
| 8 MB  | 0     | 0     | 8 MB   |
| 8 MB  | 8 MB  | 0     | 16 MB  |
| 8 MB  | 8 MB  | 8 MB  | 24 MB  |
| 16 MB | 0 MB  | 0     | 16 MB  |
| 16 MB | 16 MB | 0     | 32 MB  |
| 16 MB | 16 MB | 16 MB | 48 MB  |
| 32 MB | 0     | 0     | 32 MB  |
| 32 MB | 32 MB | 0     | 64 MB  |
| 32 MB | 32 MB | 32 MB | 96 MB  |
| 64 MB | 0     | 0     | 64 MB  |
| 64 MB | 64 MB | 0     | 128 MB |

## 2-2 SRAM INSTALLATION

This M / B can support cache memory form 128KB up to 512KB .

| CACHE SIZE | TAG SRAM     | DATA   | JP 6 | JP 7 |
|------------|--------------|--------|------|------|
| 128KB      | 8K/16K/32K*8 | 32K*8  | 2-3  | 2-3  |
| 256KB      | 16K/32K*8    | 64K*8  | 2-3  | 1-2  |
| 512KB      | 32K*8        | 128K*8 | 1-2  | 1-2  |



## 2-3 *New* Terminator

### CPU type jumper setting

Please check CPU type & power voltage & pin 1 location before install CPU to ZIF socket.

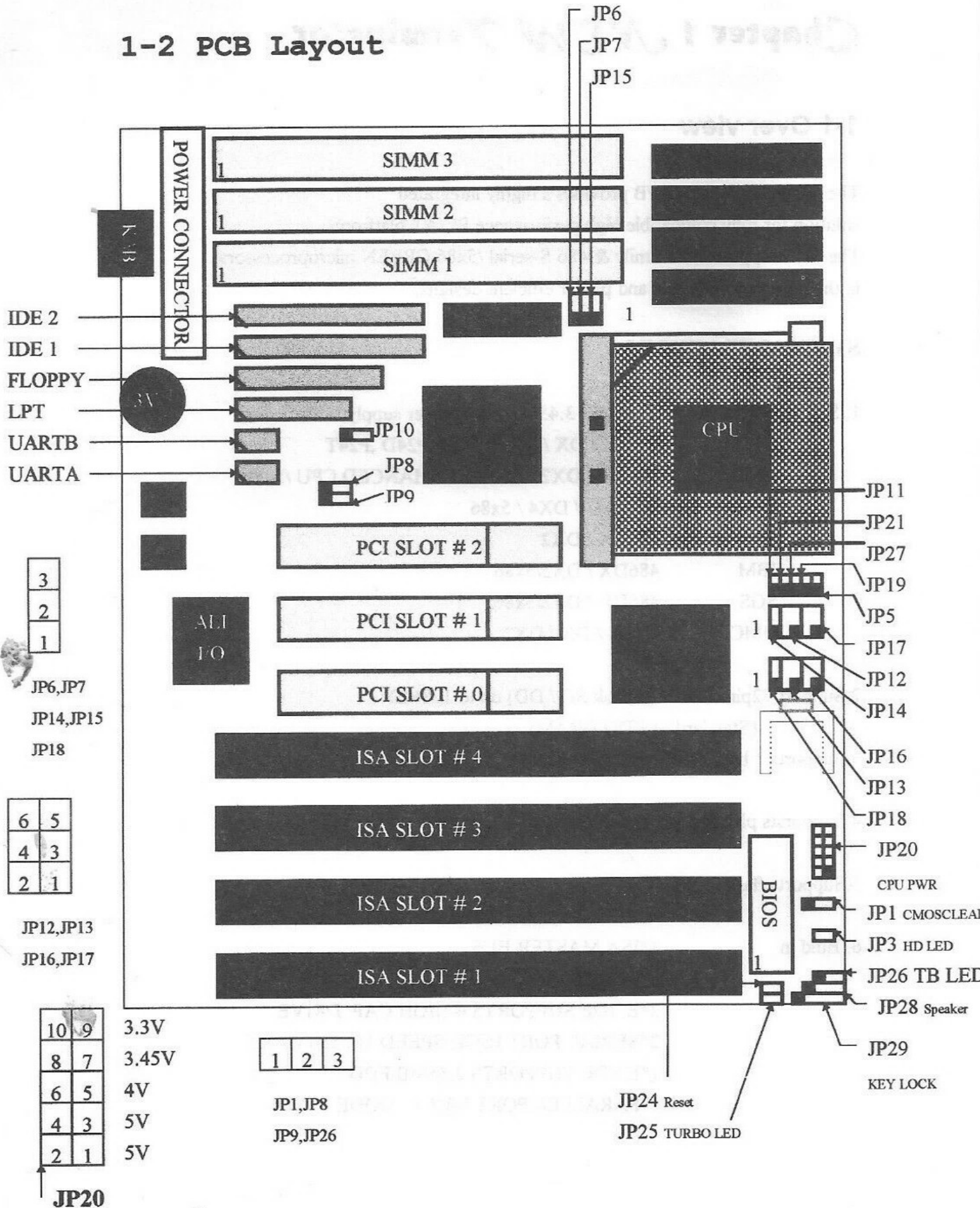
#### A. Voltage regulator JUMPER :

| VOLTAGE | JP20    | P.S.             |
|---------|---------|------------------|
| 5V      | 1-2,3-4 | DX.DX2           |
| 4V      | 5-6     | Cyrix DX2-80     |
| 3.45V   | 7-8     | DX4(Default)5x86 |
| 3.3V    | 9-10    | DX4              |

#### B. CPU speed selection : JP8, JP9, JP10,JP11

| Mhz\JUMPER                                  | JP8 | JP9 | JP10 | JP11 |
|---|-----|-----|------|------|
| 25MHz/<br>DX2-50<br>DX4-75                  | OFF | OFF | 1-2  | OFF  |
| 33MHZ/<br>DX2-66<br>DX4-100<br>5x86-133     | ON  | ON  | 1-2  | OFF  |
| 40MHz/<br>DX2-80<br>DX4-120<br>5x86-120/160 | ON  | OFF | 2-3  | ON   |
| 50MHz/<br>5x86-150                          | OFF | ON  | 2-3  | ON   |

# 1-2 PCB Layout



|   |
|---|
| 3 |
| 2 |
| 1 |

JP6,JP7  
JP14,JP15  
JP18

|   |   |
|---|---|
| 6 | 5 |
| 4 | 3 |
| 2 | 1 |

JP12,JP13  
JP16,JP17

|    |   |
|----|---|
| 10 | 9 |
| 8  | 7 |
| 6  | 5 |
| 4  | 3 |
| 2  | 1 |

3.3V  
3.45V  
4V  
5V  
5V

|   |   |   |
|---|---|---|
| 1 | 2 | 3 |
|---|---|---|

JP1,JP8  
JP9,JP26

JP24 Reset  
JP25 TURBO LED