



Manuals

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Video Cards

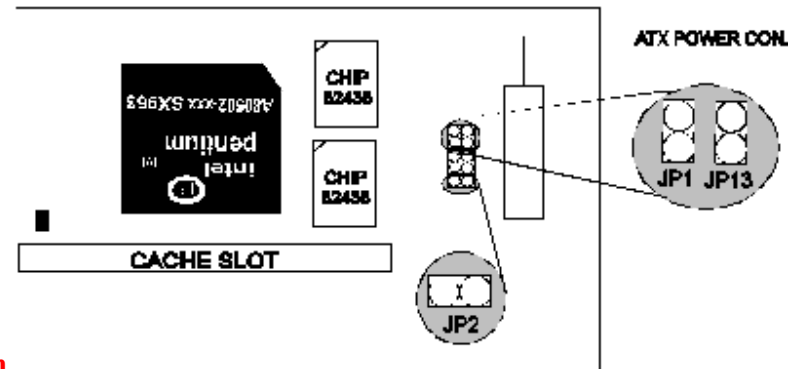


BONUS

8500TAX

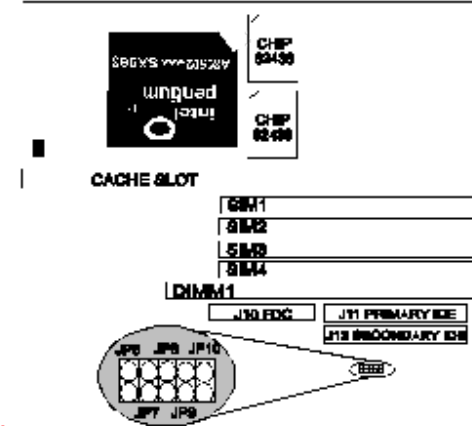
JP12 CMOS Function Selection

Open	To maintain setup and extended setup data in CMOS for normal functioning. (default)
Closed	To clear CMOS setup memory, if there has been any inappropriate operation incurring the system is failure.



JP1,JP2,JP13 CPU Core Voltage Selection

CPU TYPE	JP1	JP2	JP13	Remark	
				V I/O	Vcore
Single Voltage INTELTM P54C/CQS/CT Cyrix 6x86TM AMD K5TM K5 PR xxxAB K5 PR xxxAC K5 PR xxxAF	open	open	open	3.5V	3.5V
	closed	open	open	3.45V	3.45V
Dual Voltage INTELTM P55C/CT Cyrix 6x86LTM AMD K5TM PRxxxAH PRxxxAJ PRxxxAK	open	open	open	3.5V	2.8V
	open	closed	open	3.45V	2.8V
	open	open	closed	3.5V	2.6V
	open	closed	closed	3.45V	2.6V



JP6,JP7,JP8,JP9,JP10 CPU Clock Selection

INTEL CPU

CPU Speed	Bus Clock Multiplier	JP6	JP7	JP8	JP9	JP10
75MHz	50MHz x 1.5	closed	closed	closed	open	open
90MHz	60MHz x 1.5	closed	closed	open	open	open
100MHz	66MHz x 1.5	closed	open	closed	open	open
120MHz	60MHz x 2	closed	closed	open	open	closed
133MHz	66MHz x 2	closed	open	closed	open	closed
150MHz	60MHz x 2.5	closed	closed	open	closed	closed
166MHz	66MHz x 2.5	closed	open	closed	closed	closed
180MHz	60MHz x 3	closed	closed	open	closed	open
200MHz	66MHz x 3	closed	open	closed	closed	open

* JP6 closed & JP7 closed & JP8 closed : Bus Clock = 50MHz

* JP6 closed & JP7 closed & JP8 open : Bus Clock = 60MHz

* JP6 closed & JP7 open & JP8 closed : Bus Clock = 66MHz

* JP9 open & JP10 open : Multiplier = 1.5

* JP9 open & JP10 closed : Multiplier = 2

* JP9 closed & JP10 closed : Multiplier = 2.5

* JP9 closed & JP10 open : Multiplier = 3

Cyrix 6x86™ CPU

CPU Speed	Bus Clock Multiplier	JP6	JP7	JP8	JP9	JP10
P-120+100MHz	50MHz x 2	closed	closed	closed	open	closed
P-133+110MHz	55MHz x 2	open	closed	closed	open	closed
P-150+120MHz	60MHz x 2	closed	closed	open	open	closed
P-166+133MHz	66MHz x 2	closed	open	closed	open	closed

* JP6 closed & JP7 closed & JP8 closed : Bus Clock = 50MHz

* JP6 open & JP7 closed & JP8 closed : Bus Clock = 55MHz

* JP6 closed & JP7 closed & JP8 open : Bus Clock = 60MHz

* JP6 closed & JP7 open & JP8 closed : Bus Clock = 66MHz

* JP9 open & JP10 closed : Multiplier = 2

AMD-K5™ CPU

CPU Speed	Bus Clock Multiplier	JP6	JP7	JP8	JP9	JP10
PR-7575MHz	50MHz x 1.5	closed	closed	closed	open	open
PR-9090MHz	60MHz x 1.5	closed	closed	open	open	open
PR-100100MHz	66MHz x 1.5	closed	open	closed	open	open
PR-12090MHz	60MHz x 1.5	closed	closed	open	open	open
PR-133100MHz	66MHz x 1.5	closed	open	closed	open	open
PR-150120MHz	60MHz x 2	closed	closed	open	open	closed
PR-166133MHz	66MHz x 2	closed	open	closed	open	closed

* JP6 closed & JP7 closed & JP8 closed : Bus Clock = 50MHz

* JP6 closed & JP7 closed & JP8 open :Bus Clock = 60MHz

* JP6 closed & JP7 open & JP8 closed : Bus Clock = 66MHz

* JP9 open & JP10 open : Multiplier = 1.5

* JP9 open & JP10 closed : Multiplier = 2

Connectors J7

Pin No.	Assignment	Function
1	Speaker	Speaker Connector
2	NC	
3	Key	
4	+5 V VCC	
5	Reset Control	Reset
6	Ground	NC
7	NC	
8	Power LED (+)	
9	Key	Power LED
10	Ground	HDD LED
11	NC	
12	HDD LED (+)	
13	HDD LED (-)	
14	Key	
15	HDD (+5v)	NC
16	NC	
17	IRTX	IR
18	Ground	
19	IRRX	
20	Key	
21	+5V VCC	NC
22	NC	
23	Ground	
24	Green Control	Green Switch
25	Ground	Power On
26	Power On	

DRAM Installation

DRAM Access Time: fast page mode 70ns, EDO mode 60ns and SDRAM 15ns required.

DRAM Type:
 1) 4MB/8MB/16MB/32MB SIMM Module (72Pin for SIMM1~SIMM4)
 2) 8MB/16MB/32MB/64MB DIMM Module (168Pin for DIMM1)

Total Memory Size (MB)	Bank 0(SIMM1-SIMM2)	Bank 1(SIMM3-SIMM4)
8M	4M x 2 pcs	----
16M	8M x 2 pcs	----
32M	16M x 2 pcs	----
64M	32M x 2 pcs	----
16M	4M x 2pcs	4M x 2 pcs
24M	4M x 2pcs	8M x 2 pcs
32M	8M x 2pcs	8M x 2 pcs
40M	4M x 2pcs	16M x 2 pcs
48M	8M x 2pcs	16M x 2 pcs
64M	16M x 2pcs	16M x 2 pcs
72M	4M x 2 pcs	32M x 2 pcs
80M	8M x 2pcs	32M x 2 pcs
96M	16M x 2pcs	32M x 2 pcs
128M	32M x 2pcs	32M x 2 pcs

*Each Bank can be installed and worked individually, the mainboard provides optimal performance and free choices depending on your needs.
 *The list above for DRAM configuration is just for reference.
 *EDO Type DRAM and FAST PAGE Type DRAM can be Mixed in per bank.
 *Dual In-line Memory Module(DIMM1) is Bank1, the DRAM size configuration is the same as Bank1 & can be Mixed with EDO type or Fast Page Type on Bank0.