SBC8176

Dual Pentium[®] II/III PCI/ISA CPU Card

User's Manual

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ESD Precautions

Integrated circuits on computer boards are sensitive to static electricity. To avoid damaging chips from electrostatic discharge, observe the following precautions:

- Do not remove boards or integrated circuits from their anti-static packaging until you are ready to install them.
- Before handling a board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. This helps to discharge any static electricity on your body.
- Wear a wrist-grounding strap, available from most electronic component stores, when handling boards and components.

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iii

Unpacking

After unpacking the CPU card, check and see if the following items are included and in good condition. If any of the items is missing or damaged, notify your dealer immediately.

- SBC8176 Dual Pentium[®] II/III PCI/ISA CPU Card x 1
- User's Manual and Warranty Card x 1
- IDE Ribbon Cable x 1
- PS/2 Keyboard Adapter Cable x 1
- Floppy Ribbon Connector x 1
- One serial port ribbon cable and one parallel port attached to a mounting bracket x 1 set
- Thermistor with cables x 3
- Termination board x 1
- Diskette containing System Hardware Doctor Utility x 2

Make sure that all of the items listed above are present.

What To Do If There Is A Problem

If there are damaged or missing parts, contact your supplier and/or dealer immediately. Do not attempt to apply power to the board if there is damage to any of its components.



Table of Contents

Chapter 1 Introduction 1.3 Intelligence......4 1.4 1.5 Chapter 2 Installation 2.1 CPU Instal lation7 2.1.1 2.1.2 Memory Installation8 2.2 16MB SDRAM installed on Bank09 Chapter 3 Jumpers and Connectors 3.1 Jumpers 13 CPU Settings...... 15 3.1.1 3.1.1.1 Core Frequency to System Bus Multiplier Configuration: JP415 System Bus Freuency Setting: JP6 15 3.1.1.2 3.1.1.3 CPU Frequency Reference Table 16 3.1.2 Clear CMOS Jumper: JP8 18 AT or ATX Power Supply Selector: JP7, JP15 18 3.1.3 3.1.4 DiskOnChip[™] BIOS Expansion Address Select: JP10.. 19 RS232/422/485 (COM2) Selection: JP11, JP12, 3.1.5 SMI Selector: JP5 20 3.1.6 3.1.7 Watchdog Timer Selector: JP9 20 3.2 3.2.1 Speaker: Pins 2, 4, 6, 8 23 Power LED and Keylock: Pins 1, 3, 5, 7, 9 24 SMI/Hardware Switch: Pins 11 and 1224 Reset Switch: Pins 17 and 18......24 Hard Disk Drive LED Connector:

Table of Contents

	3.2.2	EIDE Connectors: CN7, CN8	. 25
		CN7: Primary IDE Connector	25
	2 7 2	Floppy Drive Connector: CN9	20
	3.2.3	Parallel Port Connector: CN11	. 27
	3.2.4	COM1 Serial Port: CN17	. 20
	3.2.5	COM2 Serial Port: CN16	. 20
	3.2.0	Internal Keyboard Connector: CN10 Internal Mouse	. 27
	5.2.7	Connector: CN14	. 29
	3.2.8	External ATX Power Connector: CN15	30
	3.2.9	External PS/2 Keyboard and Mouse Connector:	
		CN18	. 30
	3.2.10	CPUI Fan Power Connector: CN1	. 30
	3.2.11	CPUII Fan2 Connector: CN2	. 30
	3.2.12	USB Connector: CN13	. 31
	3.2.13	IrDA Connector: CN12	. 31
	3.2.14	Temperature Thermistor Connectors:	
		CN4, CN5, CN6	. 32
Char	nter 4	4 BIOS Configuration	
4.1	BIOS	Introduction	33
4.2	BIOS	Setup	33
4.3	Stand	lard CMOS Setup	35
4.4	BIOS	Features Setup	38
4.5	Chips	et Features Setup	42
4.6	Power	Management Setup	45
4.7	PNP/P	PCI Configuration	48
4.8	Load	BIOS Defaul ts	49
4.9	Load	Setup Defaul ts	50
4.10	Integ	rated Peripherals	51
4.11	Super	visor / User Password	53
4.12	IDE H	DD Auto Detection	54
4.13	Save a	& Exit Setup	55
4.14	Exit V	Vithout Saving	56
Char	ator I	E System Hardward Doctor	
		m Hardware Dector Instal Lation	57
5.1	Jaune	hing the System Hardward	57
J.Z		ning the system haruware	БQ
	υστι	^{<i>n</i>} Othilty	00

Table of Contents

vi

Appendix AWatchdog Timer
ConfigurationUsing the Watchdog Function61Appendix BI/O Port Address MapAppendix CInterrupt Request Lines
(IRQ)

Table of Contents

vii

This page does not contain any information.

viii

Chapter 1 Introduction

1.1 General Description



The **SBC8176** is a Dual Pentium[®] II/III industrial CPU card based on Intel's 440BX chipset and is fully designed for harsh industrial environment. It features two Slot 1 processor connectors that are compatible with Pentium[®] II/III processors. This card accommodates up to 512MB of unbuffered SDRAMs or 1GB of buffer SDRAM configurations.

The **SBC8176** comes with Winbond's W83781D hardware monitoring device that monitors system/CPU temperatures, system voltages, and CPU/chassis fan speeds, preventing system crashes by warning the user of adverse conditions. The power management feature provides power savings by slowing down the CPU clock, turning OFF the monitor screen and stopping the HDD spindle motor.

Introduction

1.2 Features

- CPU Speed 266~500MHz, Intel Pentium[®] II/III
- Bus Speed 66MHz/100MHz
- Intel 440BX AGPset
- Up to 512MB of unbuffered SDRAMs or 1GB of buffered SDRAM system memory, ECC function supported
- 16-level software programmable watchdog timer with temperature and time intervals from 0.5 to 1000 seconds
- High speed bi-directional SPP/ECP/EPP parallel port
- Hardware Monitoring
- Win 95/98 shut-off

1.3 Specifications

- Processor Socket: Dual Slot 1 connector
- Processor:
 - Intel Pentium[®] II 333MHz (66MHz / Deschutes)
 - Intel Pentium[®] II 300/350/400/450/500 MHz (100MHz / Deschutes)
 - Intel Pentium[®] III 450/500/550/600 MHz (Katmai)
 - Intel Pentium[®] III 600E/650E/700E MHz (Coppermine)
- Chipset: Intel 440BX AGPset with PCI EIDE
- Secondary Cache: CPU integrated
- Memory Sockets:

2

- Four 168-pin DIMM sockets
- Memory types: SDRAM (Synchronous DRAM)
- NOTE: Only SDRAM modules that support SPD (Serial Presence Detect) should be used. Use PC100 modules when running 100MHz CPU bus speed and use PC66/PC100 modules when running 66MHz CPU bus speed.

Introduction

- **BIOS**: Award BIOS, PnP support
 - FLASH EEPROM (256KB) for BIOS update
 - ISA Plug and Play (PnP) extension
 - Power management
- Multi I/O: Winbond W83977TF
- Parallel Port: One high-speed parallel port, SPP/EPP/ECP mode
- Serial Port: Two 16550 UART compatible ports with COM1 as RS232 and COM2 jumper configurable as RS232/422/485
- Enhanced IDE:
 - Two EIDE interfaces for up to four devices
 - Supports PIO Mode 3/4, DMA Mode 0~2, and Ultra DMA/33 IDE Hard Disk and ATAPI CD-ROM
- FDD Interface: Two floppy drives (360KB, 720KB, 1.2MB, 1.44MB, 2.88MB, LS-120)
- USB Interface: Two USB box-header connectors, compliant with USB Specification Rev. 1.0
- **DiskOnChip™**: The M-Systems flask disk supports system boot and storage capacity from 2MB to 72MB.
- **Real Time Clock**: DS12887A or STM48T86
- Watchdog Timer: Generates a system reset 16-level software programmable time and temperature interval from 0.5 to 1000 seconds
- Green Function: Power management via BIOS, activated through mouse/keyboard movement
- Keyboard and Mouse Connectors:
 - One mini-DIN connector supporting external keyboard and mouse
 - One 5-pin connector supporting internal keyboard
 - One 5-pin connector supporting internal mouse
- IrDA Interface: Box-header connector for the optional IrDA external connector

Introduction

- **PICMG Compliance**: Fully compliant to PICMG standards
- Power Consumption:
 - +5V @16A, +12V @1.5A (max.)
 (Dual Pentium[®] II 400MHz, 128MB SDRAM)
- Environmental and Mechanical:
 - Temperature: 0°C to 60°C
 - Humidity: 5% to 95%
 - Dimensions: 338.2mm x 121.9mm

1.4 Intelligence

- Windows 95 shut-off: Allows shut-off control from within Windows 95 and through an ATX power supply.
- Year 2000 Compliant BIOS: The onboard Award BIOS is Year 2000 Compliant and will pass software applications that have the tendency to invoke INT1AH function 04H such as year2000.exe utility released by NSTL.
- Temperature Monitoring and Alert (optional): Two sensor connectors on the SBC8176 monitors the CPU and system temperature. When temperature exceeds the safe heat level, this feature alerts the user through the speaker or buzzer.





1.5 Board Dimensions

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Chapter 2 Installation

This chapter provides information on how to configure the jumpers and connectors on the **SBC8176** in order to set up a workable system.

2.1 CPU Installation

The **SBC8176** Industrial CPU card supports Dual Slot 1 connector/processor sockets for Pentium[®] II/III processors. This connector secures the installation of the Pentium[®] II/III processor, ensuring the proper installation of the CPU.

Before installing the Pentium[®] II/III processor into the Slot 1 connector, ensure that the CPU fan is securely installed. After doing so, insert the processor into the Slot 1 connector. Locking mechanism on both sides of the Slot 1 connector will 'click' and secure the Pentium[®] II/III processor.

To uninstall the Pentium[®] II/III processor, simply push the locking mechanism on both sides simultaneously and remove the Pentium[®] II/III processor. No tools are needed.

- CAUTION: When installing two Pentium[®] II/III processors onboard the SBC8176, make sure that both processors have identical clock speeds. Mixed combinations are not allowed.
- WARNING: SBC8176 supports installation of a single or dual Pentium[®] II/III processor. When using only a single Pentium[®] II/III processor, install only on CPUI Slot1 connector. Do not attempt to mount the Pentium[®] II/III processor into the CPU2 Slot 2 connector!

Installation

2.1.1 Single Processor Installation

When configuring the SBC8176 for **single processor** setting, *install the processor into CPUI slot*. Refer to the illustration on Section 3.2 for the actual location of CPUI slot. After installing the CPU into the CPUI slot, *mount the termination board into CPUII slot*.

2.1.2 Dual Processor Installation

When configuring the SBC8176 for **dual processor** setting, *install the processors into CPUI and CPUII slots. Make sure that both processors* **have identical frequency speed**.

2.2 Memory Installation

The **SBC8176** Industrial CPU Card supports four 168-pin DIMM (Dual In-line Memory Module) sockets for a maximum total memory of 512MB unbuffered SDRAMs or 1GB buffer SDRAMs. The memory modules can come in sizes of 16MB, 32MB, 64MB, 128MB and 256MB (for buffer type) SDRAMs.

When populating the DIMM sockets, DIMM1,2,3 or DIMM4 bank can be populated first. Refer to the following table on how to configure the memory.

NOTE: Use SDRAM modules with PC100 specification when running 100MHz CPU bus speed. With 66MHz CPU bus speed, SDRAM modules with PC66 or PC100 specification can be used. You have to install the Pentium[®] II/III processor before installing the memory modules.

Installation

BankO	Bank1	Bank2	Bank 3	Total
16MB				16MB
16MB		8MB	8MB	32MB
16MB		16MB	16MB	48MB
16MB		32MB	32MB	80MB
16MB		64MB	64MB	144MB
16MB		128MB	128MB	272MB
16MB	8MB	8MB	8MB	40MB
16MB	8MB	16MB	16MB	56MB
16MB	8MB	32MB	32MB	88MB
16MB	8MB	64MB	64MB	152MB
16MB	8MB	128MB	128MB	280MB
16MB	16MB	8MB	8MB	48MB
16MB	16MB	16MB	16MB	64MB
16MB	16MB	32MB	32MB	96MB
16MB	16MB	64MB	64MB	160MB
16MB	16MB	128MB	128MB	288MB
16MB	32MB	8MB	8MB	64MB
16MB	32MB	16MB	16MB	80MB
16MB	32MB	32MB	32MB	112MB
16MB	32MB	64MB	64MB	176MB
16MB	32MB	128MB	128MB	304MB
16MB	64MB	8MB	8MB	96MB
16MB	64MB	16MB	16MB	112MB
16MB	64MB	32MB	32MB	144MB
16MB	64MB	64MB	64MB	208MB
16MB	64MB	128MB	128MB	336MB
16MB	128MB	8MB	8MB	160MB
16MB	128MB	16MB	16MB	176MB
16MB	128MB	32MB	32MB	208MB
16MB	128MB	64MB	64MB	272MB
16MB	128MB	128MB	128MB	400MB

Installation

BankO	Bank1	Bank2	Bank 3	Total
32MB				32MB
32MB		8MB	8MB	48MB
32MB		16MB	16MB	64MB
32MB		32MB	32MB	96MB
32MB		64MB	64MB	160MB
32MB		128MB	128MB	288MB
32MB	8MB	8MB	8MB	56MB
32MB	8MB	16MB	16MB	72MB
32MB	8MB	32MB	32MB	104MB
32MB	8MB	64MB	64MB	168MB
32MB	8MB	128MB	128MB	296MB
32MB	16MB	8MB	8MB	64MB
32MB	16MB	16MB	16MB	80MB
32MB	16MB	32MB	32MB	112MB
32MB	16MB	64MB	64MB	176MB
32MB	16MB	128MB	128MB	304MB
32MB	32MB	8MB	8MB	80MB
32MB	32MB	16MB	16MB	96MB
32MB	32MB	32MB	32MB	128MB
32MB	32MB	64MB	64MB	192MB
32MB	32MB	128MB	128MB	320MB
32MB	64MB	8MB	8MB	112MB
32MB	64MB	16MB	16MB	128MB
32MB	64MB	32MB	32MB	160MB
32MB	64MB	64MB	64MB	224MB
32MB	64MB	128MB	128MB	352MB
32MB	128MB	8MB	8MB	176MB
32MB	128MB	16MB	16MB	192MB
32MB	128MB	32MB	32MB	224MB
32MB	128MB	64MB	64MB	288MB
32MB	128MB	128MB	128MB	416MB

Installation

BankO	Bank1	Bank2	Bank 3	Total
64MB				64MB
64MB		8MB	8MB	80MB
64MB		16MB	16MB	96MB
64MB		32MB	32MB	128MB
64MB		64MB	64MB	192MB
64MB		128MB	128MB	320MB
64MB	8MB	8MB	8MB	88MB
64MB	8MB	16MB	16MB	104MB
64MB	8MB	32MB	32MB	136MB
64MB	8MB	64MB	64MB	200MB
64MB	8MB	128MB	128MB	328MB
64MB	16MB	8MB	8MB	96MB
64MB	16MB	16MB	16MB	112MB
64MB	16MB	32MB	32MB	144MB
64MB	16MB	64MB	64MB	208MB
64MB	16MB	128MB	128MB	336MB
64MB	32MB	8MB	8MB	112MB
64MB	32MB	16MB	16MB	128MB
64MB	32MB	32MB	32MB	160MB
64MB	32MB	64MB	64MB	224MB
64MB	32MB	128MB	128MB	352MB
64MB	64MB	8MB	8MB	144MB
64MB	64MB	16MB	16MB	160MB
64MB	64MB	32MB	32MB	192MB
64MB	64MB	64MB	64MB	256MB
64MB	64MB	128MB	128MB	384MB
64MB	128MB	8MB	8MB	208MB
64MB	128MB	16MB	16MB	224MB
64MB	128MB	32MB	32MB	256MB
64MB	128MB	64MB	64MB	320MB
64MB	128MB	128MB	128MB	448MB

Installation

BankO	Bank1	Bank2	Bank 3	Total
128MB				128MB
128MB		8MB	8MB	144MB
128MB		16MB	16MB	160MB
128MB		32MB	32MB	192MB
128MB		64MB	64MB	256MB
128MB		128MB	128MB	384MB
128MB	8MB	8MB	8MB	152MB
128MB	8MB	16MB	16MB	168MB
128MB	8MB	32MB	32MB	200MB
128MB	8MB	64MB	64MB	264MB
128MB	8MB	128MB	128MB	392MB
128MB	16MB	8MB	8MB	160MB
128MB	16MB	16MB	16MB	176MB
128MB	16MB	32MB	32MB	208MB
128MB	16MB	64MB	64MB	272MB
128MB	16MB	128MB	128MB	400MB
128MB	32MB	8MB	8MB	176MB
128MB	32MB	16MB	16MB	192MB
128MB	32MB	32MB	32MB	224MB
128MB	32MB	64MB	64MB	288MB
128MB	32MB	128MB	128MB	416MB
128MB	64MB	8MB	8MB	208MB
128MB	64MB	16MB	16MB	224MB
128MB	64MB	32MB	32MB	256MB
128MB	64MB	64MB	64MB	320MB
128MB	64MB	128MB	128MB	448MB
128MB	128MB	8MB	8MB	272MB
128MB	128MB	16MB	16MB	288MB
128MB	128MB	32MB	32MB	320MB
128MB	128MB	64MB	64MB	384MB
128MB	128MB	128MB	128MB	512MB

Installation

Chapter 3 Jumpers and Connectors

3.1 Jumpers

The jumpers on the **SBC8176** allow you to configure your CPU card according to the needs of your applications. If you have doubts about the best jumper configuration for your needs, contact your dealer or sales representative. The following table lists the connectors on **SBC8176** and their respective functions.

Jumper	Description
JP4, JP6	CPU Frequency Selector
JP5	SMI Selector
JP7, JP15	AT or ATX Power Supply Selector
JP8	Clear CMOS Jumper
JP9	Watchdog Timer Selector
JP10	DiskOnChip™ BIOS Expansion Address Select
JP11, JP12, JP13	RS232/422/485 (COM2) Selection

NOTE: Jumpers **JP1** and **JP2** (All open default) are for manufacturer testing use only.

Jumper JP3 is not used. Short 1-2 is the default setting of JP16.

Jumpers and Connectors





Jumpers and Connectors



3.1.1 CPU Settings

When a new CPU is to be installed, only jumpers JP4 and JP6 need to be adjusted.

3.1.1.1 Core Frequency to System Bus Multiplier Configuration: JP4

Multiplication of Core Frequency to System Bus Frequency	JP4 Setting
1/2.0	Short 1-2, 3-4, 5-6, 7-8
1/2.5	Short 1-2, 3-4, 7-8
1/3.0	Short 1-2, 5-6, 7-8
1/3.5	Short 1-2, 7-8
1/4.0	Short 3-4, 5-6, 7-8
1/4.5	Short 3-4, 7-8
1/5.0	Short 5-6, 7-8
1/5.5	Short 7-8
1/6.0	Short 1-2, 3-4, 5-6
1/6.5	Short 1-2, 3-4
1/7.0	Short 1-2, 5-6
1/7.5	Short 1-2
1/8.0	Short 3-4, 5-6

3.1.1.2 System Bus Freuency Setting: JP6

System Bus Frequency	JP6 Setting
66.6MHz	Short 1-2
100MHz	Short 2-4

Jumpers and Connectors

3.1.1.3 CPU Frequency Reference Table

The following table serves as a reference on the correct jumper settings that match a few selected CPU frequencies.

СРИ Туре	CPU Frequency	JP4 Setting	JP6 Setting
Deschutes 66MHz Host Clock CPU	5x 66.6MHz 333MHz	2 8 0 0 0 0 1 7	
	3 x 100MHz 300MHz		
PII Deschutes 100MHz Host Clock CPU	3.5 x 100MHz 350MHz	2 8 0 0 0 1 7	2 8 0 0 0 0 1 7
	4 x 100MHz 400MHz	2 8 0 0 0 1 7	2 8 0 0 0 0 1 7
	4.5 x 100MHz 450MHz	2 8 0 0 0 1 7	2 8 0 0 0 0 1 7
	5 x 100MHz 500MHz	2 8 0 0 0 1 7	2 8 0 0 0 0 1 7

Continued

Jumpers and Connectors

СРИ Туре	CPU Frequency	JP4 Setting	JP6 Setting
	4.5 x 100MHz 450MHz	2 8	
PIII Katmai 100MHz Host Clock	5.0 x 100MHz 500MHz	2 8	2 8 0 0 0 1 7
CFU	5.5 x 100MHz 550MHz	2 8	
	6.0 x 100MHz 600MHz		2 8 0 0 0 0 1 7
	6.0 x 100MHz 600E		
PIII Coppermine 100MHz Host Clock	6.5 x 100MHz 650E	2 8 0 0 0 0 0 0 0 1 7	
CPU	7.0 x 100MHz 700E	2 8 0 0 0 0 0 0 0 1 7	2 8 0 0 0 0 1 7

Jumpers and Connectors

3.1.2 Clear CMOS Jumper: JP8

This 2-pin connector allows the user to clear the CMOS RAM data.





Normal operation: Open (default)

Clear CMOS Contents: Short 1-2

3.1.3 AT or ATX Power Supply Selector: JP7, JP15

JP7	JP15	Setting	Function
2 6 1 5	■ □ □ 1 2 3	Short 2-3	ATX Power
2 6 • • • • • • • • • • • • • • • • • • •		Short 1-2	AT Power (default)

NOTE: When configured for the ATX power supply, your system must have a special backplane that supports an ATX power supply.

Jumpers and Connectors



3.1.4 DiskOnChip™ BIOS Expansion Address Select: JP10

JP10	Address	JP10	Address
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	D8000-D7FFF	3 1 — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — — —	D4000-DBFFF
$ \begin{array}{c} 3 \\ \hline \\ \hline \\ \\ \\ 4 \\ 2 \end{array} $	D0000-D3FFF (default)	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	DC000-DFFFF

3.1.5 RS232/422/485 (COM2) Selection: JP11, JP12, JP13

COM1 is fixed for RS-232 use only.

COM2 is selectable for RS232, RS-422 and RS-485. The following table describes the jumper settings for this connector.

Lumpors		COM2 Function					
Jumpers		RS-232	RS-422	RS-485			
JP	11	Short 1-2	Short 3-4	Short 5-6, 7-8			
JP	12	Short 3-5, 4-6	Short 1-3, 2-4	Short 1-3, 2-4			
JP	13	Short 3-5, 4-6	Short 1-3, 2-4	Short 1-3, 2-4			
Jumper Illustration	JP11	1 2 7	1	1			

Continued

Jumpers and Connectors

		COM2 Eunction					
Jumpers							
		RS-232	RS-422	RS-485			
Jumper	JP12						
inustration	JP13						

3.1.6 SMI Selector: JP5

20

Options	Setting	JP5
CPU SMI comes from IOAPIC	Short 1-2	
CPU SMI comes from PIIX4 (default)	Short 2-3	

3.1.7 Watchdog Timer Selector: JP9

Options	Setting	JP9	
Watchdog generates NMI	Short 1-2	1 ■ 2 □ 3 □	
Watchdog generates Reset	Short 2-3	1 ■ 2 □ 3 □	

Default: Open (Watchdog function disabled)

Jumpers and Connectors

3.2 Connectors

The connectors on the **SBC8176** allows you to connect external devices such as keyboard, floppy disk drives, hard disk drives, printers, etc. The following table lists the connectors on **SBC8176** and their respective functions.

Connectors	Label
CPUI FAN Connector	CN1
CPUII FAN Connector	CN2
Front Bezel Connector	CN3
System Temperature Connector	CN4
CPUI Temperature Connector	CN5
CPUII Temperature Connector	CN6
EIDE Connectors	CN7, CN8
Floppy Drive Connector	CN9
Internal Keyboard Connector	CN10
Parallel Port Connector	CN11
IrDA Connector	CN12
USB Connectors	CN13
Internal PS/2 Mouse Connector	CN14
External ATX Power Connector	CN15
COM2 Serial Port	CN16
COM1 Serial Port	CN17
External PS/2 Keyboard/Mouse Connector	CN18

Jumpers and Connectors





22

Jumpers and Connectors

3.2.1 Front Bezel Connector: CN3

The front bezel of the case has a control panel that provides light indication of the computer activities and switches to change the computer status. **CN3** is a 20-pin header that provides interfaces for the following functions.



Speaker: Pins 2, 4, 6, 8

This connector provides an interface to a speaker for audio tone generation. An 8-ohm speaker is recommended.

2 4 6 8	Pin #	Signal Name
	2	Speaker out
	4	Short 2-3 for
	6	Internal Buzzer
	8	+5V

Jumpers and Connectors

Power LED and Keylock: Pins 1, 3, 5, 7, 9

The power LED indicates the status of the main power switch. The keylock switch, when closed, will disable the keyboard function.

1	3	5	7	9				

Pin #	Signal Name	
1	Power LED (+)	
3	No connector	
5	Ground	
7	Keylock	
9	Ground	

SMI/Hardware Switch: Pins 11 and 12

This connector supports the "Green Switch" from the control panel, which, when pressed, will force the system into the power-saving mode immediately.

12	Pin #	Signal Nam
	11	Ground
	12	Sleep

1 3 5 7 9 11

Reset Switch: Pins 17 and 18

The reset switch allows the user to reset the system without turning the main power switch OFF and then ON. Orientation is not required when making a connection to this header.

				18	20
1				17	19

Jumpers and Connectors

Hard Disk Drive LED Connector: Pins 19 and 20

This connector connects to the hard drive activity LED on control panel. This LED will flash when the HDD is being accessed.

20	Pin #	Signal Name
	19	LED(-)
	20	LED(+)
1 19		

3.2.2 EIDE Connectors: CN7, CN8

CN7: Primary IDE Connector

¹ 2	Signal Name	Pin #	Pin #	Signal Name
	Reset IDE	1	2	Ground
	Host data 7	3	4	Host data 8
	Host data 6	5	6	Host data 9
	Host data 5	7	8	Host data 10
	Host data 4	9	10	Host data 11
	Host data 3	11	12	Host data 12
	Host data 2	13	14	Host data 13
	Host data 1	15	16	Host data 14
	Host data 0	17	18	Host data 15
	Ground	19	20	Кеу
	DRQ0	21	22	Ground
	Host IOW	23	24	Ground
	Host IOR	25	26	Ground
LĮĮ	IOCHRDY	27	28	Host ALE
39' '40	DACK0	29	30	Ground
CN7	IRQ14	31	32	No connector
J.11	Address 1	33	34	No connector
	Address 0	35	36	Address 2
	Chip select 0	37	38	Chip select 1
	Activity	39	40	Ground

Jumpers and Connectors

⊐ г 2	Signal Name	Pin #	Pin #	Signal Name
	Reset IDE	1	2	Ground
	Host data 7	3	4	Host data 8
	Host data 6	5	6	Host data 9
	Host data 5	7	8	Host data 10
	Host data 4	9	10	Host data 11
	Host data 3	11	12	Host data 12
	Host data 2	13	14	Host data 13
	Host data 1	15	16	Host data 14
	Host data 0	17	18	Host data 15
	Ground	19	20	Кеу
	DRQ1	21	22	Ground
	Host IOW	23	24	Ground
	Host IOR	25	26	Ground
μ	IOCHRDY	27	28	Host ALE
9' ' 40	DACK1	29	30	Ground
CN8	IRQ15	31	32	No connector
••••	Address 1	33	34	No connector
	Address 0	35	36	Address 2
	Chip select 0	37	38	Chip select 1
	Activity	39	40	Ground

CN8: Secondary IDE Connector

Jumpers and Connectors

3.2.3 Floppy Drive Connector: CN9

CN9 is a 34-pin header and supports disk drives of up to 2.88MB.

1 ₇ _2	Signal Name	Pin #	Pin #	Signal Name
	Ground	1	2	RM/LC
	Ground	3	4	No connector
	Ground	5	6	No connector
	Ground	7	8	Index
	Ground	9	10	Motor enable 0
	Ground	11	12	Drive select 1
	Ground	13	14	Drive select 0
	Ground	15	16	Motor enable 1
	Ground	17	18	Direction
	Ground	19	20	Step
	Ground	21	22	Write data
33 34	Ground	23	24	Write gate
0.10	Ground	25	26	Track 00
CN9	Ground	27	28	Write protect
	Ground	29	30	Read data
	Ground	31	32	Side 1 select
	Ground	33	34	Diskette change

Jumpers and Connectors

3.2.4 Parallel Port Connector: CN11

	Signal Name	Pin #	Pin #	Signal Name
║■■║	Line printer strobe	1	2	AutoFeed
	PD0, parallel data 0	3	4	Error
	PD1, parallel data 1	5	6	Initialize
	PD2, parallel data 2	7	8	Select
	PD3, parallel data 3	9	10	Ground
	PD4, parallel data 4	11	12	Ground
	PD5, parallel data 5	13	14	Ground
	PD6, parallel data 6	15	16	Ground
	PD7, parallel data 7	17	18	Ground
□ ■	ACK, acknowledge	19	20	Ground
	Busy	21	22	Ground
	Paper empty	23	24	Ground
	Select	25	26	Ground
25 26				

The following table describes the pin out assignments of this connector.

CN11

3.2.5 COM1 Serial Port: CN17

CN17, a DB-9 connector, is an onboard serial port of the **SBC8176**. The following table shows the pin assignments of this connector.

	Pin #	Signal Name
	• •• ••	
	1	DCD, Data carrier detect
	2	RXD, Receive data
	3	TXD, Transmit data
$\mathbb{P}^{(10000)}$	4	DTR, Data terminal ready
	5	GND, ground
CN17	6	DSR, Data set ready
•••••	7	RTS, Request to send
	8	CTS, Clear to send
	9	RI, Ring indicator

Jumpers and Connectors
3.2.6 **COM2 Serial Port: CN16**

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Signal Name Pin # RS-232 R2-422 RS-485 DCD TX-DATA-1 2 RX TX+ DATA+ 3 ТΧ RX+ No connector 4 DTR RX-No connector 100000 ⊕ 5 GND GND GND 60000 DSR No connector No connector 6 CN16 RTS No connector No connector 7 CTS No connector No connector 8 9 RI No connector No connector

CN16, a DB-9 connector, is the onboard COM2 serial port of the SBC8176. The following table shows its pin assignments.

3.2.7 Internal Keyboard Connector: CN10 **Internal Mouse Connector: CN14**

4 5

	Pin #	Signal Name		
	1	Keyboard clock		
	2	Keyboard data		
-	3	PG		
L= ï	4	GND		
CN10 ⁵	5	Vcc		
	-			
	Pin #	Signal Name		
	1	Mouse clock		
	2	Mouse data		
	3	PG		

GND

Vcc

Jumpers and Connectors

CN14

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3.2.8 External ATX Power Connector: CN15

	1	Pin #	Signal Name
•		1	SMI
		2	GND
		3	ATX Power Button
•	6	4	GND
	0	5	PS-ON (soft ON/OFF); to ATX power supply
CN15		6	5V SB (standby +5V); from ATX power supply

3.2.9 External PS/2 Keyboard and Mouse Connector: CN18

CN10	

Pin #	Signal Name
1	Keyboard data
2	Mouse clock
3	GND
4	5V
5	Keyboard clock
6	Mouse data

3.2.10 CPUI Fan Power Connector: CN1

 $\ensuremath{\text{CN1}}$ is a 3-pin header for the CPU fan. The fan must be a 12V fan.

_		-	Pin #	Signal Name
			1	Ground
			2	+12V
3	2	1	3	Rotation

3.2.11 CPUII Fan2 Connector: CN2

CN2 is a 3-pin header for the chassis fan. The fan must be a 12V fan.

_		-7	Pin #	Signal Name
			1	Rotation
			2	+12V
3	2	1	3	Ground

Jumpers and Connectors



3.2.12 USB Connector: CN13

The following table shows the pin outs of the USB connector.

1	2		CN17 Pin #	Signal Name
			1, 2	USB Vcc
			3	USBO-
			4	USB1-
J			5	USB0+
			6	USB1-
1			7,8	USBGND
			9, 10	Reserved
9	10			
CN1	3			

3.2.13 IrDA Connector: CN12

This connector is used for an IrDA connection for wireless communication.



Pin #	Signal Name
1	+5V
2	No connector
3	FIr RX
4	Clr RX
5	Ir RX
6	5VSB
7	GND
8	No connector
9	Ir TX
10	No connector

Jumpers and Connectors

3.2.14 Temperature Thermistor Connectors: CN4, CN5, CN6

	Connector	Function
	CN4	Thermistor connector for system temperature
2 1	CN5	Thermistor connector for CPUI temperature
Pin1: GND Pin2: Thermistor	CN6	Thermistor connector for CPUII temperature

NOTE: The SBC8176 can use any thermistor with $10k\Omega + / 2.5\%$ and B(25/85) value equal to 3435k +/-2%.

Jumpers and Connectors

Chapter 4 BIOS Configuration

Chapter 3 describes the different settings available in the Award BIOS that comes with the **SBC8176** CPU card. Also contained here are instructions on how to set up the BIOS configuration.

4.1 **BIOS Introduction**

The Award BIOS (Basic Input/Output System) installed in your computer system's ROM supports Intel Pentium[®] II/III processors in a standard IBM-AT compatible I/O system. The BIOS provides critical low-level support for standard devices such as disk drives, serial and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

4.2 BIOS Setup

The Award BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn ON the computer, the Award BIOS is immediately activated. Pressing the key immediately allows you to enter the Setup utility. If you are a little bit late pressing the key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system OFF and back ON again. The following message will appear on the screen:

Press to Enter Setup

In general, you press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

BIOS Configuration

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS			
BIOS FEATURES SETUP	SUPERVISOR PASSWORD			
CHIPSET FEATURES SETUP	USER PASSWORD			
POWER MANAGEMENT SETUP	IDE HDD AUTO DETECTION			
PNP/PCI CONFIGURATION	SAVE & EXIT SETUP			
LOAD BIOS DEFAULTS	EXIT WITHOUT SAVING			
LOAD SETUP DEFAULTS				
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item			
F10 : Save & Exit Setup	(Shift) F2 : Change Color			
Time, Date, Hard Disk Type				

The section below the setup items of the Main Menu displays the control keys for this menu. Another section located at the bottom of the Main Menu, just below the control keys section, displays information on the currently highlighted item in the list.

NOTE: If you find that your computer cannot boot after making and saving system changes with Setup, the Award BIOS, via its built-in override feature, resets your system to the CMOS default settings.

We strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your system manufacturer to provide the absolute maximum performance and reliability.

BIOS Configuration

4.3 Standard CMOS Setup

"Standard CMOS Setup" allows you to record some basic hardware configurations in your computer system and set the system clock and error handling. If the motherboard is already installed in a working system, you will not need to select this option. You will need to run the Standard CMOS option, however, if you change your system hardware configurations, the onboard battery fails, or the configuration stored in the CMOS memory was lost or damaged.

ROM PCI/ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.

Date (mm:dd:yy) : Wed, Mar 4 1998								
Time (hh:mm:ss) :	Time (hh:mm:ss) : 00 : 00 : 00							
HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOR	MODE
Primary Master	Auto	0	0	0	0	0	0	Auto
Primary Slave	Auto	0	0	0	0	0	0	Auto
Secondary Master	Auto	0	0	0	0	0	0	Auto
Secondary Slave	Auto	0	0	0	0	0	0	Auto
Drive A	: 1.44	M, 3.5	in		Base	Memory	: 6	40K
Drive B	: None	•			Extended	Memory	: 153	50K
					Othe	r Memory	: 3	34K
Video	: EGA	/ VGA	4					
Halt On	: All E	rrors			Tota	l Memory	: 163	34K
ESC : Quit		$\wedge \downarrow \rightarrow$	←:Se	lect	Item P	U / PD /	+ / - : Mo	dify
F1 : Help		(Shift)	F2 : C	hange C	Color			

At the bottom of the menu are the control keys for use on this menu. If you need any help in each item field, you can press the <F1> key. It will display the relevant information to help you. The memory display at the lower right-hand side of the menu is read-only. It will adjust automatically according to the memory changed. The following pages describe each item of this menu.

BIOS Configuration

Date

The date format is:

Day	The day of week, from Sun to Sat, determined by the BIOS, is read only
Month	The month, Jan (1) through Dec (12)
Date	The date, from 1 to 31 (or the maximum allowed in the month), can key in the numerical / function key
Year	The year, from 1994 to 2079

To set the date, highlight the "Date" field and use the PageUp/ PageDown or +/- keys to set the current time.

Time

36

The time format is:

Hour	From 00 to 23
Minute	From 00 to 59
Second	From 00 to 59

To set the time, highlight the "Time" field and use the <PgUp>/ <PgDn> or +/- keys to set the current time.

Primary HDDs / Secondary HDDs

The onboard PCI IDE connectors provide Primary and Secondary channels for connecting up to four IDE hard disks or other IDE devices. Each channel can support up to two hard disks; the first is the "Master" and the second is the "Slave".

To enter the specifications for a hard disk drive, you must select first a "Type". There are 45 predefined types and 4 user definable types are for Enhanced IDE BIOS. Types 1 to 45 are predefined. Type "User" is user-definable. For the Primary Master/Slave as well as Secondary Master/Slave, you can select "Auto" under the TYPE and MODE fields. This will enable auto detection of your IDE drives and CD-ROM drive during POST.

Press <PgUp>/<PgDn> to select a numbered hard disk type or type the number and press the <Enter> key. The hard disk will not work properly if you enter incorrect information for this field. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually. If you select Type User, the utility will ask you to enter the following information:

CYLS	number of cylinders		
HEAD	number of read/write heads		
PRECOMP	write precompensation		
LANDZ	landing zone		
SECTOR	number of sectors		
SIZE	Automatically adjust according to the configuration		
MODE (for IDE HDD only):	Auto Normal (HD < 528MB) Large (for MS-DOS only) LBA (HD > 528MB and supports Logical Block Addressing)		

NOTE: The specifications of your drive must match with the drive table. The hard disk will not work properly if you enter incorrect information in these fields. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

• Drive A / Drive B

These fields identify the types of floppy disk drive A or drive B that has been installed in the computer. The available specifications are:

360K, 5.25 in	5.25 inch PC-type standard drive; 360Kb capacity
1.2M, 5.25 in	5.25 inch AT-type high-density drive; 1.2MB capacity
720K, 3.5 in	3.5 inch double-sided drive; 720Kb capacity
1.44M, 3.5 in	3.5 inch double-sided drive; 1.44MB capacity
2.88M, 3.5 in	3.5 inch double-sided drive; 2.88MB capacity

Video

This field selects the type of video display card installed in your system. You can choose the following video display cards:

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA, SVGA or PGA monitor adapters. (default)
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	For Hercules or MDS adapters, includes high resolution monochrome adapters

BIOS Configuration

Halt On

38

This field determines whether the system will halt if an error is detected during power up.

No errors	The system boot will halt on any error detected. (default)
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not stop for a keyboard error; it will stop for all other errors.
All, But Diskette	The system boot will not stop for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error; it will stop for all other errors.

4.4 BIOS Features Setup

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

ROM / PCI ISA BIOS BIOS FEATURES SETUP AWARD SOFTWARE, INC.

Virus Warning	: Disabled	Video BIOS Sha	adow	: Enabled
CPU Internal Cache	: Enabled	C8000-CBFFF S	Shadow	: Disabled
External Cache	: Enabled	CC000-CFFFF S	Shadow	: Disabled
CPU L2 Cache ECC Checking	g : Enabled	D0000-D3FFF S	Shadow	: Disabled
Quick Power On Self Test	: Enabled	D4000-D7FFF S	Shadow	: Disabled
Boot Sequence	: A, C, SCSI	D8000-DBFFF S	Shadow	: Disabled
Swap Floppy Drive	: Disabled	DC000-DFFF S	Shadow	: Disabled
Boot Up Floppy Drive	: Enabled			
Boot Up Numlock Status	: On			
Gate A20 Option	: Fast			
Typematic Rate Setting	: Disabled			
Typematic Rate (chars/Sec)	: 6			
Typematic Delay (Msec)	: 250			
Security Option	: Setup			
PCI /VGA Palette Snoop	: Disabled			
OS Select For DRAM>64MB	: Non-OS2	ESC : Quit	$\land \lor \to \leftarrow :$	Select Item
Report No FDD For WIN 95	: No	F1 : Help	PU/PD/+/-	: Modify
		F5 : Old Values	(Shift) F2 :	: Color
		F6 : Load BIOS I	Defaults	
		F7 : Load Setup	Defaults	

• Virus Warning

This item protects the boot sector and partition table of your hard disk against accidental modifications. If an attempt is made, the BIOS will halt the system and display a warning message. If this occurs, you can either allow the operation to continue or run an anti-virus program to locate and remove the problem.

NOTE: Many disk diagnostic programs, which attempt to access the boot sector table, can cause the virus warning. If you will run such a program, disable the Virus Warning feature.

CPU Internal Cache / External Cache

Cache memory is additional memory that is much faster than conventional DRAM (system memory). CPUs from 486-type on up contain internal cache memory, and most, but not all, modern PCs have additional (external) cache memory. When the CPU requests data, the system transfers the requested data from the main DRAM into cache memory, for even faster access by the CPU. These items allow you to enable (speed up memory access) or disable the cache function. By default, these items are *Enabled*.

• CPU L2 Cache ECC Checking

When enabled, this allows ECC checking of the CPU's L2 cache. By default, this field is *Enabled*.

Quick Power On Self Test

When enabled, this field speeds up the Power On Self Test (POST) after the system is turned ON. If it is set to *Enabled*, BIOS will skip some items.

Boot Sequence

This field determines the drive that the system searches first for an operating system. The options are:A, C, SCSI

- C, A, SCSI
- C, CDROM, A
- CDROM, C, A
- D, A, SCSI
- SCSI, C, A

The default value is A, C, SCSI.

- E, A, SCSIC only
- F, A, SCSI
- LS/ZIP, C
- SCSI, A, C

BIOS Configuration

• Swap Floppy Drive

This allows you to determine whether to enable Swap Floppy Drive or not. When enabled, the BIOS swaps floppy drive assignments so that Drive A becomes Drive B, and Drive B becomes Drive A. By default, this field is set to *Disabled*.

Boot Up Floppy Seek

When enabled, the BIOS will seek whether or not the floppy drive installed has 40 or 80 tracks. 360K type has 40 tracks while 760K, 1.2M and 1.44M all have 80 tracks. By default, this field is set to *Enabled*.

Boot Up NumLock Status

This allows you to activate the NumLock function after you power up the system. By default, the system boots up with *NumLock* ON.

• Boot Up System Speed

This has no function and selects the default system speed (High).

• Gate A20 Option

This you to select how Gate A20 is worked. Gate A20 is a device used to address memory above 1 MB. The default setting is *Fast*.

• Typematic Rate Setting

When disabled, continually holding down a key on your keyboard will generate only one instance. When enabled, you can set the two typematic controls listed next. By default, this field is set to *Disabled*.

• Typematic Rate (Chars/Sec)

When the typematic rate is enabled, the system registers repeated keystrokes speeds. You can select speed range from 6 to 30 characters per second. By default, this item is set to $\boldsymbol{6}$.

Typematic Delay (Msec)

When the typematic rate is enabled, this item allows you to set the time interval for displaying the first and second characters. By default, this item is set to *250msec*.

Security Option

40

This field allows you to limit access to the System and Setup. The default value is *Setup*. When you select *System*, the system prompts for the User Password every time you boot up. When you select *Setup*, the system always boots up and prompts for the Supervisor Password only when the Setup utility is called up.

PCI/VGA Palette Snoop

Some non-standard VGA display cards may not show colors properly. This field allows you to set whether MPEG ISA/VESA VGA Cards can work with PCI/VGA or not. When this field is enabled, a PCI/VGA can work with a MPEG ISA/VESA VGA card. When this field is disabled, a PCI/VGA cannot work with a MPEG ISA/VESA Card.

• OS Select for DRAM > 64MB

This option allows the system to access greater than 64MB of DRAM memory when used with OS/2 that depends on certain BIOS calls to access memory. The default setting is *Non-OS/2*.

• Report No FDD For WIN 95

This option allows Windows 95 to share IRQ6 (assigned to a floppy disk drive) with other peripherals in case the drive is not existing. The default setting is No.

• Video BIOS Shadow

This item allows you to change the Video BIOS location from ROM to RAM. Video Shadow will increase the video speed.

• C8000 - CBFFF Shadow/DC000 - DFFFF Shadow

Shadowing a ROM reduces the memory available between 640KB to 1024KB. These fields determine whether optional ROM will be copied to RAM or not.



4.5 Chipset Features Setup

This Setup menu controls the configuration of the motherboard chipset.

ROM PCI/ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE INC.

Auto Configuration	: Enabled	CPU Warning Temperature : 66°C/151°F
		Current CPU Block Temp. : 41°C/ 105°F
		Current CPU/Chips Temp. : 27°C/ 80°F
SDRAM RAS-to-CAS Delay	3	Current System Temp. : 34°C/ 93°F
SDRAM RAS Precharge Time	3	Current CPU Fan Speed : 2789 RPM
SDRAM CAS Latency Time	3	Current Chassis Fan Speed : 2045 RPM
DRAM Integrity Mode	Non-ECC	
System BIOS Cacheable	: Disabled	VCCP (V) : 1.98 V VTT (V) : 1.50 V
Video BIOS Cacheable	: Enabled	VCC3 (V) : 3.45 V + 5 V : 4.99 V
Video RAM Cacheable	: Disabled	+12 V : 12.46 V -12 V : -12.54V
8 Bit I/O Recovery Time	: 3	-5V : - 5.21 V
16 Bit I/O Recovery Time	: 2	
Memory Hole At 15MB-16MB	: Disabled	
Passive Release	: Disabled	
Delayed Transaction	: Disabled	
AGP Aperture Size (MB)	: 64	ESC : Quit $\land \lor \to \leftarrow$: Select Item
		F1 : Help PU/PD/+/- : Modify
		F5 : Old Values (Shift) F2 : Color
		F6 : Load BIOS Defaults
		F7 : Load Setup Defaults

Auto Configuration

This predefines values for DRAM, cache timing according to CPU type and system clock. When enabled, the predefined items will become read-only.

SDRAM RAS-to-CAS Delay

During DRAM refresh, both rows and columns are addressed separately. This field allows you to determine the timing of transition from Row Address Strobe (RAS) to Column Address Strobe (CAS). The default setting is *3*.

• SDRAM RAS Precharge Time

The precharge time is the number of cycles for the RAS to accumulate its charge before DRAM refreshes. If time is insufficient, refresh may be incomplete and the DRAM may fail to retain data. The default setting is 3.

BIOS Configuration

SDRAM CAS Latency Time

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer. The default setting is *3*.

• DRAM Data Integrity Mode

This option sets the data integrity mode of the DRAM installed in the system. The default setting is *Non-ECC*.

• System BIOS Cacheable

When enabled, access to the system BIOS ROM addressed at F0000H-FFFFH is cached, provided that the cache controller is disabled.

• Video BIOS Cacheable

When enabled, access to video BIOS addressed at C0000H to C7FFFH is cached, provided that the cache controller is disabled.

• Video RAM Cacheable

Selecting *Enabled* allows caching of the video BIOS ROM at C0000h to C7FFFh, resulting in better video performance. However, if any program writes to this memory area, a memory access error may result.

• 8 Bit I/O Recovery Time

This option specifies the length of the delay (in sysclks) inserted between consecutive 8-bit I/O operations. The settings are 1, 2, 3, 4, 5, 6, 7, or 8. The default setting is *3*.

• 16 Bit I/O Recovery Time

This option specifies the length of the delay (in sysclks) inserted between consecutive 16-bit I/O operations. The settings are 1, 2, 3, 4, 5, 6, 7, or 8. The default setting is *2*.

• Memory Hole at 15MB - 16MB

In order to improve performance, certain space in memory can be reserved for ISA cards. This field allows you to reserve 15MB to 16MB memory address space to ISA expansion cards. This makes memory from 15MB and up unavailable to the system. Expansion cards can only access memory up to 16MB. By default, this field is set to *Disabled*.

Passive Release

When enabled, CPU to PCI bus accesses are allowed during passive release. Otherwise, the arbiter only accepts another PCI master access to local DRAM.



• Delayed Transaction

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select *Enabled* to support compliance with PCI specification version 2.1. The default setting is *Disabled*.

• AGP Aperture Size (MB)

The field sets aperture size of the graphics. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation. The options available are 4M, 8M, 16M, 32M, 64M, 128M and 256M. The default setting is *64M*.

• CPU Warning Temperature

This field sets the threshold temperature at which an alert is sounded through the system's speaker. An onboard thermal sensor monitors the CPU temperature to prevent CPU overheating.

• Current System/CPU Temp.

These read-only fields reflect the functions of the hardware thermal sensor that monitors the chip blocks and system temperatures to ensure the system is stable.

• Current CPU Fan1/CPU Fan2 Speed

These optional and read-only fields show the current speeds in RPM (revolution per minute) for the CPU fan and chassis fan as monitored by the hardware monitoring IC.

• VCCP / VTT / VCC3

These optional and read-only fields show the current voltages in the voltage regulators and power supply as monitored by the hardware monitoring IC.



4.6 **Power Management Setup**

The Power Management Setup allows you to save energy of your system effectively. It will shut down the hard disk and turn OFF video display after a period of inactivity.

ROM PCI/ISA BIOS POWER MANAGEMENT SETUP AWARD SOFTWARE, INC.

Power Management	: User Define	** Reload Glo	bal Timer Events **
PM Control by APM	: Yes	IRQ3 (3-7, 9-15),	NMI : Disabled
Video Off Method	: V/H SYNC +Blank	Primary IDE 0	: Enabled
Video Off After	: Standby	Primary IDE 1	: Enabled
MODEM Use IRQ	: 3	Secondary IDE 0	: Disabled
		Secondary IDE 1	: Disabled
Doze Mode	: Disabled	Floppy Disk	: Disabled
Standby Mode	: Disabled	Serial Port	: Enabled
Suspend Mode	: Disabled	Parallel Port	: Disabled
HDD Power Down	: Disabled		
Throttle Duty Cycle	: 62.5%		
VGA Active Monitor	: Enabled		
Soft-Off by PWR-BTTN	: Instant-Off		
Resume by Ring	: Disabled		
Resume by Alarm	: Disabled		
		ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item
IRQ 8 Break Suspend	: Disabled	F1 : Help	PU/PD/+/- : Modify
		F5 : Old Values	(Shift) F2 : Color
		F6 : Load BIOS D	efaults
		F7 : Load Setup D	efaults

Power Management

This field allows you to select the type of power saving management modes. There are four selections for Power Management.

Min. Power Saving	Minimum power management
Max. Power Saving	Maximum power management. Only available for SL CPU
User Define	Each of the ranges is from 1 min. to 1hr. Except for HDD Power Down which ranges from 1 min. to 15 min. (Default)

NOTE: In order to enable the CPU overheat protection feature, the Power Management field should not be set to Disabled.

BIOS Configuration

• PM Control by APM

This field allows you to use the Advanced Power Management device to enhance the Max. Power Saving mode and stop the CPU's internal clock. If the Max. Power Saving is not enabled, this will be preset to NO.

• Video Off Method

This field defines the Video Off features. There are three options.

V/H SYNC + Blank	Default setting, blank the screen and turn OFF vertical and horizontal scanning	
DPMS	Allows the BIOS to control the video display card if it supports the DPMS feature	
Blank Screen	This option only writes blanks to the video buffer	

Video Off After

As the system moves from lesser to greater power-saving modes, select the mode in which you want the monitor to blank.

Modem Use IRQ

This field names the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system. By default, the IRQ is set to *3*.

Doze Mode

When enabled, and after the set time of system inactivity, the CPU clock will run at a slower speed while all other devices still operate at full speed.

Standby Mode

After the selected period of system inactivity, the fixed disk drive and the video shut OFF while all other devices still operate at full speed.

Suspend Mode

When enabled, and after the set time of system inactivity, all devices except the CPU will be shut OFF.

HDD Power Down

When enabled, and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

• Throttle Duty Cycle

When the system enters Doze mode, the CPU clock runs only part of the time. You may select the percent of time that the clock runs.

BIOS Configuration

• VGA Active Monitor

When enabled, any video activity restarts the global timer for Standby mode. The default setting is *Enabled*.

• Soft-Off by PWR-BTTN

This field defines the power-OFF mode when using an ATX power supply. The Instant-Off mode allows powering OFF immediately upon pressing the power button. In the Delay 4 Sec mode, the following happen:

- The system powers OFF when the power button is pressed for more than four seconds, setting the system in a very low-powerusage state, with only enough circuitry receiving power to detect power button activity
- Resume by Ring activity (see next field) when pressed for less than 4 seconds.

The default value is *Instant-Off*.

Resume by Ring

An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) awakens the system from a soft OFF state.

• Resume by Alarm

This allows a computer to be turned ON automatically through the timer set in the BIOS to make the system more schedule-configured. By default, this field is set to *Disabled*.

IRQ 8 Break Suspend

You can enable or disable the monitoring of IRQ 8 (Real Time Clock) so it does not awaken the system from Suspend mode.

Reload Global Timer Events

This section determines the reloading of the 'timers' after entering the Full On. You can enable or disable the monitoring of IRQ 8 (Real Time Clock) so it does not awaken the system from Suspend mode.

4.7 **PNP/PCI** Configuration

This option configures the PCI bus system. All PCI bus systems on the system use INT#, thus all installed PCI cards must be set to this value.

ROM PCI/ISA BIOS PNP/PCI CONFIGURATION AWARD SOFTWARE INC.

PNP OS Installed	: No	Used MEM base a	ddr : N/A
Resources Controlled by	: Manual		
Reset Configuration Data	: Disabled		
IRQ-3 assigned to	: Legacy ISA		
IRQ-4 assigned to	: Legacy ISA		
IRQ-5 assigned to	: PCI/ISA PnP		
IRQ-7 assigned to	: Legacy ISA		
IRQ-9 assigned to	: PCI/ISA PnP		
IRQ-10 assigned to	: PCI/ISA PnP		
IRQ-11 assigned to	: PCI/ISA PnP		
IRQ-12 assigned to	: PCI/ISA PnP		
IRQ-14 assigned to	: PCI/ISA PnP		
IRQ-15 assigned to	: PCI/ISA PnP		
DMA-0 assigned to	: PCI/ISA PnP		
DMA-1 assigned to	: PCI/ISA PnP	ESC : Quit	$\land \downarrow \leftarrow$: Select Item
DMA-3 assigned to	: PCI/ISA PnP	F1 : Help	PU/PD/+/- : Modify
DMA-5 assigned to	: PCI/ISA PnP	F5 : Old Values	(Shift) F2 : Color
DMA-6 assigned to	: PCI/ISA PnP	F6 : Load BIOS De	efaults
DMA-7 assigned to	: PCI/ISA PnP	F7 : Load Setup D	efaults

• PNP OS Installed

This field allows you to specify if the operating system installed in your system is plug and play aware.

Resources Controlled by

This PnP BIOS can configure all of the boot and compatible devices automatically. However, this capability needs you to use a PnP operating system such as Windows 95. The default value is *Manual*.

Reset Configuration Data

48

This field allows you to determine whether to reset the configuration data or not. The default value is *Disabled*.

NOTE: Operating systems such as DOS, OS/2, and Windows 3.x do not use PnP

IRQ3/4/5/7/9/10/11/12/14/15, DMA0/1/3/5/6/7 assigned to

These fields allow you to determine the IRQ/DMA assigned to the ISA bus and is not available to any PCI slot.

• Used MEM base addr

Select a base address for the memory area used by any peripheral that requires high memory. The default setting is N/A.

4.8 Load BIOS Defaults

This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS	
BIOS FEATURES SETUP	SUPERVISOR PASSWORD	
CHIPSET FEATURES SETUP	USER PASSWORD	
POWER MANAGE	DETECTION	
PNP/PCI CONFIG Load BIOS Defa	ults (Y/N)? N ETUP	
LOAD BIOS DEF	SAVING	
LOAD SETUP DEFAULTS		
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item	
F10 : Save & Exit Setup	(Shift) F2 : Change Color	
Load BIOS Defaults except Standard CMOS Setup		

To load BIOS defaults value to CMOS SRAM, enter "Y". If not, enter "N".

BIOS Configuration

4.9 Load Setup Defaults

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATU <u>RES SETUP</u>	USER PASSWORD
POWER MANAG	DETECTION
PNP/PCI CONFIC Load Setup Defaults (Y/N)? N ETUP	
LOAD BIOS DEF	EAT WITTEET SAVING
LOAD SETUP DEFAULTS	
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Load BIOS Defaults except Standard CMOS Setup	

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

BIOS Configuration

4.10 Integrated Peripherals

This option sets your hard disk configuration, mode and port.

ROM PCI/ISA BIOS INTEGRATED PERIPHERALSP AWARD SOFTWARE INC.

IDE HDD Block Mode	: Enabled	Onboard Parallel	Port	: 378/IRQ7
IDE Primary Master PIO	: Auto	Parallel Port Mode	e	: SPP
IDE Primary Slave PIO	: Auto			
IDE Secondary Master PIO	: Auto			
IDE Secondary Slave PIO	: Auto			
IDE Primary Master UDMA	: Auto			
IDE Primary Slave UDMA	: Auto			
IDE Secondary Master UDMA	: Auto			
IDE Secondary Slave UDMA	: Auto			
On-Chip Primary PCI IDE	: Enabled			
On-Chip Secondary PCI IDE	: Enabled			
USB Keyboard Support	: Disabled			
Init AGP Display First	: Disabled			
Onboard FDC Controller	: Enabled			
Onboard Serial Port 1	: 3F8/IRQ4	ESC : Quit	$\land \lor \leftarrow :$	Select Item
Onboard Serial Port 2	: 2F8/IRQ3	F1 : Help	PU/PD/-	+/- : Modify
UART Mode Select	: Normal	F5 : Old Values	(Shift) F	2 : Color
		F6 : Load BIOS D	efaults	
		F7 : Load Setup D	efaults	

IDE HDD Block Mode

This field allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive.

• IDE Primary/Secondary Master/Slave PIO

These fields allow your system hard disk controller to work faster. Rather than have the BIOS issue a series of commands that transfer to or from the disk drive, PIO (Programmed Input/Output) allows the BIOS to communicate with the controller and CPU directly.

The system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When Auto is selected, the BIOS will select the best available mode.

• IDE Primary/Secondary Master/Slave UDMA

These fields allow your system to improve disk I/O throughput to 33Mb/sec with the Ultra DMA/33 feature. The options are *Auto* and *Disabled*.

BIOS Configuration

• On-Chip Primary/Secondary PCI IDE

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select *Enabled* to activate each channel separately.

• USB Keyboard Support

Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

• Init AGP Display First

This field allows the system to initialize first the VGA card in the AGP slot on the motherboard when system is turned ON.

Onboard FDC Controller

Select *Enabled* if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install an add-in FDC or the system has no floppy drive, select Disabled in this field. This option allows you to select the onboard FDD port.

• Onboard Serial/Parallel Port

These fields allow you to select the onboard serial and parallel ports and their addresses. The default values for these ports are:

Serial Port 1	3F8/IR04
Sorial Port 2	2E8/ID03
	21 0/11(0)

UART Mode Select

This field determines the UART mode in your computer. The settings are *Normal*, *IrDA and ASKIR*. The default value is *Normal*.

Parallel Port Mode

52

This field allows you to determine parallel port mode function.

SPP	Normal Parallel Port
EPP	Enhanced Capabilities Port
ECP	Extended Capabilities Port

4.11 Supervisor / User Password

These two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password, up to eight characters in length, and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS	
BIOS FEATURES SETUP	SUPERVISOR PASSWORD	
CHIPSET FEATURES SETUP	USER PASSWORD	
POWER MANAG	DETECTION	
PNP/PCI CONFICE Enter Passwo	EL FORMAT	
LOAD BIOS DEF	ETUP	
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING	
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item	
F10 : Save & Exit Setup	(Shift) F2 : Change Color	
Change / Set / Disable Password		

4.12 IDE HDD Auto Detection

This option detects the parameters of an IDE hard disk drive, and automatically enters them into the Standard CMOS Setup screen.

ROM PCI/ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.



Up to four IDE drives can be detected, with parameters for each appearing in sequence inside a box. To accept the displayed entries, press the "Y" key; to skip to the next drive, press the "N" key. If you accept the values, the parameters will appear listed beside the drive letter on the screen.

BIOS Configuration

4.13 Save & Exit Setup

This option allows you to determine whether to accept the modifications or not. If you type "Y", you will quit the setup utility and save all changes into the CMOS memory. If you type "N", you will return to Setup utility.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS	
BIOS FEATURES SETUP	SUPERVISOR PASSWORD	
CHIPSET FEATURES SETUP	USER PASSWORD	
POWER MANAG	DETECTION	
PNP/PCI CONFIC Save to CMOS and Exit(y/N)? N ETUP		
LOAD BIOS DEFAOLTS	EXIT WITHOUT SAVING	
LOAD SETUP DEFAULTS		
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item	
F10 : Save & Exit Setup	(Shift) F2 : Change Color	
Save Data to CMOS & Exit Setup		

BIOS Configuration

4.14 Exit Without Saving

Select this option to exit the Setup utility without saving the changes you have made in this session. Typing "Y" will quit the Setup utility without saving the modifications. Typing "N" will return you to Setup utility.

ROM PCI/ISA BIOS
CMOS SETUP UTILITY
AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS	
BIOS FEATURES SETUP	SUPERVISOR PASSWORD	
CHIPSET FEATURES SETUP	USER PASSWORD	
POWER MANAGE	DETECTION	
PNP/PCI CONFIG Quit Without Saving (Y/N)? N		
LOAD BIOS DEFAULTO	SAVING	
LOAD SETUP DEFAULTS		
ESC : Quit	$\land \lor \rightarrow \leftarrow$: Select Item	
F10 : Save & Exit Setup	(Shift) F2 : Change Color	
Abandon all Data & Exit Setup		

BIOS Configuration

Chapter 5 System Hardware Doctor

This chapter introduces System hardware Doctor that comes with the **SBC8176**. The following sections are in conjunction with the onboard hardware monitoring IC and the functions that come with the utility.

System Hardware Doctor is a utility software that monitors the system temperature, system voltage, CPU and system fan rotational speeds. If conditions become adverse, that is,, when voltages are erratic or CPU temperature exceeds the safe limits, an alarm will be sounded; thereby preventing the system from crashing and ensuring overall stability.

5.1 System Hardware Doctor Installation

The following section lists the steps when installing the System Hardware Doctor into your system.

Step 1. Under Windows operating environment, insert your System hardware Doctor Utility Disk 1 into your floppy drive. Run SETUP.EXE and follow the instructions stated in the Setup box that follows.



System Hardware Doctor

Step 2. By clicking on the Chang Directory box, you specify the directory where you'd like the System Hardware Doctor be installed. If you choose to install System Hardware Doctor utility to the default directory C:\Program Files\Hardware Doctor\, click on the Setup button to complete the installation process.

5.2 Launching the System Hardware Doctor Utility

After installation is completed, you may launch the System Hardware Doctor utility by following the steps below.

Step 1. Press the Start button on your Windows Program Manager (located at the lower left corner of the screen). Point your cursor to Programs and a list of applications pops up as shown on the following illustration. Select Hardware Doctor.



System Hardware Doctor

Step 2. Click on Hardware Doctor and the System Hardware Doctor application window appears.



System Hardware Doctor

This page does not contain any information.

Appendix A Watchdog Timer Configuration

Using the Watchdog Function

Follow these steps when using the watchdog function onboard the SBC8155 CPU card.

1. To enable the watchdog function, read I/O port 121H (e.g., "I 121") under "debug" of MS-DOS environment. To disable the function, place I/O port 120H under "debug" of MS-DOS environment.

Reset Watchdog -Read I/O Port 121H

Disable Watchdog - Read I/O Port 120H

In system Run_Time, you still have to read I/O Port 121H to reset the Watchdog timer.

If the system fails, the TSR should be stopped for the Watchdog Reset to be activated.

2. Set the corresponding Watchdog option in the Integrated Peripherals menu of the CMOS Setup utility. The available Time-out Trigger options are 0.5, 1, 5, 10, 20, 50, 100, 150, 200, 250, 300, 350, 400, 450, 500 and 1000 sec(s).

Watchdog Timer Configuration

This page does not contain any information.

Appendix B I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. There are a total of 1K port address spaces available. The following table lists the I/O port addresses used on the Industrial CPU Card.

Address	Device Description
000h - 01Fh	DMA Controller #1
020h - 03Fh	Interrupt Controller #1
040h - 05Fh	Timer
060h - 06Fh	Keyboard Controller
070h - 07Fh	Real Time Clock, NMI
080h - 09Fh	DMA Page Register
0A0h - 0BFh	Interrupt Controller #2
0C0h - 0DFh	DMA Controller #2
0F0h	Clear Math Coprocessor Busy Signal
0F1h	Reset Math Coprocessor
1F0h - 1F7h	IDE Interface
278 - 27F	Parallel Port #2(LPT2)
290 - 297	LM78
2F8h - 2FFh	Serial Port #2(COM2)
2BO - 2DF	Graphics Adapter Controller
378h - 3FFh	Parallel Port #1(LPT1)
360 - 36F	Network Ports
3BO - 3BF	Monochrome & Printer adapter
3CO - 3CF	EGA adapter
3D0 - 3DF	CGA adapter
3F0h - 3F7h	Floppy Disk Controller
3F8h - 3FFh	Serial Port #1(COM1)

I/O Port Address Map

This page does not contain any information.
Appendix C Interrupt Request Lines (IRQ)

There are a total of 15 IRQ lines available on the Industrial CPU Card. Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on the Industrial CPU Card.

Level	Function
IRQ0	System Timer Output
IRQ1	Keyboard
IRQ2	Interrupt Cascade
IRQ3	Serial Port #2
IRQ4	Serial Port #1
IRQ5	Parallel Port #2
IRQ6	Floppy Disk Controller
IRQ7	Parallel Port #1
IRQ8	Real Time Clock
IRQ9	Software Redirected to Int 0Ah
IRQ10	Reserved
IRQ11	Reserved
IRQ12	Reserved
IRQ13	80287
IRQ14	Primary IDE
IRQ15	Secondary IDE

Interrupt Request Lines (IRQ)

This page does not contain any information.

66