

QUICK REFERENCE

JUMPER SETTINGS (* : Initial Setting)

W1, W16, W17, W18, W19 - CPU Setups						
AMD	Type Spec.	W1	W16	W17	W18	W19
486DX2SV (3.3V)	Wr-Th/clk 2x	1-2	on	1-2	off	1-2
	Wr-Bk/clk 2x	1-2	on	2-3	on	2-3
486DX4 (3.3V)	Wr-Th/clk 2x	1-2	on	1-2	off	1-2
	Wr-Bk/clk 2x	1-2	on	2-3	on	2-3
	Wr-Th/clk 3x	1-2	off	1-2	off	1-2
	Wr-Bk/clk 3x	1-2	off	2-3	on	2-3
5x86 or 486DX5 (133/3.3V)	Wr-Th/clk 3x	1-2	off	1-2	off	1-2
	Wr-Bk/clk 3x	1-2	off	2-3	on	2-3
	Wr-Th/clk 4x	1-2	on	1-2	off	1-2
	Wr-Bk/clk 4x	1-2	on	2-3	on	2-3

Careful attention should be taken when installing a processor. Faulty jumper settings may damage both your processor and your board.

W2 - SCSI Active Termination	
Controlled by Software	2-3
Disabled by Hardware	off
Controlled by Hardware * (Board is Terminated)	1-2

W6, 12, 13, 14, 15 - Serial Port 2 Setup					
(configure jointly)	W6	W12	W13	W14	W15
RS-232 *	off	1-2	1-2	1-2	1-2
RS-422/485	on	2-3	2-3	2-3	2-3

W3, W24 - External CPU Bus Speed		
	W3	W24
25MHz	off	3-4
33MHz *	off	1-2

W7, W8 - Serial Port 2 Signal Setup		
(RS-422/485 mode only)	Loopback	Normal
W7: RTS2-CTS2 Loopback	on	off
W8: DSR2-DTR2 Loopback	on	off

W4, W5 - Feature Connector/V-Port		
(configure jointly)	W4	W5
Feature connector Enabled	off	on
Feature connector disabled *	on	off
V-Port Enabled	off	off

W9 - IOCHRDY	
To IDE Interface	on
Disabled *	off

W10 - Flat Panel	
5V signal level *	1-2
3.3V signal level	2-3

This jumper does not select flat panel power supply

W20, W21 - HDOUT / EDOUT Signal Distribution (Configure jointly)			
W20: 2.88MB High Density Floppy, HDOUT Signal			
HDOUT left to software *	none		
HDOUT to pin 33 (J2) : GND to pin 27 (J2)	1-3 ; 2-4		
HDOUT to pin 27 (J2) : GND to pin 33 (J2)	1-2 ; 3-4		
W21: 2.88MB High Density Floppy, EDOUT Signal			
EDOUT left to software *	none		
EDOUT to pin 29 (J2) : GND to pin 17 (J2)	1-3 ; 2-4		
EDOUT to pin 17 (J2) : GND to pin 29 (J2)	1-2 ; 3-4		

W22 - Supervisor I/O Base Address			
190H *	1-2 ; 3-4	390H	3-4
290H	1-2	390H	none

W23 - Extended BIOS Modes		
	on	off *
1-2	Serial Download Mode	Normal Mode
3-4	VT100 Mode	Standard Mode
5-6	Disable TEK. Extension	Enable TEK. Extension
7-8	Disable Onboard VGA	Enable Onboard VGA

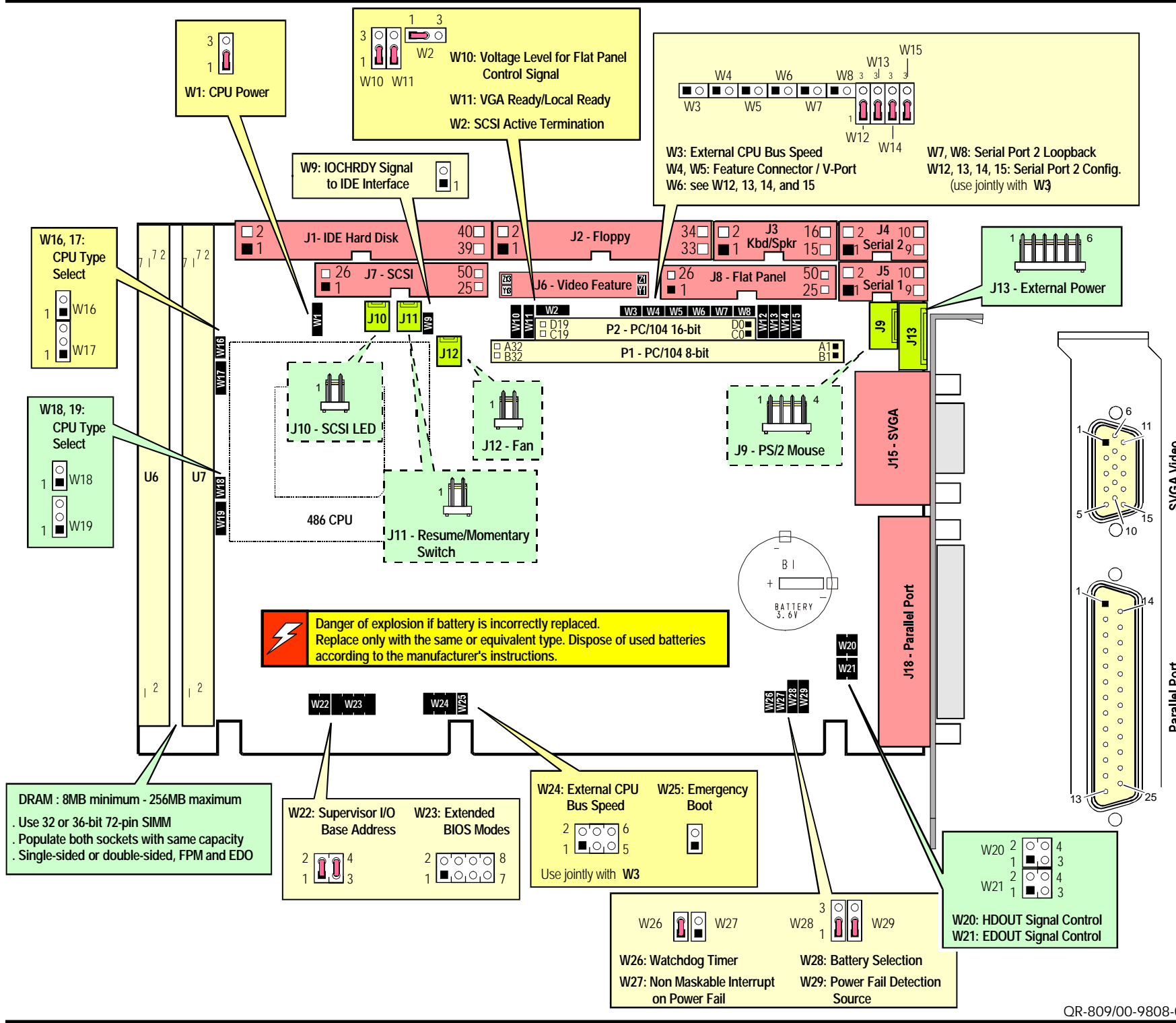
W25 - BIOS Boot	
Emergency	on
Normal *	off

W26 - Watchdog	
Enabled *	on
Disabled	off

W27 - Non Maskable Interrupt Setup	
On Power Fail Output	on
Disabled *	off

W28 - Battery Selection	
Internal Battery *	1-2
External Battery	2-3

W29 - Power Fail Detection Source	
External Power Fail Input to pin 6 of J12 *	1-2
Internal/External battery when less than 3V	2-3



Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type. Dispose of used batteries according to the manufacturer's instructions.

DRAM : 8MB minimum - 256MB maximum
 Use 32 or 36-bit 72-pin SIMM
 Populate both sockets with same capacity
 Single-sided or double-sided, FPM and EDO

W22: Supervisor I/O Base Address		W23: Extended BIOS Modes	
2	4	2	8
1	3	1	7

W24: External CPU Bus Speed		W25: Emergency Boot	
2	6		
1	5		

Use jointly with W3

W26: Watchdog Timer		W28: Battery Selection	
		3	1
		1	2

W27: Non Maskable Interrupt on Power Fail
 W29: Power Fail Detection Source

W20: HDOUT Signal Control		W21: EDOUT Signal Control	
2	4	2	4
1	3	1	3
2	4	2	4
1	3	1	3

