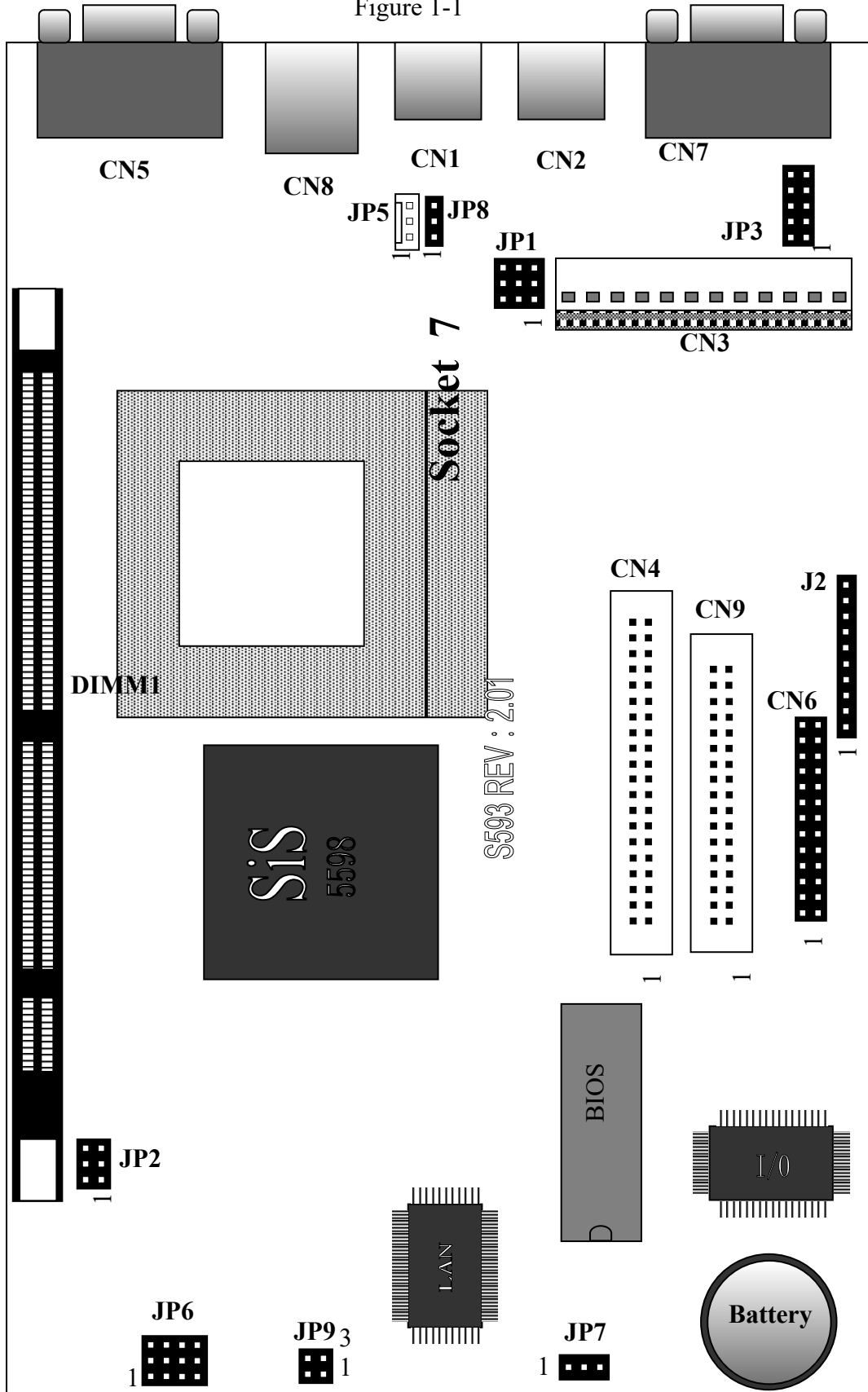

Hardware Configuration

Before install the mainboard into the system chassis, you may find it convenient to first configure the mainboard's hardware. This chapter describes how to set jumpers and where to attach components.

Mainboard Component Locations

Figure 1-1



Connectors

Attach system components and case devices to the mainboard via the mainboard connectors. A description of each connector follows. See Figure 1-1 for the location of the connectors on the mainboard.

Note: Make sure that the power is turned off before making any connection to the board.

CN1 – PS/2 Mouse Head Connector

CN2 – PS/2 Keyboard Head Connector

CN3 – AT Power Supply Connectors

CN4 – Primary IDE Ports

CN5 – VGA Port

CN6 – Parallel Port

CN7 – Serial Port

CN8 – LAN Connector

CN9 – Floppy Disk Port

J2 (1,2,3,4) – Speaker Connector

Pin	Description
1	Data Out
2	Ground
3	Ground
4	+5V

J2 (5,6) – HDD LED

Pin	Description
5	+5V
6	Active Low

J2 (7,8) – Reset Switch Connector

Setting	Description
Open	Normal Mode



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Close	Reset System
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J2 (9,10) – POWER LED

Pin	Description
9	LED Output
10	Ground

JP5 – Fan Power Connector

Pin	Description
1	N.C.
2	+12V
3	Ground

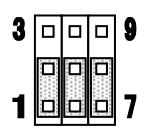
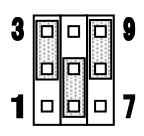
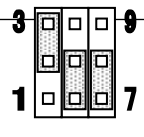
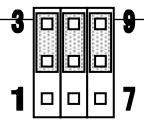
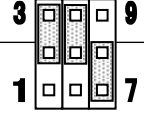
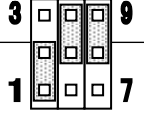
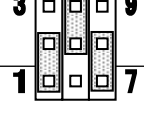
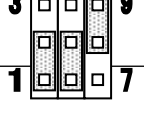
JP9 – LAN LED

Pin	Description
1	LED_Link Output
2	+5V
3	LED_RX Output
4	+5V

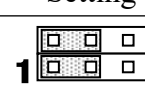
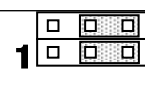
Jumper Settings

*Means default setting

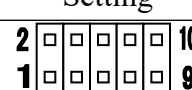
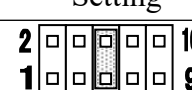
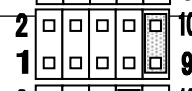
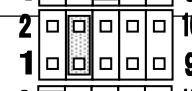
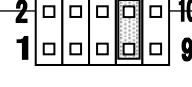
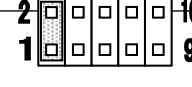
JP1 – CPU Internal Clock Speed Selectors

Description	Setting	Description	Setting
1.5X/3.5X		4.0X	
2.0X		4.5X	
2.5X		5.0X	
3.0X *		5.5X	



JP2 – DIMM Voltage Selectors

Voltage Selectors	Setting
5V	
* 3.3V	

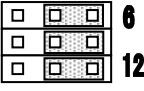
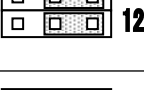
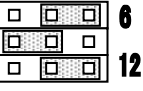
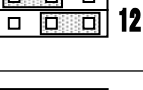
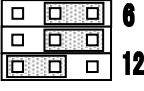
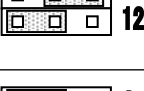
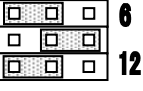
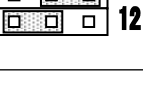
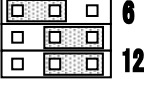
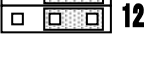
JP3 – CPU Core Voltage Selectors

Core Vcc	Setting	Core Vcc	Setting
2.5V		3.2V	
2.8V		3.3V	
* 2.9V		3.5V	





JP6(A) – PCI Clock Jumper

Description	Setting
* CPU CLK/2	1  3
33 MHz	1  3



JP6(B,C,D) – CPU External Clock Selectors

External Clock	JP6(B,C,D) Settings	External Clock	JP6(B,C,D) Settings
50 MHz	4  6 10  12	* 66.6 MHz	4  6 10  12
55 MHz	4  6 10  12	75 MHz	4  6 10  12
60 MHz	4  6 10  12		

JP7 – CMOS RAM Discharge Selector

Description	Setting
* Normal Mode	3  1 
Clear CMOS	3  1 

JP8 – BIOS Flash ROM Type Selector

Description	Setting
+5V	3  1
+12V *	3  1