

# R527 Jumper Setting

CPU Clock Setting : JP25, JP26, JP27, JP30, JP31, JP8

(A) INTEL Pentium CPU

CPU Type	System Clock	JP25	JP26	JP27	JP30	JP31	JP8
75MHz	50MHz	Open	Short	Short	1-2	1-2	1-2
90MHz	60MHz	Open	Short	Open	1-2	1-2	1-2
100MHz	66MHz	Open	Open	Short	1-2	1-2	1-2
120MHz	60MHz	Open	Short	Open	2-3	1-2	1-2
133MHz	66MHz	Open	Open	Short	2-3	1-2	1-2
150MHz	50MHz	Open	Short	Short	1-2	2-3	1-2
150MHz	60MHz	Open	Short	Open	2-3	2-3	1-2
166MHz	66MHz	Open	Open	Short	2-3	2-3	1-2

(B) Cyrix 6x86 CPU

CPU Type	System Clock	JP25	JP26	JP27	JP30	JP31	JP8
80MHz	40MHz	Short	Open	Open	2-3	1-2	2-3
100MHz	50MHz	Short	Short	Short	2-3	1-2	2-3
120MHz	60MHz	Open	Short	Open	2-3	1-2	2-3
Bus Ratio	JP30	JP31					
X 1.5	1-2	1-2					
X 2	2-3	1-2					
X 2.5	2-3	2-3					
X 3	1-2	2-3					

CPU Voltage Selector (3.3 or 3.45V) : JP38, JP39, JP40

CPU Type Voltage	JP38	JP39	JP40
Standard and VR (3.3V +/- 5%)	Short	Open	Open
VRE (3.45V~3.6V)	Open	Short	Open
Reserved	Open	Open	Short

L1 Write-Back / Write-Through Cache Selector : JP33

<b>L1 Cache</b>	<b>JP33</b>
Write-Back (default)	1-2
Write-Through	2-3

Pipeline Selector : JP32

<b>Address Pipelining</b>	<b>JP32</b>
Disable	1-2
Enable (default)	2-3

Note : Only when the Burst SRAM installed then the Pipeline can be enabled.

Level 2 Cache Options : JP5, JP6

<b>Cache Size</b>	<b>Tag RAM Type</b>	<b>SRAM Type</b>	<b>JP5</b>	<b>JP6</b>
256K (SRAM)	8K/16K/32Kx8	32Kx8 (8)	1-2	1-2
256K (B_RAM)	8K/16K/32Kx8	32Kx32 (2)	1-2	1-2
512K (SRAM)	16K/32Kx8	64Kx8 (8)	2-3	1-2
1M (SRAM)	32Kx8	128Kx8 (8)	2-3	2-3

JP8 : When Burst SRAM Cache on board using only.

1-2 Short --- Intel Pentiums CPU installed.

2-3 Short --- Cyrix 6x86 CPU installed.

SRAM Mode Selector : JP2, JP3 (Factory Setting only)

Note : 3.3V SRAM consists of 3.3V Power and 3.3V I/O interface.

Mixed SRAM consists of 5V Power and 3.3V I/O interface.

<b>SRAM Mode</b>	<b>JP2</b>	<b>JP3</b>
Mixed SRAM	1-2	1-2
3.3V SRAM	2-3	2-3

Flash EPROM Read / Write Selector : JP55

<b>Program Mode</b>	<b>JP55</b>
None Flash Mode (default)	2-3
Flash Mode	1-2

CMOS Clear Jumper : JP36

<b>Function</b>	<b>JP36</b>
Retain CMOS data (default)	1-2
Clear CMOS data	2-3

PS/2 Mouse Port Selector : JP20, JP21, JP22, JP23, JP24

These Jumper controls the on-board PS/2 Mouse lead connector.

When set to Enable, the port is active and using IRQ12

<b>PS/2 Mouse</b>	<b>JP20, JP21, JP22, JP23, JP24</b>
Disable	2-3 Short
Enable	1-2 Short (default)

Monitor Type Jumper : JP45

<b>Operation mode</b>	<b>JP45</b>
For CGA	Short
For Mono or VGA / EGA	Open