

Specification

- ❑ Intel i430VX PCiset™ chipset
- ❑ Supports 75/90/100/120/133/166/180/200 MHz Pentium™ CPUs with 321pin ZIF socket
- ❑ Supports Pentium P55C
- ❑ Supports Cyrix 6x86 and AMD K5 CPUs
- ❑ Uses four 72-pin EDO/Page Mode SIMM modules auto banking in multiple configuration up to 128MB
- ❑ Supports 256K/512K Onboard Pipelined burst synchronous L2 Write Back cache
- ❑ Four PCI Local Bus slots and three 16 bits ISA Bus slots
- ❑ All four PCI slots support Master Mode
- ❑ System BIOS supports four IDE harddisk drives without device driver for S/W application and the capacity of each harddisk can be larger than 528MB up to 8.4GB
- ❑ Onboard PCI Bus Master IDE interface with two connectors supports four IDE devices in 2 channels and the PCI IDE Controller supports PIO Mode 0 to Mode 4 at maximum transfer rate of 16.67MB/s and Bus Master IDE DMA Mode 2
- ❑ Onboard super Multi-I/O chip that supports two serial ports with 16550 Fast UART compatible, one parallel port with EPP and ECP capabilities, and one floppy disk drive interface
- ❑ Supports PS/2™ mouse
- ❑ Supports the Universal Serial Bus (USB) (optional)
- ❑ System BIOS supports NCR810SCSI BIOS firmware, Green feature function, "Plug and Play" Flash ROM.

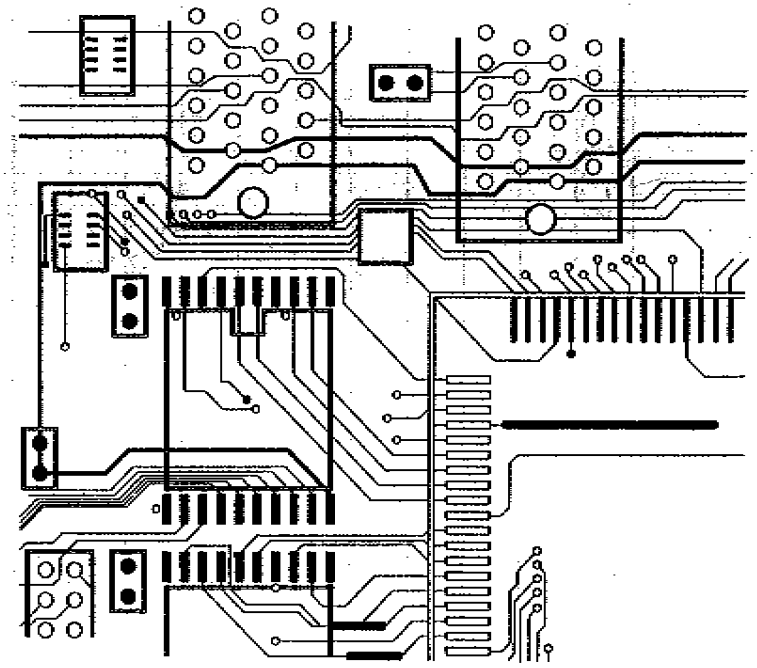
PT-7500-4A01

Pentium Mainboard

PCI Bus and ISA Bus

Supports Pentium P54C 75/90/100/120/133/150/166/180/200 MHz

Processors and Pentium P55C, Cyrix 6x86, and AMD K5 CPUs with PCI IDE & Multi I/O.





User's Manual

Jumper Settings









J7: SPK (Speaker Connector)

Pin	Description
1	Speaker Out
2	Ground
3	Ground
4	5V

JP2: Flash ROM Voltage Jumper





Description	JP2
12 Volt Flash programming	
5 Volt Flash programming	

JP7: CPU Speed Jumpers

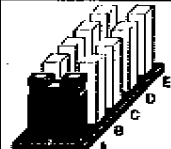





CPU Clock	JP7A	JP7B
50MHz		
55MHz		
60MHz		
66MHz		

Jumper Settings

JP6-F, G: CPU Internal Clock Speed Jumpers

Intel	Cyrix	AMD	JP6-F
1.5X	Reserved	1.5X	
2.0X	2.0X	Reserved	
2.5X	Reserved	Reserved	
3.0X	Reserved	Reserved	

JP6-A, B, C, D, E: CPU Voltage Regulator Output

Setting	Setting
3.5V 	2.8V 
3.3V 	2.7V 
2.9V 	2.5V 


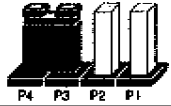
Jumper Settings

J3 Power Supply Connector

Pin	Description	Pin	Description
1	Power Good	7	Ground
2	+5VDC	8	Ground
3	+12VDC	9	-5VDC
4	-12VDC	10	+5VDC
5	Ground	11	+5VDC
6	Ground	12	+5VDC

J5: CMOS RAM Discharge Jumper/External Battery Connector

Pin	Description
1	External Battery Positive
2	Internal Battery Positive
3	Connector to CMOS
4	Ground

Description	JP2
External Battery Mode	Connect an external battery to pins 1-4
Internal Battery Mode	
Discharge CMOS	

Jumper Settings

J7: RST (Reset Switch Connector)

Setting	Description
Open	Normal Mode
Short	Reset System

J7: KEYLOCK (Keylock & Power LED Connector)

Pin	Description
1	LED Output
2	N.C.
3	Ground
4	Keylock
5	Ground

J7: TB LED (Turbo LED Connector)

Pin	Description
1	Anode (+)
2	Cathode (-), Ground

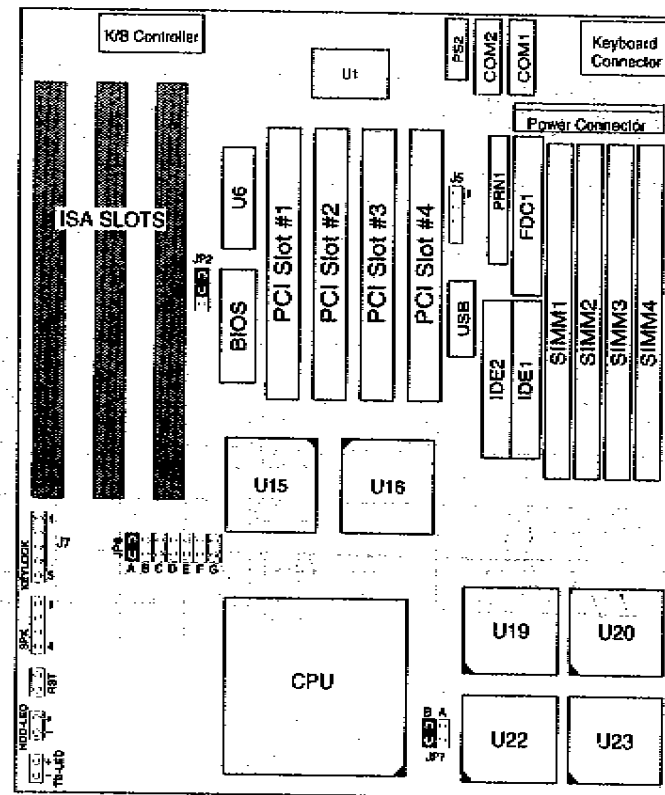
J7: HDD LED (Hard Disk LED Connector)

Pin	Description
1	Anode (+)
2	Cathode (-), Ground

Quick Installation Guide

1. Set J5 to CMOS RAM discharge jumper (pin 2-3)
2. Set JP7 to select CPU speed
3. Set JP6-F, G to select CPU Internal Clock Speed
4. Set JP6-A, B, C, D, E to select CPU Voltage Regulator Output
5. Insert CPU to CPU socket
6. Insert 72-pin SIMM module into SIMM1-4
7. Install mainboard into system chassis
8. Connect keyboard to J1
9. Insert the display card and other peripheral cards (if required) onto the mainboard
10. Connect hard disk(s) to IDE primary/secondary connector(s)
11. Connect floppy drive(s) to FDC1 connector
12. Connect serial ports to COM1 and COM2 connectors
13. Connect parallel port to PRN1 connector
14. Connect J7-HDD LED to "Hard Disk Busy" LED on the system chassis
15. Connect J7-TB LED to Turbo LED on the system chassis
16. Connect J7-RST to Reset Switch on the system chassis
17. Connect J7-SPK to Speaker on the system chassis
18. Connect J7-KEYLOCK to keylock and power LED on the system chassis
19. Close system chassis, connect all external cables to your computer.

Jumper Settings



J1 Keyboard Connector

A standard five-pin female DIN keyboard connector is located at the rear of the board J1.

Pin	Description
1	Keyboard Clock
2	Keyboard Data
3	N.C.
4	Ground
5	+5VDC