

PH4500AU R00 M/B JUMPER SETTING

PART A:

DATE:01/25/1995

1. CPU BARND SELECT:

	JP4
INTEL	2-4,1-3
CYRIX	4-6,1-3
AMD/UMC	2-4,3-5

2. CPU SELECT:

	JP27	JP14	JP13	JP15	JP16	JP18	JP10	JP17
SL DX/2/4, SX2	2-3,4-5	1-2,3-4	1-2	1-2	OPEN	3-4	1-2	OPEN
SL SX	2-3,4-5	2-3	1-2	1-2	OPEN	OPEN	1-2	OPEN
CX486DX	1-2,3-4	1-2,3-4	2-3,4-5	1-2	2-3,4-5	3-4	1-2	OPEN
UMC486SX	OPEN	2-3	5-6	2-3	OPEN	1-2	2-3	OPEN
486DX/DX2	OPEN	1-2,3-4	OPEN	OPEN	OPEN	3-4	2-3	OPEN
486SX (PGA)	OPEN	2-3	OPEN	OPEN	OPEN	OPEN	2-3	OPEN
P24T	2-3,4-5	1-2,3-4	1-2	1-2	OPEN	2-3	2-3	OPEN
P24D	2-3,4-5	1-2,3-4	1-2,3-4	1-2	1-2,3-4	3-4	2-3	2-3
AMD486DX2	OPEN	1-2,3-4	OPEN	OPEN	OPEN	3-4	2-3	1-2
AMD486DX4	OPEN	1-2,3-4	OPEN	OPEN	OPEN	3-4	2-3	OPEN

3. SPECIAL JUMPER FOR P24C (DX4), CYRIX, AMD x5

INTEL P24C CPU CLK SELECT:

JP11:	OPEN	3X	CPU CLOCK
	1-2	2.5X	CPU CLOCK
	2-3	2X	CPU CLOCK

CYRIX 486DX CLK MODE SELECTION

JP17:	3-4	2X	CLOCK MODE
	OPEN	1X	CLOCK MODE

AMD 5x86 / CYRIX 5x86 CPU CLK MODE SELECTION:

JP11:	2-3	2X	CPU CLOCK
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4. CPU VCC SELECT:

	JP20	JP21	JP22	JP26	JP25	JP24	JP23	CPU
5V	1-2	1-2	1-2	ON	-	-	-	NORMAL
3.3V	2-3	2-3	2-3	ON	-	-	-	P24C,AMD DX2/4
3.45V	2-3	2-3	2-3	-	ON	-	-	P24C,AMD DX2/4
3.6V	2-3	2-3	2-3	-	-	ON	-	CYRIX DX2-66
4.0V <sup>3</sup>	2-3	2-3	2-3	-	-	-	ON	CYRIX DX2-80

NOTE: P24C & AMD DX2/4 RUNS 3.3V OR 3.45V DEPEND ON CPU VERSION

5. CACHE SIZE SELECT

CACHE SIZE	DATA RAM	TAG RAM	JP6	JP7	JP5	JP8	JP9	JP12
128K	32K*4	32K	OFF	OFF	OFF	OFF	2-3	OFF
256K	32K*8	32K	ON	OFF	OFF	OFF	1-2	OFF
256K	64K*4	64K	ON	OFF	OFF	OFF	2-3	1-2
512K	64K*8	64K	ON	ON	OFF	OFF	1-2	2-3
512K	128K*4	128K	ON	ON	OFF	1-2	2-3	1-2
1024K	128K*8	128K	ON	ON	ON	2-3	1-2	2-3

6. CPU SPEED SELECT

CLK	JP19	JP28	JP30
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25M	ON	OFF	OFF
33M	ON	ON	ON
40M	ON	ON	OFF
50M	OFF	OFF	ON

7. ECP DMA CHANNEL SELECTOR

DMA CHANNEL	JP1	JP2
DRQ1/DACK1	1-2	1-2
DRQ3/DACK3	2-3	2-3