DTK
PEM-0039N

Processor	80386DX/CX486DLC
Processor Speed	25/33/40/MHz
Chip Set	UNI
Max. Onboard DRAM	128MB
Cache	32/64/128KB
BIOS	AMI
Dimensions	220mm x 175mm
I/O Options	None
NPU Options	80387DX
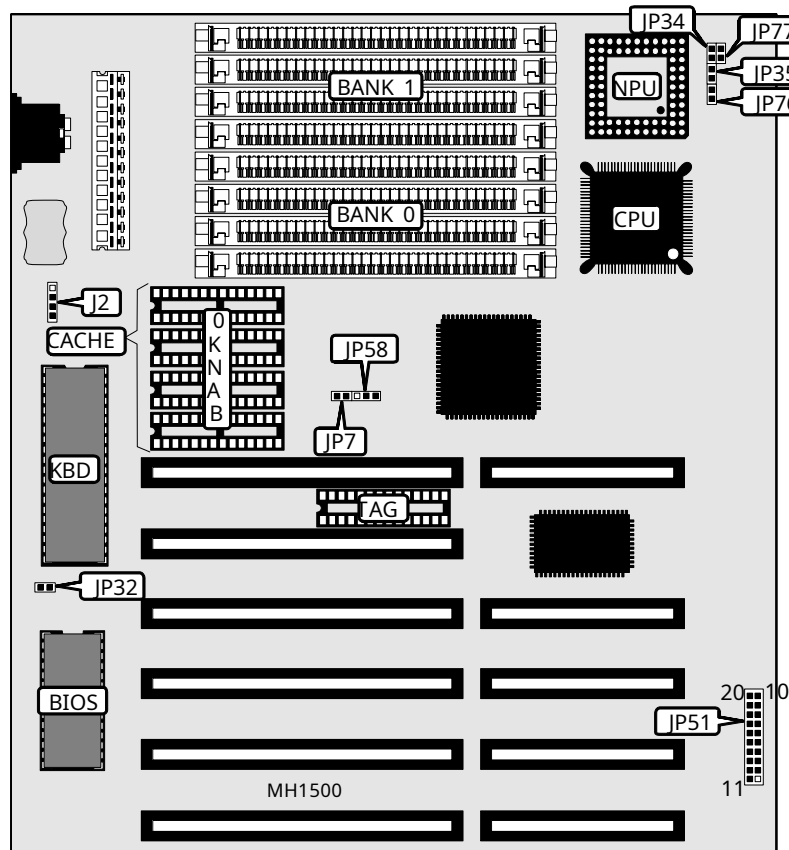
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	JP51 pins 19-20
Power LED & keylock	JP51 pins 1-5	Turbo LED	JP51 pins 12-13
Speaker	JP51 pins 7-10		

Continued on next page . . .

DTK
PEM-0039N

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation (Internal Battery)	J2	pins 2 & 3 closed
CMOS memory normal operation (External Battery)	J2	pins 1, 2, 3, & 4 closed
CMOS memory clear	J2	pins 3 & 4 closed
í Turbo mode disabled	JP51	pins 15 & 16 closed
Turbo mode enabled	JP51	pins 16 & 17 closed
í Monitor type select monochrome	JP32	Open
Monitor type select color	JP32	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
80MB	(4) 4M x 9	(4) 16M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
32KB	(4) 8K x 8	(1) 8K x 8
64KB	(4) 16K x 8	(1) 8K x 8
128KB	(4) 32K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION		
Size	JP7	JP58
32KB	Open	pins 1 & 2 closed
64KB	Closed	pins 1 & 2 closed
128KB	Closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION (PGA)		
Speed	JP34	JP35
25MHz	Open	Closed
33MHz	Closed	Open
40MHz	Open	Open

DTK
PEM-0039N

CPU SPEED CONFIGURATION (PQFP)		
Speed	JP76	JP77
25MHz	Open	Open
33MHz	Open	Closed
40MHz	Closed	Closed