



## MANUAIS DE MOTHERBOARDS JBOND

PCI600D



- **Slot 1 for Dual Intel Pentium II**
- **Intel 82440LX Chipset.**
- **ITE IT8679F-A Multi I/O Chip.**
- **Dimensions: 30.5cm x 21.5cm (12 inches x 8.5 inches) ATX form.**
- Award PnP PCI flash BIOS.
- Three 168-pin DIMM sockets.
- Two Enhance IDE sockets (up to four IDE devices) support fast ATA-2, ATAPI, and Ultra DMA/33 functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, 1.44M, and 2.88M bytes.
- One AGP slot supports x1 and x2 AGP card.
- Four PCI slots. (PCI spec. V2.1)
- Two ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).
- **System hardware health monitor.**
- **(Optional) Adaptec AIC-7880 Ultra Wide SCSI controller.**

### 1. CPU jumper settings

- **CPU System Speed Settings is on BIOS setup. Please read menu.**

Intel	JP1	JP1	JP1	JP1
Pentium II (66MHz)	PIN 1	PIN 2	PIN 3	PIN 4
233MHz	ON	NC	NC	ON
266MHz	ON	ON	ON	NC
300MHz	ON	NC	ON	NC
333MHz	ON	ON	NC	NC
366MHz	ON	NC	NC	NC
400MHz	NC	ON	ON	ON
433MHz	NC	NC	ON	ON
466MHz	NC	ON	NC	ON
500MHz	NC	NC	NC	ON
533MHz	NC	ON	ON	NC
Intel	JP1	JP1	JP1	JP1
Celeron	PIN 1	PIN 2	PIN 3	PIN 4
233MHz	ON	NC	NC	ON
266MHz	ON	ON	ON	NC
300MHz	ON	NC	ON	NC
333MHz	ON	ON	NC	NC
366MHz	ON	NC	NC	NC
400MHz	NC	ON	ON	ON
433MHz	NC	NC	ON	ON
466MHz	NC	ON	NC	ON
500MHz	NC	NC	NC	ON
533MHz	NC	ON	ON	NC

#### Note:

- ON - jumper block short
- NC- jumper block open

### 2. Clear CMOS Data Jumper Settings

Operating Mode	J20
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Normal Operation (default)	Short 1-2
Clear CMOS Data	Short 2-3 while computer powered <b>OFF</b>

**3. Support DIMM model list**

- Each DIMM socket supports 2M to128M bytes DIMM module.
- Vcc provides 3.3v
- Support SDRAM-II, SDRAM and EDO DIMM modules.

**4. Support Year 2000 Compliance**

- BIOS version 1.00 supports Year 2000 compliance.

**5. Support LS-120 Zip Driver Boot Function**