MANUAIS DE MOTHERBOARDS JBOND

PCI500C-H4 Ver 1.4

(Ver 1.4, voltage jumper settings blocks are four)

- Socket 7.
- Intel 82430VX chipset.
- Winbond W83877F Multi I/O chip.
- Dimensions: 8.7x11.3 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 512K bytes L2 SRAM cache.
- Two 168-pin DIMM sockets.
- Four 72-pin SIMM sockets.
- Two Enchance IDE sockets (up to four IDE devices) support fast ATA-2 and ATAPI functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, 1.44M, and 2.88M bytes.
- Four PCI slots. (PCI spec. V2.1)
- Four ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).

1. CPU Jumper Settings

JP3	**PIN 3** **PIN 2** **PIN 1**
JP2	**PIN 3** **PIN 2** **PIN 1**
JP1	**PIN 4** **PIN 3** **PIN 2** **PIN 1**

Intel **w/o MMX technology**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
75MHz	ON	ON	NC	ON	NC	NC	NC	ON	ON	ON
90MHz	ON	ON	NC	ON	NC	NC	NC	ON	ON	NC
100MHz	ON	ON	NC	ON	NC	NC	NC	ON	NC	ON
120MHz	ON	ON	NC	ON	ON	NC	NC	ON	ON	NC
133MHz	ON	ON	NC	ON	ON	NC	NC	ON	NC	ON
150MHz	ON	ON	NC	ON	ON	ON	NC	ON	ON	NC
166MHz	ON	ON	NC	ON	ON	ON	NC	ON	NC	ON
180MHz	ON	ON	NC	ON	NC	ON	NC	ON	ON	NC
200MHz	ON	ON	NC	ON	NC	ON	NC	ON	NC	ON
Intel **w/ MMX technology**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
150MHz	NC	NC	NC	ON	ON	ON	NC	ON	ON	NC
166MHz	NC	NC	NC	ON	ON	ON	NC	ON	NC	ON
180MHz	NC	NC	NC	ON	NC	ON	NC	ON	ON	NC
200MHz	NC	NC	NC	ON	NC	ON	NC	ON	NC	ON
233MHz	NC	NC	NC	ON	NC	NC	NC	ON	NC	ON
AMD **K5**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
K5-PR75	ON	ON	ON	ON	NC	NC	NC	ON	ON	ON
K5-PR90	ON	ON	ON	ON	NC	NC	NC	ON	ON	NC
K5-PR100	ON	ON	ON	ON	NC	NC	NC	ON	NC	ON

K5-PR120	ON	ON	ON	ON	ON	NC	NC	ON	ON	NC
K5-PR133	ON	ON	ON	ON	ON	NC	NC	ON	NC	ON
K5-PR166	ON	ON	ON	ON	ON	ON	NC	ON	NC	ON
AMD **K6**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
K6-PR166 (2.9v)	NC	NC	ON	ON	ON	ON	NC	ON	NC	ON
K6-PR200 (2.9v)	NC	NC	ON	ON	NC	ON	NC	ON	NC	ON
K6-PR200 (2.2v)	NC	ON	NC	NC	NC	ON	NC	ON	NC	ON
K6-PR233 (3.2v)	ON	NC	NC	ON	NC	NC	NC	ON	NC	ON
K6-PR233 (2.2v)	NC	ON	NC	NC	NC	NC	NC	ON	NC	ON
K6-PR266 (2.2v)	NC	ON	NC	NC	ON	NC	ON	ON	NC	ON
K6-PR300 (2.2v)	NC	ON	NC	NC	ON	ON	ON	ON	NC	ON
Cyrix / IBM **6x86**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
PR120+GP(50x2)	ON	ON	ON	ON	ON	NC	NC	ON	ON	ON
PR133+GP(55x2)	ON	ON	ON	ON	ON	NC	NC	NC	ON	ON
PR150+GP(60x2)	ON	ON	ON	ON	ON	NC	NC	ON	ON	NC
PR166+GP(66x2)	ON	ON	ON	ON	ON	NC	NC	ON	NC	ON
PR200+GP(75x2)	ON	ON	ON	ON	ON	NC	NC	NC	ON	NC
Cyrix / IBM **6x86L**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
PR166+GP(66x2)	NC	NC	ON	ON	ON	NC	NC	ON	NC	ON
PR200+GP(75x2)	NC	NC	NC	ON	ON	NC	NC	NC	ON	NC
Cyrix / IBM **6x86 MX**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
MX-PR166GP(66x2)	NC	NC	ON	ON	ON	NC	NC	ON	NC	ON
MX-PR166GP(60X2.5)	NC	NC	ON	ON	ON	ON	NC	ON	ON	NC
MX-PR200GP(75x2)	NC	NC	ON	ON	ON	NC	NC	NC	ON	NC
MX-PR200GP(66x2.5)	NC	NC	ON	ON	ON	ON	NC	ON	NC	ON
MX-PR233GP(75x2.5)	NC	NC	ON	ON	ON	ON	NC	NC	ON	NC
MX-PR233GP(66x3)	NC	NC	ON	ON	NC	ON	NC	ON	NC	ON
Cyrix / IBM **MII**	**JP1** **PIN 1**	**JP1** **PIN 2**	**JP1** **PIN 3**	**JP1** **PIN 4**	**JP2** **PIN 1**	**JP2** **PIN 2**	**JP2** **PIN 3**	**JP3** **PIN 1**	**JP3** **PIN 2**	**JP3** **PIN 3**
MII 233(75x2.5)	NC	NC	ON	ON	ON	ON	NC	NC	ON	NC
MII 233(66x3)	NC	NC	ON	ON	NC	ON	NC	ON	NC	ON
MII 300(75x3)	NC	NC	ON	ON	NC	ON	NC	NC	ON	NC
MII 300(66x3.5)	NC	NC	ON	ON	NC	NC	NC	ON	NC	ON

Note:

- ON - jumper block short
- NC- jumper block open

2. Clear CMOS Data Jumper Settings

Operating Mode	**JP13**
Normal Operating (default)	Short 1-2
Clear CMOS Data	Short 3-4 while computer power turn **OFF**

3. Support DIMM Module List

- Each DIMM socket supports 4M to 32M bytes DIMM module.
- Vcc provides 3.3v and 5.0v
- Support 4-clock SDRAM and EDO DIMM modules.

4. Support SIMM Module List

- Each SIMM socket supports 1M to 32M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

5. Support Year 2000 Compliance

- BIOS version 1.40 or later supports Year 2000 compliance.

6. Support LS-120 Zip Driver Boot Function

7. Support SCSI/CD-ROM Function

8. Support Ultra DMA/33 Function?

- Intel 82430VX chipset **does not** support Ultra DMA/33 function.