

#### **MANUAIS DE MOTHERBOARDS JBOND**

#### PCI500C-E



- Socket 7.
- Intel 82430FX chipset.
- Winbond W83787F Multi I/O chip.
- Dimensions: 8.7x13 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 256K bytes L2 SRAM cache.
- One 256K SRAM module socket.
- Four 72-pin SIMM sockets.
- Two Enchance IDE sockets (up to four IDE devices) support fast ATA-2 and ATAPI functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, and 1.44M bytes.
- Four PCI slots. (PCI spec. V2.0)
- Four ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).
- (Optional) NCR 53C810 SCSI controllor.

### 1. CPU Jumper Settings

### FOR <MX8325(U9) model>

Intel	l						
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
w/o MMX technology						ļ	
75MHz	NC	NC	NC	NC	ON	ON	1-2
90MHz	NC	NC	NC	ON	NC	ON	2-3
100MHz	NC	NC	NC	ON	ON	ON	2-3
120MHz	NC	ON	NC	ON	NC	ON	2-3
133MHz	NC	ON	NC	ON	ON	ON	2-3
150MHz	NC	ON	ON	ON	NC	ON	2-3
166MHz	NC	ON	ON	ON	ON	ON	2-3
180MHz	NC	NC	ON	ON	NC	ON	2-3
200MHz	NC	NC	ON	ON	ON	ON	2-3
AMD							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
К5							
K5-PR75	NC	NC	NC	NC	ON	ON	1-2
K5-PR90	NC	NC	NC	ON	NC	ON	2-3
K5-PR100	NC	NC	NC	ON	ON	ON	2-3
K5-PR120	NC	ON	NC	ON	NC	ON	2-3
K5-PR133	NC	ON	NC	ON	ON	ON	2-3
K5-PR166	NC	ON	ON	ON	ON	ON	2-3
Cyrix / IBM							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
6x86							
PR120+GP(50x2)	NC	NC	NC	NC	ON	ON	1-2
PR150+GP(60x2)	NC	NC	NC	ON	NC	ON	2-3
PR166+GP(66x2)	NC	ON	NC	ON	ON	ON	2-3

### FOR <IMI464(U8) model>

Intel	JP1	JP2	JP3	JP7	JP8	JP9	JP10
w/o MMX technology	J	J		5. 5	5. 0	55	51.20
75MHz	NC	NC	NC	NC	NC	ON	1-2
90MHz	NC	NC	NC	ON	NC	ON	2-3
100MHz	NC	NC	NC	ON	ON	ON	2-3

120MHz	NC	ON	NC	ON	NC	ON	2-3
133MHz	NC	ON	NC	ON	ON	ON	2-3
150MHz	NC	ON	ON	ON	NC	ON	2-3
166MHz	NC	ON	ON	ON	ON	ON	2-3
180MHz	NC	NC	ON	ON	NC	ON	2-3
200MHz	NC	NC	ON	ON	ON	ON	2-3
AMD							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
K5							
K5-PR75	NC	NC	NC	NC	NC	ON	1-2
K5-PR90	NC	NC	NC	ON	NC	ON	2-3
K5-PR100	NC	NC	NC	ON	ON	ON	2-3
K5-PR120	NC	ON	NC	ON	NC	ON	2-3
K5-PR133	NC	ON	NC	ON	ON	ON	2-3
K5-PR166	NC	ON	ON	ON	ON	ON	2-3
Cyrix / IBM							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
6x86							
PR120+GP(50x2)	NC	NC	NC	NC	NC	ON	1-2
PR150+GP(60x2)	NC	NC	NC	ON	NC	ON	2-3
PR166+GP(66x2)	NC	ON	NC	ON	ON	ON	2-3

# FOR <IMI484(U8) model>

Intel							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
w/o MMX technology							
75MHz	NC	NC	NC	ON	NC	NC	1-2
90MHz	NC	NC	NC	ON	NC	ON	2-3
100MHz	NC	NC	NC	ON	ON	ON	2-3
120MHz	NC	ON	NC	ON	NC	ON	2-3
133MHz	NC	ON	NC	ON	ON	ON	2-3
150MHz	NC	ON	ON	ON	NC	ON	2-3
166MHz	NC	ON	ON	ON	ON	ON	2-3
180MHz	NC	NC	ON	ON	NC	ON	2-3
200MHz	NC	NC	ON	ON	ON	ON	2-3
AMD							
	JP1	JP2	JP3	JP7	JP8	JP9	JP10
K5							
K5-PR75	NC	NC	NC	ON	NC	NC	1-2
K5-PR90	NC	NC	NC	ON	NC	ON	2-3
K5-PR100	NC	NC	NC	ON	ON	ON	2-3
K5-PR120	NC	ON	NC	ON	NC	ON	2-3
K5-PR133	NC	ON	NC	ON	ON	ON	2-3
K5-PR166	NC	ON	ON	ON	ON	ON	2-3
Cyrix / IBM							
•	JP1	JP2	JP3	JP7	JP8	JP9	JP10
6x86							
PR120+GP(50x2)	NC	NC	NC	ON	NC	NC	1-2
PR150+GP(60x2)	NC	NC	NC	ON	NC	ON	2-3
PR166+GP(66x2)	NC	ON	NC	ON	ON	ON	2-3

# Note:

- ON jumper block short
- NC- jumper block open

# 2. Clear CMOS Data Jumper Settings

Operating Mode	JP17
Normal Operating (default)	Short 1-2
Clear CMOS Data	Short 2-3 while computer power turn <b>OFF</b>

# 3. External Cache Memory Jumper Settings

Cache Size	Data SRAM	Tag SRAM	JP12	JP13	JP14	JP15
256K	Eight 32Kx8	8Kx8	ON	ON	NC	NC
512K	Eight 32Kx8	8Kx8	ON	NC	ON	ON

# 4. Support SIMM Module List

• Each SIMM socket supports 1M to 32M bytes SIMM module.

- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

# 5. On-board SCSI Jumper Settings

On-board SCSI	JP21	JP22	JP23
Enable	2-3	2-3	2-3
Disable	1-2	1-2	NC

# 6. On-board SCSI Terminator Power Source Jumper Settings

Operating Mode	JP25
System (Default)	ON
Peripheral	NC

# 7. Support Year 2000 Compliance

• BIOS version 1.60S and 1.60N support Year 2000 compliance.

# 8. Support LS-120 Zip Driver Boot Function

# 6. Support SCSI/CD-ROM Function