



MANUAIS DE MOTHERBOARDS JBOND

PCI500C-C



- **Socket 5.**
- **SIS 85C501/85C502/85C503 chipset.**
- Dimensions: 8.7x13 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- Two banks SRAM sockets.
- Four 72-pin SIMM sockets.
- Four PCI slots. (PCI spec. V2.0)
- Four ISA slots. (1 PCI/ISA shared slot)
- **(Optional) NCR 53C810PT SCSI controller.**

1. CPU Jumper Settings

FOR <MX8318(U35) model>

	JP7	JP8	JP9	JP22
75MHz	ON	NC	NC	NC
90MHz	NC	ON	NC	NC
100MHz	ON	NC	ON	NC
120MHz	NC	ON	NC	ON
133MHz	ON	NC	ON	ON

FOR <IMISC466(U36) model>

	JP7	JP8	JP9	JP22
75MHz	NC	NC	ON	NC
90MHz	ON	NC	ON	NC
100MHz	ON	ON	ON	NC
120MHz	ON	NC	ON	ON
133MHz	ON	ON	ON	ON

Note:

- ON - jumper block short
- NC- jumper block open

2. Clear CMOS Data Jumper Settings

Operating Mode	JP16
Normal Operating (default)	Open
Clear CMOS Data	Short 1-2 or 2-3 while computer power turn OFF

3. External Cache Memory Jumper Settings

Cache Size	Data SRAM	Tag SRAM	JP1	JP2	JP3	JP4	JP5	JP6
256K	Eight 32Kx8	8Kx8 or 32Kx8	1-2	1-2	1-2	1-2	1-2, 3-4	NC
512K	Sixteen 32Kx8	16Kx8 or 32Kx8	2-3	1-2	1-2	2-3	2-3, 4-5	NC
512K	Eight 32Kx8	16Kx8 or 32Kx8	2-3	1-2	1-2	1-2	1-2, 3-4	1-2
1M	Sixteen 64Kx8	32Kx8	2-3	2-3	1-2	2-3	2-3, 4-5	2-3
1M	Eight 128Kx8	32Kx8	2-3	2-3	1-2	1-2	1-2, 3-4	2-3
2M	Sixteen 128Kx8	64Kx8	2-3	2-3	2-3	2-3	2-3, 4-5	2-3

4. Support SIMM Module List

- Each SIMM socket supports 1M to 32M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

5. Power-saving Mode Control Jumper

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Power-saving Controlled	JP10	JP23
STP. CLK	1-2	2-3

The Input clock to the CPU in Power-saving Mode	JP21
33MHz	Open (default)
16MHz	2-3
Reserve for testing	1-2

6. CPU Pipeline Function Enable Jumper

Pipeline Function	JP14
Enable	2-3
Disable	1-2

7. CPU Internal Cache Mode Selection Jumper

Cache Mode	JP14
Write-back	2-3
Disable	1-2

8. CPU Internal Cache Data Coherency Jumper

Internal Cache Status	JP15
Always Invalid in Bus Master	1-2
Invalid only in Bus Master Write Cycle	2-3

9. Local Bus Master Selection Jumper

Local Bus Master	JP30	JP31
PCI Slot 4	1-2	1-2
On-board (Default)	2-3	2-3

10. On-board SCSI Jumper Settings

On-board SCSI	JP34	JP35	JP29	SCSI Interrupt Route
Enable	NC	ON	1-2	IRQ 9 (Default)
Enable	NC	NC	2-3	PCI Slot 4 INT A
Disable	ON	NC	X	Reserve IRQ 9

11. On-board SCSI Terminator Power Source Jumper Settings

Operating Mode	JP32
System (Default)	ON
Peripheral	NC

12. DRAM Jumper

DRAM	Bank 0 (SIMM 3 & 4)	Bank 1 (SIMM 1 & 2)
2-3/2-3 (Default)	Single X2	None
	Single X2	Single X2
	Double X2	Single X2
	Double X2	Double X2
1-2/1-2	Single X2	None
	Double X2	None
	Double X2	Single X2
	Double X2	Double X2

13. BIOS Update Jumper

Operating Mode	JP32
Update BIOS	1-2, 5-6
Normal Mode	2-3, 4-5