http://pcchips.com/m70



FEB

APR

SEP







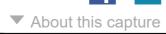
4 captures

14 Feb 1998 - 13 Oct 1999



25)

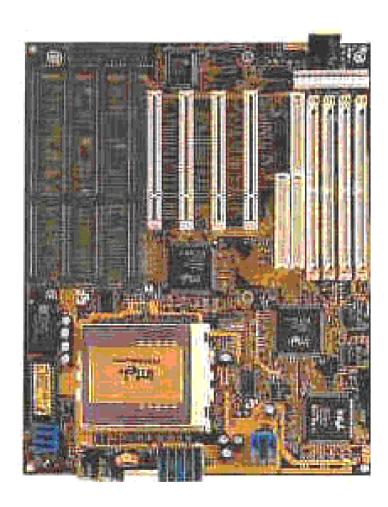
97 **1998 1999**

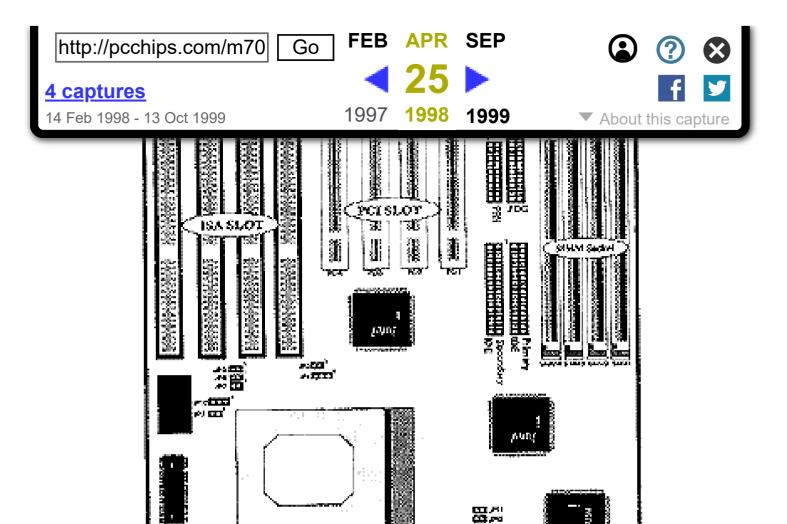


M701

JUMPER SETTINGS

{ Data from M701 manual }



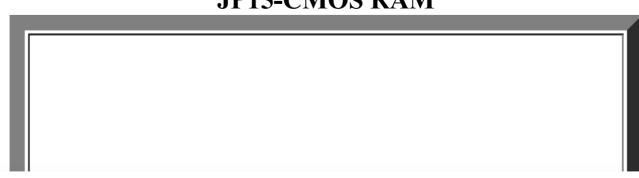


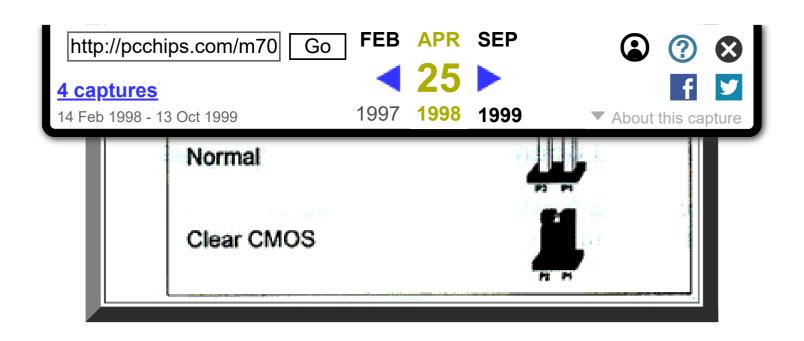
{ Data from M701 manual }

W BENG

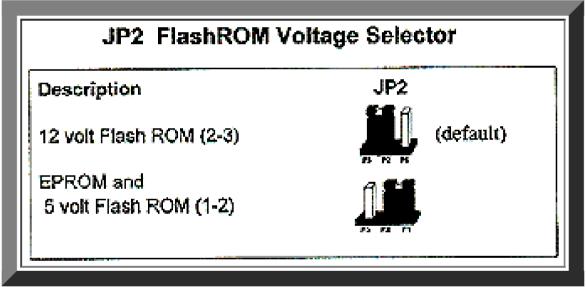
M701 Mainboard

JP15-CMOS RAM



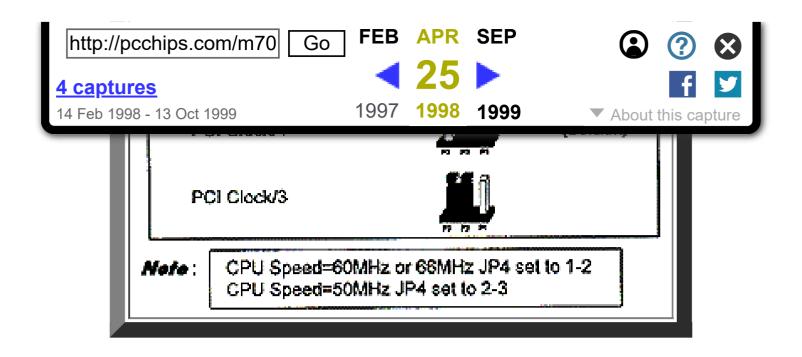


JP2 -FlashROM Voltage Selectors

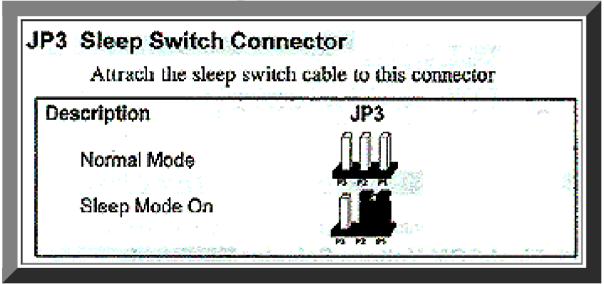


JP4 - AT Bus Clock Selectors





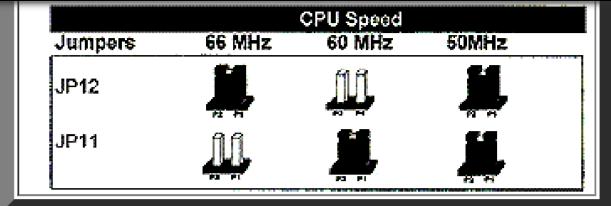
JP3 Sleep Switch Connector



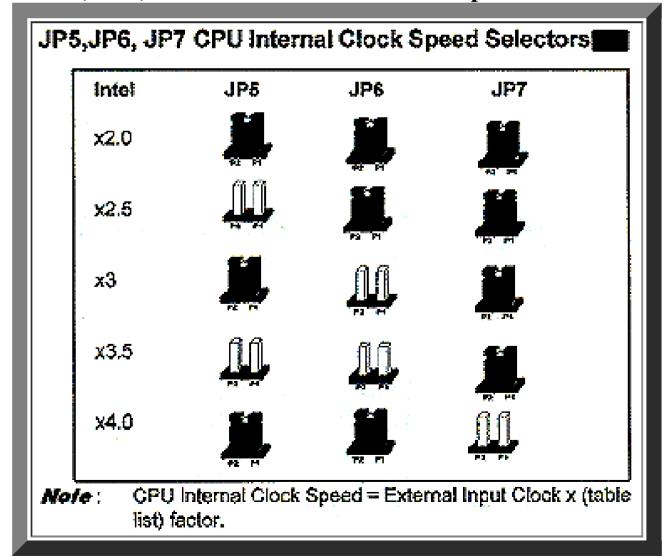
JP11, JP12 - CPU Speed Selector







JP5, JP6, JP7 - CPU Internal Clock Speed Selectors





[M701 Specification][M701 BIOS][M701 JUMPER]

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