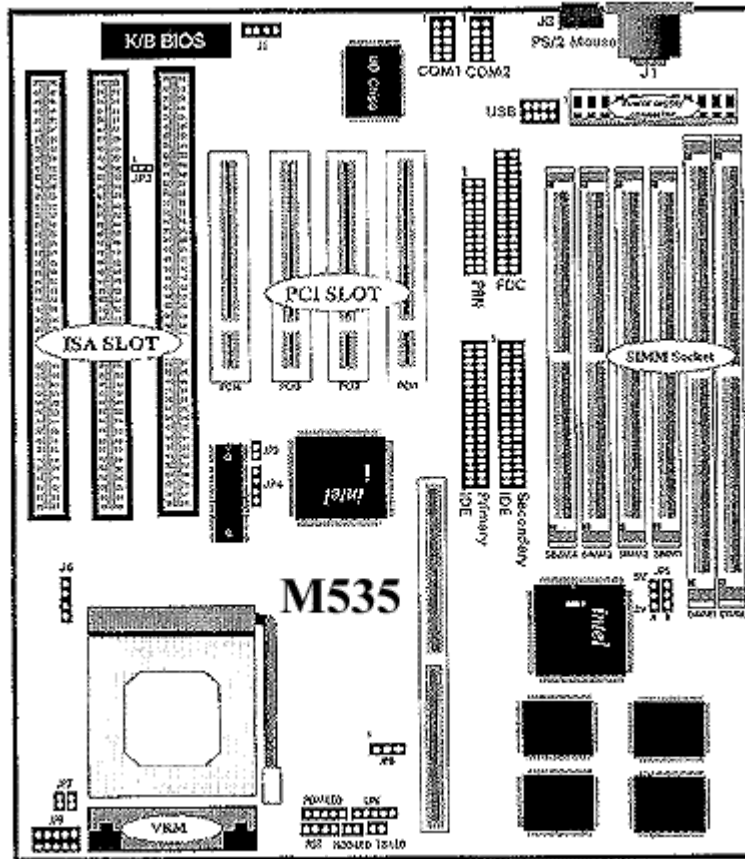


M535



M535 Mainboard

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USB1 Universal Serial Bus Connector

Pin	Description	Pin	Description
1	+5 VDC	1	+5VDC
2	DATA -	2	DATA -
3	DATA +	3	DATA +
4	Ground	4	Ground
5	N.C	5	N.C

J8 HDD-LED HDD LED Connector

Pin	Description
1	5V
2	Active Low

J8 RST - Reset Switch Connector

Attach the Reset switch cable to this connector. M535_p10

Setting	Description
Open	Normal Mode
Short	Reset System

JP1- CMOS RAM

Description	JP1
Normal (default)	
Clear CMOS	

J7 Pow-LED Keylock & Power LED Connector

J7 is a keylock connector that enables and disables the keyboard and the Power-LED on the case.

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Pin	Description
1	LED Output
2	NC
3	Ground
4	Keylock
5	Ground



J7 SPK - Speaker Connectors

Attach the system speaker to connector J7.



Pin	Description
1	DATA Out
2	NC
3	Ground
4	+5V

JP3 FlashROM Voltage Selector

The mainboard can use two types of Flash ROM - 5 volt and 12 volt. Set the mainboard for either type with jumper JP3. You can update both types with new BIOS files as they come available.



Description	JP3
12 volt Flash ROM (1-2)	 (default)
EPROM and 5 volt Flash ROM (2-3)	

JP2 AT Bus Clock Selector



Description	JP2
PCI Clock/4 (1-2) (default)	 M535_p12
PCI Clock/3 (2-3)	

Note: CPU Speed=60MHz or 66MHz JP2 set to 1-2
CPU Speed=50MHz JP2 set to 2-3

JP4 - Flash ROM Size

Description	JP4
1M (2-3) (default)	
2M (1-2)	

JP5A, JP5B DIMM Socket Voltage Selectors

Voltage Selectors	JP5A,JP5B
3.3V	
5.0V	 (default)

Note: All synchronous DRAM JP5A, JP5B must be set to 3.3V position.

JP6A, JP6B, JP6C - CPU Speed Selectors

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The mainboard has a clock generator that lets you choose the CPU frequency by settings jumpers JP6A, JP6B, JP6C. You can set the CPU speed to 50/60MHz or 66 MHz as shown below.

CPU Speed					
Jumpers	50MHz	55MHz	60 MHz	66 MHz	75MHz
A					
B					
C					

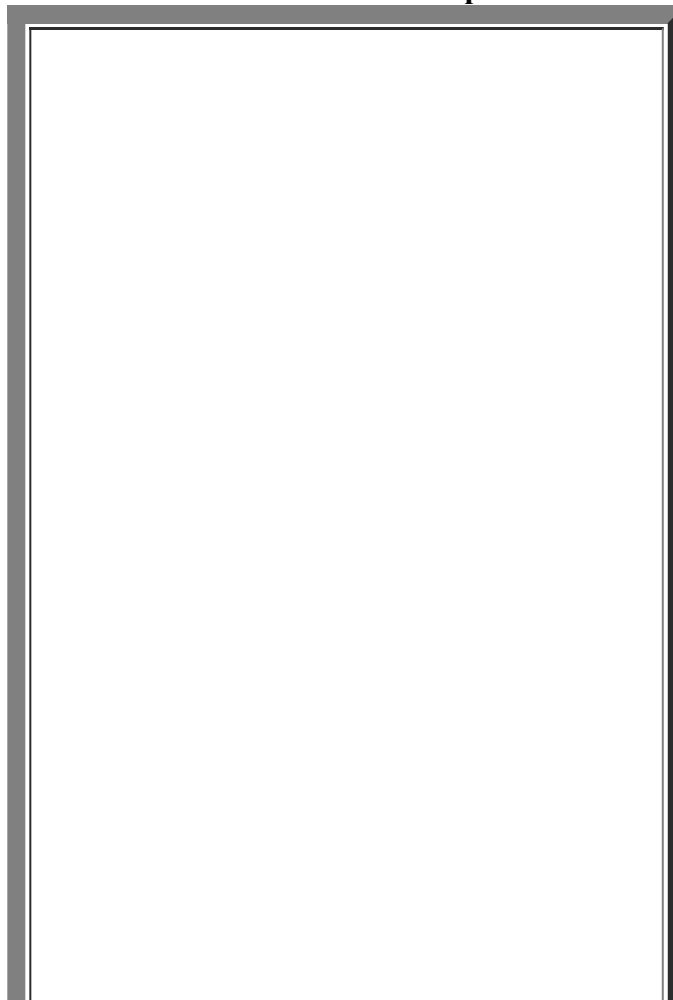
55MHz => A=12, B=12, C=23 (right)

55MHz => A=12, B=23, C=23 (wrong)

JP7A, JP7B - CPU Internal Clock Speed Selectors



Intel	Cyrix	AMD	JP7A	JP7B
x2.0	x2.0	Reserved		
x1.5 (default)	Reserved	x1.5		
x2.5	Reserved	Reserved		
x3.0	Reserved	Reserved		

Note: CPU Internal Clock Speed = External Input Clock x (table list) factor.



JP8 - Cache Size

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Description	JP8
256K (default) (1-2)	
512K (2-3)	






JP10 - RTC Chip Select

Description	JP10
(1-2) (default)	

PS1/J3 - PS/2 Mouse Connector

PS1 PIN	Description	J3 PIN	Description
1	Mouse DATA	1	Musclk
2	NC	2	Ground
3	Ground	3	N.C
4	+5VDC	4	MousDATA
5	Mouse CLK	5	N.C
6	NC	6	N.C
		7	N.C
		8	+5VDC

JP9 - P54C/P55C CPU Voltage Selectors

JP9	3.5V	2.9V	2.8V	2.7V	3.3V
ON	A	B	C	D	E
					
	(default)				

* All open 2.5V

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JP8 - Cache Memory Selectors

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SYN.Cache Size	Onboard U20,U21	On Module J5	U19 (Tag RAM)	JP8 Setting
256K	256K	None	32Kx8	1-2
512K	256K	256K	32Kx8	2-3
512K	512K	None	32Kx8	2-3

Note:

When you have a cache module to plug into a 160-pin dual readout connector. You must make sure that the cache module follow the specification of Intel COAST 3.0 or later version. Please contract the modules supplier to avoid not working properly or damaging any modules circuits.