486 MOTHERBOARD PAT48SA (1992)

- 6 ISA 16-bit slots, 1 ISA 8-bit slot.
- 8 SIMM slots 30pin.
- 85C460 single chip chipset.

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 .-----------JP10-JP11-JP12-J6-J7-------------.
 |              :   ... ...  : :   [-+]  |KB|  |
 |#   #   #   #   #   #   #     ++++++++++     |
 |#   #   #   #   #   #   #  .--------------.  |
 |#   #   #   #   #   #   #  | | | | | | | | | ||
 |#   #   #   #   #   #   #..| |30pin|SIMM | | ||
 |#   #   #   #   #   #   #JP| | | | | | | | | ||
 |#   #   #   #   #   #   #13|B|A|N|K|B|A|N|K| ||
 |                          | | |0| | | |1| | ||
 |.--.#   #   #   #   #   #  | | | | | | | | | ||
 ||BI|#   #   #   #   #   #  | | | | | | | | | ||
 ||OS|#   #   #   #   #   #  `--------------´   |
 |`--´#   #   #   #   #   #                     |
 |                               .-----\       |
 |: : :                          | 85C |       |
 |: : :                          | 460 |       |
 |:RN :        =U10=             `-----´        |
 |: : : =U4== =U9==              RN            |
 |6 1 2 =U3== =U8== .-------\ A B C           |
 |4 2 5 =U2== =U7== | 486   | : : :           |
 |   8 6 =U1== =U6== | CPU   | : : :           |
 |                  |       | : : :           |
 | 1:::::  :        `-------´                   |
 `---J1----J2-----------------------------------´
```

A memory bank must be completely filled with four(4) identical SIMM's,
or not filled at all. Possible memory configurations:

Total	1M	2M	4M	8M	16M	20M	32M
Bank0	256k	256k	1M	1M	4M	1M	4M
Bank1	-	256k	-	1M	-	4M	4M

JP10: Battery
 1-2 Use on board battery. (External battery MUST NOT be connected)
 none Use external battery connexted to J6
 2-3 Clear CMOS RAM. Short for a few seconds with system power off.

JP11: Clock
 2-3 486DX50, 1-2 All others (SX/DX/DX2/Overdrive)

JP13: Video adapter
 Closed Color(CGA/EGA/VGA), Open Monochrome(MDA/HGC)

RNA/RNB/RNC: CPU Type

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(RN=Resistor network, a single-inline 6pin little item)
RN is placed in one, and only one, of the three sockets.
   RNA  486DX, 486DX2
   RNB  487SX, Overdrive
   RNC  486SX

RN64/RN128/RN256, U1..U10 : Secondary cache size
RN is placed in one, and only one, of the three sockets.
   64kB:  RN64, U1..U4 8Kx8,  U6..U10 8Kx8
  128kB: RN128, U1..U4 32Kx8, U10 8Kx8
  256kB: RN256, U1..U4 32Kx8, U6..U10 32Kx8

J1: pins 1........10
         11.......20

J1,1-4:   Speaker. 1=out,2=NC,3=GND,4=+5V
J1,11-15: PowerLED,Keylock. 11=LED,12=NC,13=GND,14=Keyl,15=GND
J1,7,17:  TurboSwitch. 7-17 closed=turbo, open=non-turbo
J1,8,18:  TurboLED. 8=anode, 18=cathode
J1,9,19:  ResetSwitch. 9-19 closed=reset, open=normal

J1 pin 10 and 20 is directly connected to J2 pin 1 and 2. The purpose
is to connect J1 to the harddisk controller card's LED out, and the
front panel HDD LED to J2. But as the manual says, you may also connect
the HDD LED directly to the controller card.

J6 External battery. 1=Vcc,2=NC,3=GND,4=GND


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