

With a 486SLC CPU mounted, populate all SIMM slots for best bandwidth results.

JP7:

open = internal L1 cache enabled

closed = internal L1 cache disabled

JP9:

1+2 = ram parity enabled

2+3 = ram parity disabled

JP5:

1+2 = L2 cache 32k x8

2+3 = L2 cache 8k x8