MB-PAM-0052I PENTIUM MOTHERBOARD LAYOUT AND CONFIGURATION

S=SHORTED 0=OPEN 1-2 = PINS 1 AND 2 SHORTED

CONNECTORS AND JUMPERS:

CONTROL THE SOME LINE !		
POWER SUPPLY CONNECTO 1 = GROUND 2 = GROUND	OR (PW1)	RESET SWITCH CONNECTOR (J1) S = RESET O = NOT RESET
3 = -5 VDC 4 = +5 VDC 5 = +5 VDC 6 = +5 VDC		SPEAKER CONNECTOR (J2) 1 = DATA OUT 2 = NOT USED 3 = GROUND
1 = POWER GOO 2 = +5 VDC 3 = +12 VDC 4 = -12 VDC 5 = GROUND 6 = GROUND	OD	4 = GROUND KEYLOCK CONNECTOR (J3) 1 = LED POWER 2 = NOT USED 3 = GROUND
KEYBOARD INHIBITOR		4 =
KEYBOARD CONNECTOR (R 1 = KEYBOARD 2 = KEYBOARD 3 = SPARE 4 = GROUND 5 = +5 VDC	CLOCK	5 = GROUND TURBO LED CONNECTOR (JP18) 1 = +ANODE 2 = -CATHODE
PCI BUS INTERRUPT SET 1-2 JP4 IRQ14 JP5 IRQ15 JP6 IRQ12 JP7 IRQ5	2_2	
CPU CLOCK SELECTION W 1-2 75 MHZ S 90 MHZ O 100 MHZ S	VITH MX8315 (JP8) 3-4 5-6 0 0 S 0 0 S	
CPU CLOCK SETTING WIT 1-2 75 MHZ S 90 MHZ S	TH IMI466 (JP8) 3-4 5-6 0 0 0 S	

100 MHZ S S S

CPU CLOCK SETTING WITH ADV9107-03 (JP8)

1-2 3-4 5-6 75 MHZ S O S 90 MHZ O S O 100 MHZ S S O

CACHE SIZE AND JUMPER SETTINGS (JP10, JP13)

	256K	512K
SRAM	32K*8	64K*8
JP10	1-2	2-3
JP13	2-3	2-3

MEMORY INSTALLATION:

THIS MOTHERBOARD IS EQUIPPED WITH THREE BANKS OF TWO SIMM SOCKETS EACH. THE USER CAN INSTALL EITHER 256K, 1/4/16MB 72 PIN SIMMS. THE SPEED OF THE SIMMS MUST BE AT LEAST 70ns OR FASTER.