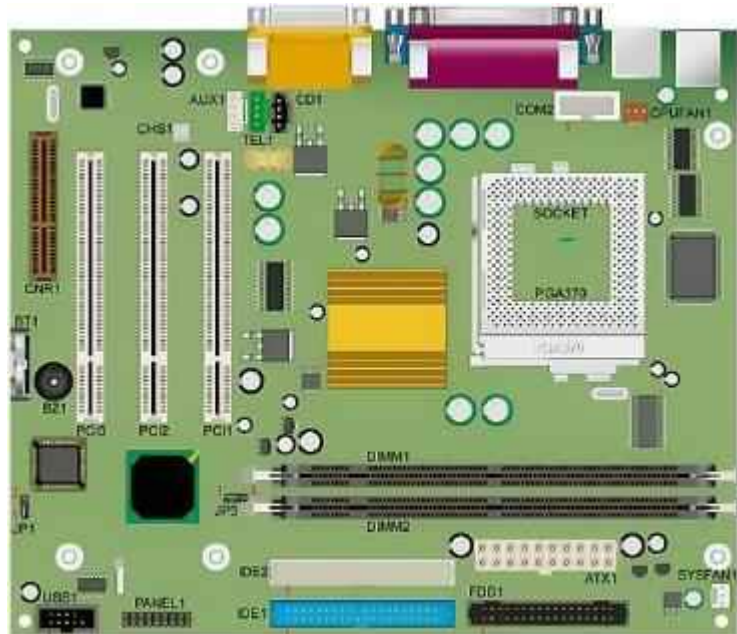


Packard Bell Challenger M/Board



Specification

- **Audio:** SigmaTel STAC9721 - audio codec
- **Bios:** AMI
- **Chipset :** Intel 810E2 chipset consisting of the 82810E Graphics and AGP Memory Controller Hub (GMCH) and the 82801BA/M I/O Controller Hub (ICH2)(-M).
- **CPU Support :**
 - Supports Intel Celeron FC-PGA (Flip-Chip Pin Grid Array) 533/66 MHz to 766/66 MHz, 800/100 MHz to 1100/100 MHz
 - Supports Intel PIII Coppermine FC-PGA 450/100 MHz to 850/100 MHz, 533/133 MHz to 1200/133 MHz
 - Supports 32-bit AGTL or AGTL+ bus addressing (no support for 36-bit address extension)
 - 66/100/133 MHz Host Bus speed processors are supported
- **Form Factor :** Flex-ATX 22.8 x 19.0 cm form factor
- **RAM :**
 - Supports 2 memory banks using two 168-pin unbuffered DIMM
 - Supports up to 2 double sided DIMMs at a clock speed of 100 MHz
 - Supports a maximum memory size of 512 MB (128-bit technology) or 256 MB (64-bit technology) for SDRAM
 - Supports only 3.3 V SDRAM DIMMs with gold-plated contacts
 - ECC memory checking is not supported

Jumpers

Jumper	Configuration
JP1 - Clear CMOS jumper	1-2: Normal (default) 2-3: Clear CMOS

JP3 - Safe mode/Recovery/Normal selection

1-2: Normal (default)

2-3: Safe mode (restores safe values and automatically loads the BIOS setup on boot)

Open: Recovery (used only to flash a corrupted BIOS)