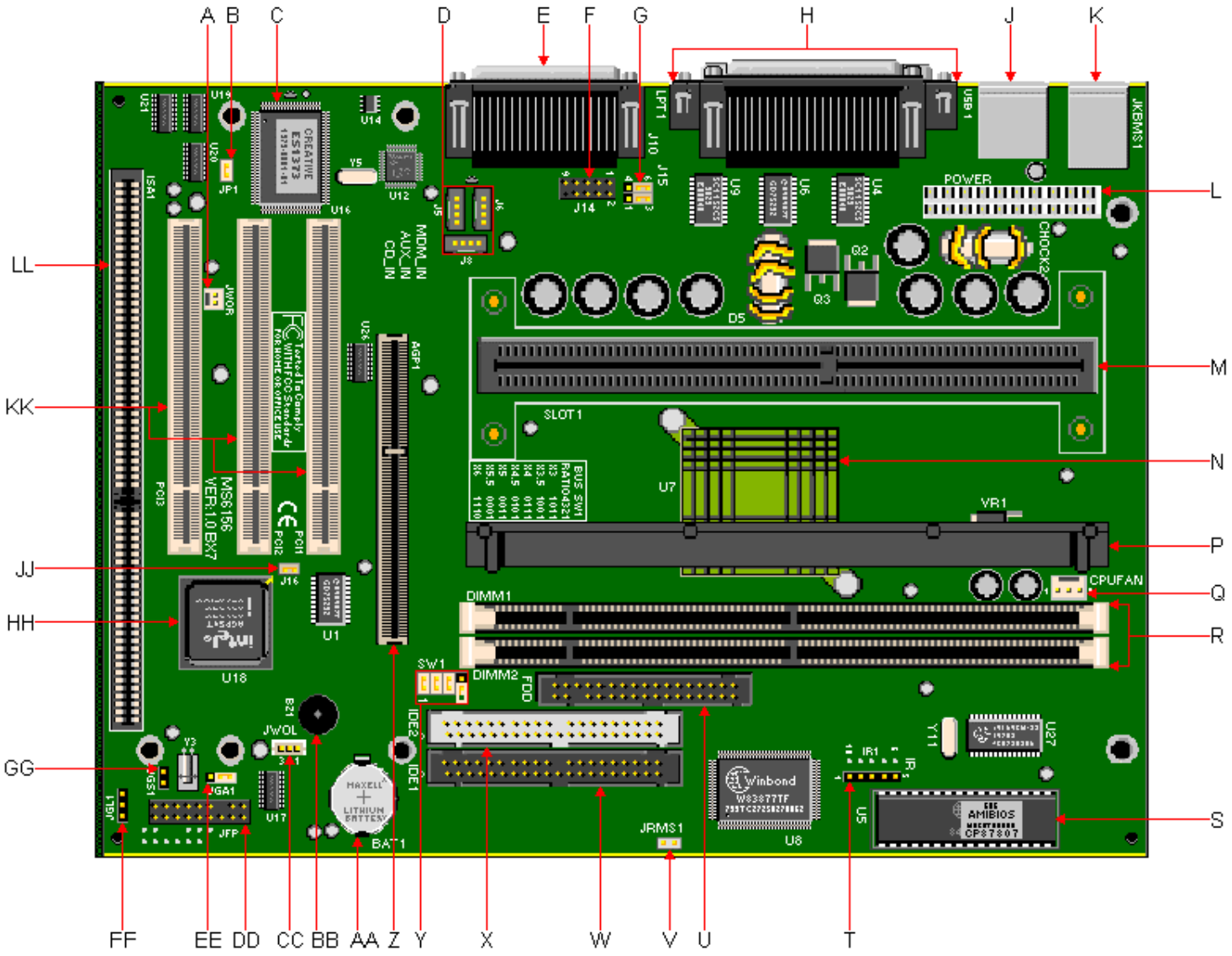
Packard Bell 911 M/Board



Item	Description	Item	Description
A	Wake-On-Ring Header	T	IrDA Connector
B	Power-On Mode Jumper (JP1)	U	FDD Connector
C	Creative Labs ES1373 Audio Chipset	V	Power On/Off Switch Header (JRMS)
D	AUX Line In (J5),Modem In (J6) & CD In (J8)	W	Primary IDE Connector
E	MIDI/Game Port, Line In, Line Out & Mic In Jacks	X	Secondary IDE Connector
F	Front Side USB Header (J14)	Y	Core/Bus Ratio Jumper Block (SW1)
G	Front Side USB Select Jumper Block (J15)	Z	AGP Port
H	Parallel & Serial Ports	AA	Battery
J	Stacked USB Ports	BB	Piezo Speaker
K	PS/2 Keyboard & Mouse Ports	CC	Wake-On-LAN Header
L	Primary Power Connector	DD	Front Panel Header (J13)
M	SEC Processor Cartridge Interface (SLOT1)	EE	CMOS Clear Jumper (JGA1)
N	Intel 82443ZXPCI AGP Controller (PAC)	FF	Power Management LED Header (JGL1)
P	CPU Heatsink Support Bar	GG	Power Management Switch Header (JGS1)
Q	CPU Heatsink Fan Power Header	HH	Intel 82371EB Chipset (PIIX4E)

R	DIMM Slots	JJ	Password Clear Jumper (J16)
S	Flash BIOS	KK	PCI Slots
		LL	ISA Slot

Specification

- **Audio :** Creative Labs ES1373 Audio Chipset.
- **Battery :** CR2032 Lithium 3V Socketed.
- **BIOS :** Intel E28F002BXT80 2 MBit Flash EPROM.
- **Bus :** PCI/ISA based system bus. 100MHz maximum bus speed.
- **Cache :** 32K level 1 cache. 0, 128, 256 or 512KB level 2 cache in the Single Edge Contact cartridge.
- **Chipset :** Intel 82443ZX PCI AGP Controller (PAC). Intel 82371EB PCI chipset (PllX4E).
- **CPU :** Intel PentiumII, Celeron and Katmai processors from 333 to 500 MHz and faster.
- **Interfaces :**
  - 2 DB-9 Serial ports
  - 1 DB-25 Parallel port
  - 1 PS/2 keyboard port
  - 1 PS/2 mouse port
  - 2 USB connector
  - 1 DB-15 MIDI/Game port (Optional)
  - 3 Stereo mini-jacks for Line In, Line Out & Mic (Optional)
- **RAM :** 2 168-pin DIMM sockets, upgradeable to 256MB total SDRAM.
- **Video :** No on-board video.

Jumpers

Jumper	Function	Configuration
JGA1	CMOS Clear	1-2 - Normal Operation 2-3 - Clear CMOS Data
J16	Password Clear	Closed - Normal Operation Open - Clear Password
JP1	Power On Mode	Closed - Select Boot-up by switch (Default) Open - Select Immediate Boot-up/Last State

Core/Bus Ratio Header (SW1)

Jumper 1	Jumper 2	Jumper 3	Jumper 4	Core/Bus Ratio
On	Off	On	On	2.5
On	On	Off	On	3
On	Off	Off	On	3.5
On	On	On	Off	4
On	Off	On	Off	4.5
On	On	Off	Off	5
On	Off	Off	Off	5.5
Off	On	On	On	6
Off	Off	On	On	6.5
Off	On	Off	On	7
Off	Off	Off	On	7.5
Off	On	On	Off	8

Note - If the CPU is 400MHz then the Core/Bus Ratio is 4.

Upgrades

- **Cache Upgrade**. The 911 motherboard comes with 0, 128K, 256K or 512K Level 2 cache, depending on the processor. The cache is not upgradeable.

- **CPU Upgrade**. This motherboard can accommodate the following CPUs:
  - Pentium II 233, 266, 300, 333, 350, 400, 450 MHz with MMX technology.
  - Pentium II/Celeron 266, 300, 333, 366 MHz with MMX technology (66MHz Host Bus frequency).
  - Pentium II/Pentium III 400, 450, 500 MHz
  - Single Edge Contact (SEC) cartridge. Uses the SLOT1 CPU socket. Not keyboard switchable. Disable cache to slow down the system.

- **RAM Upgrade**. Maximum on Motherboard: 256MB. Uses 8/16/32/64/128 MB DIMMs, 60ns or faster SDRAM DIMMs Supports only non-parity DIMM modules. The 168-pin DIMM sockets will support PC66/PC100 unbuffered 3.3V 1MBx64 (8MB), 2MBx64 (16MB), 4MBx64 (32MB), and 8MBx64 (64MB) and 16MBx64 (128MB) SDRAM DIMMs. PC100-compliant (100MHz) DIMMS should be used with any processor operating at 350MHz or faster. Bank 1 will be populated as the default from the factory. No configuration jumpers: BIOS detects memory size, and memory type. This motherboard will not accept 36-bit DIMM modules (parity).