

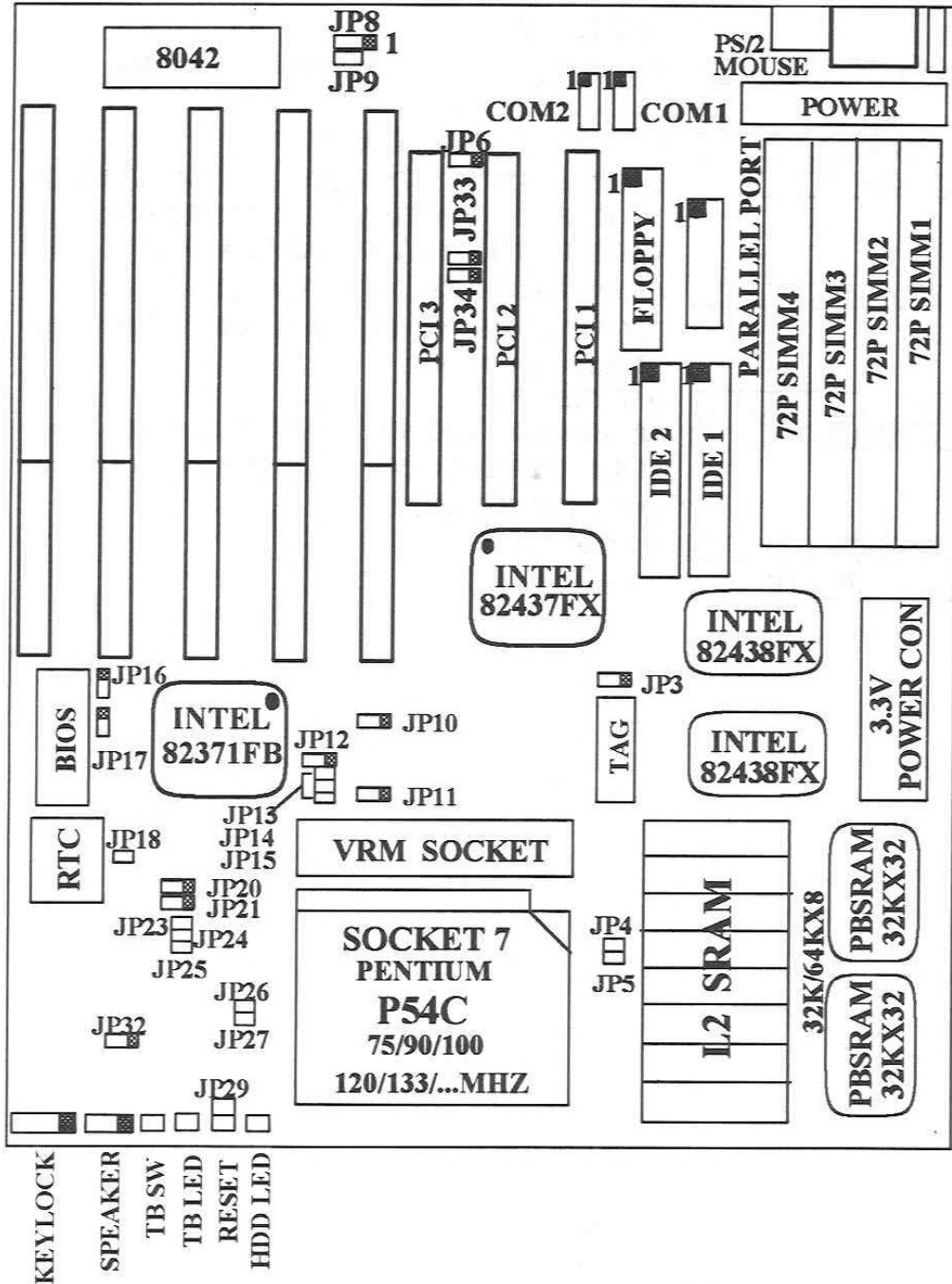
CHAPTER 1. FEATURES

1-1 SPECIFICATIONS

| | |
|-----------------------|--|
| CPU | : INTEL 75/ 90/100/120/133 MHz..... CPUs (FOR INTEL CPUs ONLY , NOT FOR CYRIX M1 OR AMD K5 CPUs) . |
| MEMORY | : 4 X 72-Pin SIMM UP TO 128M BYTES, EDO DRAM SUPPORTED |
| EXP. SLOT | : 5 X ISA AND 3 X PCI SLOTS. |
| CHIPSET | : INTEL 82437FX TRITON SYSTEM CONTROLLER. INTEL 82371FB PCI ISA IDE XCELERATOR. INTEL 82438FX TRITON DATA PATH UNIT. |
| CACHE SIZE | : ASYNCHRONOUS L2 WRITE BACK CACHE 256/512K OR SYNCHRONOUS BURST/PIPELINED BURST SRAM 256K. |
| BIOS | : AWARD BIOS. |
| I/O FUNCTION | : ON BOARD 2 x PCI IDE DEVICES , 1 x FDC , 2 x SERIAL PORTS(16550 FAST COM), 1 x PARALLEL PORT DEVICE /EPP/ECP. |
| BOARD SIZE | : 22 CM x 29 CM. |
| GREEN FUNCTION | : YES |

2-1 LAYOUT REFERENCE

J1



1. JP13,JP14,JP15 : CPU CLOCK SELECTOR

| | (50 MHZ) 75 MHZ | (60 MHZ) 90 MHZ, 120 MHZ | (66 MHZ) 100 MHZ, 133 MHZ |
|-------------|--------------------|-----------------------------|------------------------------|
| JP13 | ON | ON | ON |
| JP14 | OFF | OFF | ON |
| JP15 | OFF | ON | ON |

2. JP12,JP25,JP29 : SYSTEM AT BUS CLOCK SELECTOR

| JP12 | AT CLOCK | CPU SPEED |
|---------------|-------------|-------------------|
| 1-2 | PCI CLOCK/3 | 75 MHZ |
| 2-3 (DEFAULT) | PCI CLOCK/4 | 90/100/120/133MHZ |

NOTE : 1. PCI CLOCK = SYSTEM CLOCK / 2
2. JP12 IS SET AS " 1-2 " ONLY WHEN CPU IS 75MHZ .

| JP25 | JP29 | RATIO | CPU SPEED |
|------|------|-------|-------------------------|
| OFF | OFF | X1.5 | 75/90/100 MHZ (DEFAULT) |
| ON | OFF | X2 | 120/133MHZ |
| ON | ON | X2.5 | OVER 133 MHZ |
| OFF | ON | X3 | OVER 133 MHZ |

NOTE : USER MUST SET PROPER JP12,JP25,JP29 ACCORD TO CPU SPEED OR SYSTEM WILL HANG UP.

3. JP32 : CPU VOLTAGE

| CPU VLOTAGE | 3.3V (DEFAULT) | 3.45V | 3.6V |
|-------------|----------------|-------|---------|
| JP32 | 1-2, 3-4 | 1-2 | ALL OFF |

NOTE : CURRENT INTEL PENTIUM CPU's ARE 3.3V, DON'T CHANGE DEFAULT SETTING.

4. JP24 : CPU NA# (NEXT ADDRESS)

| PIPE LINE | DISABLE | ENABLE |
|-----------|---------|--------------|
| JP24 | OFF | ON (DEFAULT) |

NOTE : THIS JUMPER CAN ENABLE CPU PIPELINE FUNCTION TO ENHANCE SYSTEM PERFORMANCE WHEN JP24 IS " ON".

5. JP23 : CPU INTERNAL CACHE CONTROL

| L1 CACHE | W/T (WRITE THROUGH) | W/B (WRITE BACK) |
|----------|---------------------|------------------|
| JP23 | ON | OFF (DEFAULT) |

6. JP16,JP17 : BIOS TYPE OPTION

| TYPE | JP16 | JP17 |
|---------------|------|------|
| EPROM | 1-2 | 1-2 |
| 5V FLASH ROM | 1-2 | 2-3 |
| 12V FLASH ROM | 2-3 | 2-3 |

7. JP33,JP34 : IR/COM2 SELECTOR

| IR & COM2 FUNCTION CHANGE | COM2 | IR |
|---------------------------|------|-----|
| JP33 | 1-2 | 2-3 |
| JP34 | 1-2 | 2-3 |

NOTE : INFRA-RED(I.R.) USES TWO PINS OF COM2,USER JUST CAN SELECT EITHER ONE ONLY.

IR CONNECTOR PIN OUT ASSIGNMENT

| JP8 IR PIN OUT | | | | | | | |
|----------------|----|---|-----|---|----|---|-----|
| 1 | RX | 2 | GND | 3 | TX | 4 | +5V |

8. JP6 : ON BOARD I/O DISABLE/ENABLE JUMPER SETTING

| ON BOARD I/O CHIPSET FUNCTION | ENABLE | DISABLE |
|-------------------------------|--------|---------|
| JP6 | 1-2 | 2-3 |

9. JP3,JP20,JP21 : CACHE MEMORY CONFIGURATION

| L2 CACHE SIZE | 256K | 512K | PBSRAM |
|-------------------|----------------------------|------------------|----------------------------|
| JP3 | 2-3 | 1-2 | 2-3 |
| JP20 | 1-2 | 2-3 | 1-2 |
| JP21 | 1-2 | 2-3 | 1-2 |
| DATA RAM (U7-U14) | 32KX8 8PCS | 64KX8 8PCS | 32KX32 2PCS |
| TAG SIZE (U20) | 8K X 8, 16K X 8 32K X 8 | 16K X 8, 32K X 8 | 8K X 8, 16K X 8 32K X 8 |

NOTE: 1. PBSRAM : PIPELINED BURST SRAM
2. WHEN PBSRAM IS USED ON BOARD,USER MUST REMOVE ALL ASYNCHRONOUS CACHE RAMs

10. OTHER JUMPER SETTINGS AND CONNECTORS :

| JUMPER | SETTING | FUNCTION. |
|-----------|----------------|--|
| JP10 | 1-2 (DEFAULT) | CPU & SYSTEM CLK= PCI CLK X2. |
| | 2-3 | CPU & SYSTEM CLK= PCI CLK. |
| J11 | | EXTERNAL BATTERY CONNECTOR |
| JP18 | BREAK FUNCTION | CONNECT TO CASE TURBO PUSH BUTTON AS A TRIGGLE SWITCH (ON THEN OFF) , SWITCH THAT SYSTEM GO INTO GREEN MODE IMMEDIATELY BY HARDWARE. |
| JP9 | ON | FOR DALLAS CMOS DATA CLEAR. |
| JP30 | 2-3 | FOR RTC POWER PIN. |
| JP31 | ON | JP30 (OFF) FIRST AND JP31 (ON) THEN CLEAR RTC CMOS DATA. |
| J1 | | EXTERNAL PS2 MOUSE CONNECTOR |
| JP4, JP5 | ON (DEFAULT) | FOR P54C CPU VOLTAGE |
| JP26,JP27 | OFF | FOR P55CT CPU WITH VRM MODULE |

2-3 MEMORY CONFIGURATION

This section provides information on how to install the DRAM and Cache RAM. Improper installation of DRAM or Cache RAM will cause the system to shutdown.

There are no jumpers for the DRAM configuration. The BIOS will test the DRAM type and size automatically. There are four banks from SIMM1 to SIMM4 on main board. please follow SIMM1, then 2,3,4 to install memory. DRAM speed must be 70ns or faster. Both parity (x36) or non-parity (x32) are acceptable.

| SIMM1 | SIMM2 | SIMM3 | SIMM4 | TOTAL |
|-------|-------|-------|-------|-----------|
| 4MB | 4MB | --- | --- | 8MBytes |
| 4MB | 4MB | 4MB | 4MB | 16MBytes |
| 8MB | 8MB | --- | --- | 16MBytes |
| 4MB | 4MB | 8MB | 8MB | 24MBytes |
| 8MB | 8MB | 4MB | 4MB | 24MBytes |
| 8MB | 8MB | 8MB | 8MB | 32MBytes |
| 16MB | 16MB | --- | --- | 32MBytes |
| 16MB | 16MB | 4MB | 4MB | 40MBytes |
| 8MB | 8MB | 16MB | 16MB | 48MBytes |
| 16MB | 16MB | 8MB | 8MB | 48MBytes |
| 16MB | 16MB | 16MB | 16MB | 64MBytes |
| 32MB | 32MB | --- | --- | 64MBytes |
| 4MB | 4MB | 32MB | 32MB | 72MBytes |
| 8MB | 8MB | 32MB | 32MB | 80MBytes |
| 32MB | 32MB | 8MB | 8MB | 80MBytes |
| 16MB | 16MB | 32MB | 32MB | 96MBytes |
| 32MB | 32MB | 16MB | 16MB | 96MBytes |
| 32MB | 32MB | 32MB | 32MB | 128MBytes |