

P4GE-MX



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CPU

Validated
since PCB

Validated since
BIOS

Note

Celeron 2.0 GHz (400 FSB, L2
cache:128KB)

ALL

ALL

GO

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Celeron 2.1 GHz (400 FSB, L2 cache:128KB)	ALL	GO
Celeron 2.2 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.3 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.4 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.5 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.6 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.7 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron 2.8 GHz (400 FSB, L2 cache:128KB)	ALL	ALL GO
Celeron D 310 revE0(2.13 GHz, 533 FSB, Socket478)	ALL	GO
Celeron D 310 revG0(2.13 GHz, 533 FSB, Socket478)	ALL	1007 GO
Celeron D 315 (2.26 GHz, 533 FSB, Socket478)	ALL	1006 GO
Celeron D 320 (2.4 GHz, 533 FSB, Socket478)	ALL	ALL GO
Celeron D 325 (2.53 GHz, 533 FSB, Socket478, revC0/D0)	ALL	ALL GO
Celeron D 325 (2.53 GHz, 533 FSB, Socket478, revE0)	ALL	1007 GO

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Celeron D 330 (2.66 GHz, 533 FSB, Socket478)	ALL	GO
Celeron D 335 (2.8 GHz, 533 FSB, Socket478)	ALL	ALL GO
Celeron D 340 (2.93 GHz, 533 FSB, Socket478)	ALL	1005 GO
Celeron D 345 (3.06 GHz, 533 FSB, Socket478)	ALL	1005 GO
Celeron D 350 (3.20 GHz, 533 FSB, Socket478, revE0)	ALL	1006 GO
Celeron D 350 (3.20 GHz, 533 FSB, Socket478, revG1)	ALL	1009 GO
P4-1.6A GHz (Socket478, 400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-1.8A GHz (Socket478, 400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.20 GHz (400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.26 GHz (533 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.40 GHz (400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.40A GHz (533 FSB, L2 cache:1MB, 90nm)	ALL	ALL GO
P4-2.40B GHz (533 FSB, L2 cache:512KB)	ALL	ALL GO

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P4-2.00 GHz (400 FSB, L2 cache:512KB)	ALL	GO
P4-2.53 GHz (533 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.60 GHz (400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.66 GHz (533 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.80 GHz (533 FSB, L2 cache:512KB)	ALL	ALL GO
P4-2.80A GHz (533 FSB, L2 cache:1MB, 90nm)	ALL	1005 GO
P4-2A GHz (Socket478, 400 FSB, L2 cache:512KB)	ALL	ALL GO
P4-3.06 GHz (533 FSB, L2 cache:512KB, HT, C1/D1)	ALL	ALL GO
P4-3.40 GHz (800 FSB, L2 cache:1MB, rev.G1)	ALL	1009 GO



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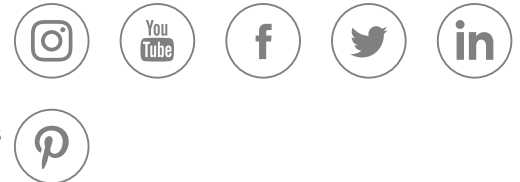
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