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P4GD1

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CPU	Validated since PCB	Validated since BIOS	Note
Celeron D 310 revE0(2.13 GHz, 533 FSB, Socket478)	ALL	GO	

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Celeron D 310 revC0 (2.13 GHz, 533 FSB, Socket478)	ALL	
Celeron D 315 (2.26 GHz, 533 FSB, Socket478)	ALL	1005 
Celeron D 320 (2.4 GHz, 533 FSB, Socket478)	ALL	1002 
Celeron D 325 (2.53 GHz, 533 FSB, Socket478, revC0/D0)	ALL	1002 
Celeron D 325 (2.53 GHz, 533 FSB, Socket478, revE0)	ALL	1008.003 
Celeron D 330 (2.66 GHz, 533 FSB, Socket478)	ALL	1002 
Celeron D 335 (2.8 GHz, 533 FSB, Socket478)	ALL	1002 
Celeron D 340 (2.93 GHz, 533 FSB, Socket478)	ALL	1003 
Celeron D 345 (3.06 GHz, 533 FSB, Socket478)	ALL	1003 
Celeron D 350 (3.20 GHz, 533 FSB, Socket478, revE0)	ALL	1005 
Celeron D 350 (3.20 GHz, 533 FSB, Socket478, revG1)	ALL	1008.003 
P4-2.26 GHz (533 FSB, L2 cache:512KB)	ALL	ALL 
P4-2.40A GHz (533 FSB, L2 cache:1MB, 90nm)	ALL	ALL 
P4-2.40B GHz (533 FSB, L2 cache:512KB)	ALL	ALL 

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P4-2.13C GHz (800 FSB, L2 cache:512KB, HT)	ALL	
P4-2.53 GHz (533 FSB, L2 cache:512KB)	ALL	ALL 
P4-2.60C GHz (800 FSB, L2 cache:512KB, HT)	ALL	ALL 
P4-2.66 GHz (533 FSB, L2 cache:512KB)	ALL	ALL 
P4-2.80 GHz (533 FSB, L2 cache:512KB)	ALL	ALL 
P4-2.80A GHz (533 FSB, L2 cache:1MB, 90nm)	ALL	1002 
P4-2.80C GHz (800 FSB, L2 cache:512KB, HT, D1)	ALL	ALL 
P4-2.80E GHz (800 FSB, L2 cache:1MB, HT, 90nm, CO)	ALL	1002 
P4-3 GHz (800 FSB, L2 cache:512KB, HT, D1/M0)	ALL	ALL 
P4-3.06 GHz (533 FSB, L2 cache:512KB, HT, C1/D1)	ALL	ALL 
P4-3.20 GHz (800 FSB, L2 cache:512KB, HT, D1)	ALL	ALL 
P4-3.20 GHz Extreme Ed. (800 FSB, HT)	ALL	ALL 
P4-3.20E GHz (800 FSB, L2 cache:1MB, HT, 90nm)	ALL	1002 
P4-3.40 GHz (800 FSB, L2 cache:1MB, rev.G1)	ALL	1008.003 

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P4-3.0 GHz (800 FSB, L2 cache:512KB, HT, D1)	ALL	GO
P4-3.40 GHz Extreme Ed. (Socket478, 800 FSB, HT)	ALL	ALL GO
P4-3.40E GHz (800 FSB, L2 cache:1MB, HT, 90nm)	ALL	1002 GO
P4-3E GHz (800 FSB, L2 cache:1MB, HT, 90nm)	ALL	1002 GO

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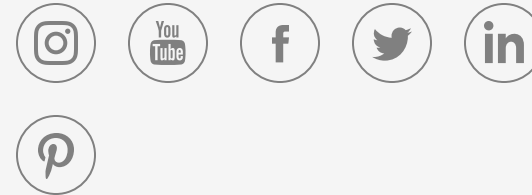
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