

MV026 USER'S MANUAL
OPTi 486DX VL-BUS MOTHERBOARD

Jumpers and Connectors reference

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Before installing the mother board, make sure the jumpers setting are set for your configuration. The Function of each jumpers are as follows:

| | |
|--|--|
| CPU Type Selector | JP3, JP5, JE8, JP9, JP12, JP22, JP23, JP45, JP50 |
| PQFP CPU Selector | JP1, JP6, JP7 |
| SL-enhanced CPU Selector | JP24, JP25, JP36 |
| CYRIX M7 Clock Selector | JP27 |
| P24C Clock selector | JP43 |
| CPU VOLTAGE Selector | JP44 |
| Mono / Color Belector | JP30 |
| Clock Generator Frequency Selector | JP38, JP39, JP40, JP48 |
| CACHE Option | JP10, JP11, JP13, JP3S |
| VL-BUS Option | JP33, JP34, JP46 |
| Charge / Discharge CMOS | JP26 |
| Monitor Power saving | JP32 |
| Speaker Connector | JP18 |
| Turbo Switch Connector | JP16 |
| Turbo LED Connector | JP17 |
| Reset Switch Connector | S1 |
| External Battery Connector | J1 |
| Keylock/Power LED Connector | S2 |
| Keyboard Connector | S3 |
| Power Supply Connector | PLI |

CPU Type Selector

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CPU Type Selector is used to select different type of CPU.

| CPU TYPE | JP3 | JP5 | JP8 | JP9 | JP12 | JP22 | JP23 | JP45 | JP50 |
|----------|------|------|-----|------|-------|------|-------|-------|------|
| 486SX | OPEN | OPEN | 2-3 | OPEN | OPEN | 1-2 | OPEN | CLOSE | OPEN |
| 487SX | 2-3 | 2-3 | 1-2 | OPEN | OPEN | 1-2 | OPEN | CLOSE | OPEN |
| 486DX | 1-2 | 2-3 | 1-2 | OPEN | OPEN | 1-2 | OPEN | CLOSE | OPEN |
| M6 | OPEN | OPEN | 2-3 | 1-2 | OPEN | 1-2 | OPEN | OPEN | 2-3 |
| M7 | 1-2 | 2-3 | 1-2 | OPEN | OPEN | 1-2 | OPEN | OPEN | 2-3 |
| P24C | 1-2 | 2-3 | 1-2 | OPEN | OPEN | 1-2 | OPEN | CLOSE | OPEN |
| P24D | 1-2 | 2-3 | 1-2 | OPEN | CLOSE | 1-2 | CLOSE | CLOSE | 1-2 |
| P24T | 1-2 | 2-3 | 1-2 | 2-3 | OPEN | 2-3 | OPEN | CLOSE | OPEN |

NOTE: If CYRIX M7 CPU is used note JP27 CYRIX M7 CLOCK Selector.

NOTE: If P24C (DX4-i00 MHz) CPU is used note JP43 P24C CLOCK Selector, Clock Generator Frequency selector and JP44 CPU VOLTAGE Selector. P24C (DX4-100 MHz) select 50/16 P24C CLK 2X.

WARNING: Wrong CPU Voltage to P24C will DAMAGE the CPU.

JP1, JP6, JP7 PQFP CPU Selector

| | | |
|-------|--------|------------------|
| JP6 | JP7 | JP1 |
| OPEN | OPEN | OPEN |
| CLOSE | CLOSE | CLOSE |
| | SX CPU | ENABLE PQFP CPU |
| | DX CPU | DISABLE PQFP CPU |

JP24, JP25, JP36 SL-ENCHANCED CPU Selector

| | | |
|-------|-------|----------------------|
| JP24 | JP25 | JP36 |
| OPEN | OPEN | OPEN |
| CLOSE | CLOSE | CLOSE |
| | | NON SL-ENCHANCED CPU |
| | | SL-ENCHANCED CPU |

JP27 CYRIX M7 CLOCK Selector

1-2 M7 x1 CLK
2-3 M7 x2 CLK
OPEN OTHER CPU

JP43 P24C CLOCK Selector

OPEN P24C x3 CLK
1-2 P24C x2 CLK
2-3 P24C x2.5 CLK

JP44 CPU VOLTAGE Selector

2-3, 5-6 3.45 VOLTS P24C CPU
1-2, 4-5 5.0 VOLTS OTHER CPU

JP30 MONO/COLOR Selector

OPEN MONO include EGA and VGA
CLOSE COLOR for CGA only

JP38, JP39, JP40, JP48

CLOCK GENERATOR FREQUENCY SELECTOR

| | JP38 | JP39 | JP40 | JP48 |
|-------|-------|-------|-------|------|
| 25/8 | CLOSE | CLOSE | OPEN | 1-2 |
| 33/8 | CLOSE | OPEN | CLOSE | 1-2 |
| 40/8 | OPEN | CLOSE | CLOSE | 1-2 |
| 5018 | CLOSE | CLOSE | CLOSE | 1-2 |
| 33/16 | CLOSE | OPEN | OPEN | 2-3 |
| 50/16 | CLOSE | CLOSE | OPEN | 2-3 |

JP10, JP11, JP13, JP35 CACHE OPTION

| SIZE | JP10 | JP11 | JP13 | JP35 | TAG RAM |
|-------|-------|-------|------|------|---------|
| 64 K | OPEN | OPEN | 2-3 | 1-2 | 8K * 8 |
| 128 K | OPEN | CLOSE | 1-2 | 1-2 | 8K * 8 |
| 256 K | CLOSE | CLOSE | 2-3 | 2-3 | 16K * 8 |
| | | | | 1-2 | 32K * 8 |

| SRAM | BANK 0 (U5 - U8) | BANK 1 (U9 - U12) | TAG RAM U13 |
|-------|---------------------|-----------------------|-------------------|
| 64 K | 8K * 8 | 8K * 8 | 8K * 8 |
| 128 K | 32K * 8 | NONE | 8K * 8 |
| 256 K | 32K * 8 | 32K * 8 | 32K * 8 / 16K * 8 |

JP33, JP34, JP46 VL-BWS OPTION

| JP33 | JP34 | JP46 |
|-------------|----------------|-------------------|
| OPEN 0 W/S | OPEN <=33 MHz | 2-3 LCLK <=33 MHz |
| CLOSE 1 W/S | CLOSE > 33 MHz | 1-2 LCLK > 33 MHz |

JP26 CHARGE / DISCHARGE CMOS

1-2 DISCHARGE CMOS NOTE: All DATA in the CMOS will be ERASE.
2-3 CHARGE CMOS

JP32 Monitor Power Saving

NOTE: This function need a Green function power supply and also note of polarity.

bottom of the screen during the POST (Power On Self Test) press DEL key to enter setup.

2. Press the DEL key to enter the Award BIOS program and the main menu will appear on the screen. The main menu allows you to select from ten setup functions and two exit choices.

3. For the safe, please select "LOAD SETUP DEFAULTS" for BIOS setup.

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