User's Manual

VIA *K8T800* mainboard for AMD Socket 754 based Athlon 64 processor

TRADEMARK

All products and company names are trademarks or registered trademarks of their respective holders.

These specifications are subject to change without notice.

Manual Revision 1.0 August 20, 2003

DISCLAIMER OF WARRANTIES:

THERE ARE NO WARRANTIES WHICH EXTEND BEYOND THE DESCRIPTION ON THE FACE OF THE MANUFACTURER LIMITED WARRANTY. THE MANUFACTURER EXPRESSLY EXCLUDES ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING ITS PRODUCTS; INCLUDING ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NONINFRINGEMENT. THIS DISCLAIMER OF WARRANTIES SHALL APPLY TO THE EXTENT ALLOWED UNDER LOCAL LAWS IN THE COUNTRY PURCHASED IN WHICH LOCAL LAWS DO NOT ALLOW OR LIMIT THE EXCLUSION OF THE IMPLIED WARRANTIES.

80 Port Frequently Asked Questions

Below is a list of some basic POST Codes, possible problems and solutions. For more detailed information about POST Codes, refer to Appendix D in this manual.

POST CODE	Problem	Solution
FFh or CFh	 BIOS chip inserted incorrectly Incorrect BIOS update version Mainboard problem Add-on card inserted incorrectly. 	 Reinsert the BIOS chip Download the correct BIOS version update from the manufacturer's Web site. Replace mainboard Remove and replace the add-on card
C1h - C5h	 Memory module inserted incorrectly Memory compatibility problem Memory module damaged 	 Reinsert memory module Replace memory with correct type Replace memory module
2Dh	 Error occured in VGA BIOS VGA card inserted incorrectly 	 Replace VGA card Reinsert the VGA card
26h	Overclock error	Clear CMOS or press the insert key to power on the system
07h - 12h	 Initial Keyboard controller error RTC error 	 Ensure that the keyboard and mouse are connected correctly. Replace the RTC battery.

Table of Contents

	Page
Section 1	Introduction
	Package Contents1-1
	Overview
	Athlon TM 64 Processor1-2
	Chipset Overview1-2
	Accelerated Graphics Port 1-3
	Ultra ATA66/100/1331-3
	Hardware Monitoring1-3
	LAN (Optional) 1-3
	Serial ATA1-3
	Mainboard Form-Factor1-4
	I/O Shield Connector 1-5
	Power-On/Off(Remote) 1-5
	System Block Diagram 1-6
Section 2	Features
	Mainboard Features2-1
Section 3	Installation
	Mainboard Layout
	Easy Installation Procedure
	CPU Insertion
	Jumper Settings
	System Memory Configuration
	Expansion Slots
	Device Connectors
	STR (Suspend To RAM) Function
	CPU Overheating Protection

Section 4	BIOS Setup
	Main Menu4-1
	Standard CMOS Setup 4-2
	Advanced BIOS Features
	Advanced Chipset Features
	Integrated Peripherals 4-11
	Power Management Setup 4-16
	PNP/PCI Configuration Setup 4-20
	PC Health Status4-22
	Power BIOS Features 4-24
	Defaults Menu4-26
	Supervisor/User Password Setting
	Exit Selecting
Section 5	S-ATA RAID Configuration
	Introduction 5-1
	Key Features 5-3
	BIOS Configuration Utility
Section 6	Driver Installation
	Easy Driver Installation 6-1
	Realtek Sound Manager Quick User-guide 6-2
Appendix	Appendix A
	Realtek Media Player User's Guide A-1
	Appendix B
	Update Your System BIOS B-1
	Appendix C
	EEPROM BIOS Remover C-1
	Appendix D
	POST Codes D-1

Page Left Blank

Section 1

Package Contents

Contents

- A. Mainboard
- B. User's manual
- C. Floppy drive cable
- D. HDD ribbon cable
- E. CD or diskette (drivers and utilities)
- F. I/O Shield
- G. S-ATA data and power cables

Deluxe Pack Items

H. S-ATA with Game port bracket (for SATA3 ~ SATA6 connector only)

Other Optional Items

I. Extra USB2.0 port cable

If you need the other optional item, please contact your dealer for assistance.



AthIon[™] 64 Processor

The AMD Athlon[™] 64 (ClawHammer) processor family is designed to support performance desktop. It provides a high performance HyperTransport[™] link to I/O, as well as a single 64-bit high-performance DDR memory controller. Main key features such as:

- AMD64 technology enables the industry to advance software development on all fronts, with x86 compatibility, while providing the capability of delivering 64-bit technology benefits.
- HyperTransport technology helps reduces the number of buses in a system, which can reduces system bottlenecks and enable today's faster microprocessors to use system memory more efficiently in high-end multiprocessor systems.
- The memory controller provides a programmable interface to a variety of standard DDR SDRAM DIMM configuration.

For more information about all the new features AthlonTM 64 Processor deliver, check out the AMD website at <u>http://www.amd.com</u>

Chipset Overview

The board is designed with VIA chipset, K8T800 as North Bridge and VT8237 as South Bridge, providing a feature rich and scalable platform. The K8T800 North Bridge is designed with an ultra fast HyperTransport link to the Athlon[™] 64 and comes equipped with AGP 8X features for VGA card expansion.

The K8T800 North Bridge connects to the VT8237 South Bridge through the fast Ultra V-Link connection at 1.06GB/s. With support for two Serial ATA channels at 150MB/s bandwidth, integrated RAID (0, 1, 0+1 & JBOD) capability as well as up to eight USB 2.0 ports, the VT8237 offers the highest possible peripheral performance for professional users. To extend flexibility to the system integrator, the VT8237 features dual channel Parallel ATA-133 allowing for configurations that leverage the existing technology standard. Additional features include a VIA MAC for 10/100Mbps Fast Ethernet, integrated PCI support.

Accelerated Graphics Port (AGP)

The AGP slot on the board is compliant with the new AGP 3.0 specification. This new specification enhances the functionality of the original AGP specification by allowing 8X data transfers (8 data samples per clock) resulting in maximum bandwidth of 2.1GB/s. In addition, it defines 1.5 volt power supply operation. Complying with this specification, this board supports external AGP-8X/4X cards with Fast Write Transactions. Only 1.5V AGP cards are supported.

Ultra ATA/66/100/133

This mainboard provides two independent ATA133 IDE controllers, supporting standard programmable input/output (PIO) and Direct Memory Access (DMA) mode operations, as well as UltraDMA-133/100/66/33 standards for a maximum data transfer rate of 133MB/sec per channel.

Hardware Monitoring

Hardware monitoring enables you to monitor various aspects of the system operation and status. The features include CPU temperature, voltage and fan speed in RPMs.

LAN (Optional)

This mainboard is optionally mounted with LAN chipset. It allows the mainboard to connect to a local area network by means of a network hub.

Serial ATA

The evolutionary serial ATA interface replaces the standard parallel ATA physical storage interface. The serial ATA specification provides scalability and allows future enhancements to the computing platform. Serial technology overcomes performance limits of parallel interface architecture, meeting the escalating need for faster data throughput in servers and storage devices.

Serial ATA is completely software compatible with parallel ATA, requiring no modification to your operating system. The serial ATA interface cable requires lower voltages and uses smaller cable connectors, providing ease of installation. You can easily upgrade storage devices that are compatible with the serial ATA interface specification.

Mainboard Form-Factor

This board is designed with ATX form factor - the latest industry standard for chassis design. The ATX form factor is essentially a Baby-AT baseboard rotated 90 degrees within the chassis enclosure and a new mounting configuration for the power supply. With these changes the processor is relocated away from the expansion slots, allowing them all to hold full length add-in cards. ATX defines a double height aperture to the rear of the chassis which can be used to host a wide range of onboard I/O. Only the size and position of this aperture is defined, allowing PC manufacturers to add new I/O features (e.g.; TV input, TV output, joystick, modem, LAN, audio, etc.) to systems. This will help systems integrators differentiate their products in the marketplace, and better meet your needs.

- By integrating more I/O down onto the board and better positioning the hard drive and floppy connectors, material cost of cables and add-in cards are reduced.
- By reducing the number of cables and components in the system, manufacturing time and inventory holding costs are reduced and reliability will increase.
- By using an optimized power supply, it's possible to reduce cooling costs and lower acoustical noise. An ATX power supply, which has a sidemounted fan, allows direct cooling of the processor and add-in cards making a secondary fan or active heatsink unnecessary in most system applications.



Figure 2: Summary of ATX chassis features

I/O Shield Connector

The I/O back panel for this mainboard is shown below (Figure 3). When installing the mainboard into the computer case, use the bundled I/O shield to protect this back panel. $R_{I-45} LAN = RAC Lack$



Power-On/Off (Remote)

This board has a 20-pin ATX power supply connector (Figure 4). For power supplies that support the **Remote On/Off** feature, this should be connected to the mainboard front panel PW_ON connector for the computer power On/Off button. This board has been designed with "Soft Off" function. You can turn off the system in two ways: by pressing the front panel power On/Off button or using the "Soft Off" function that can be controlled by an operating system such as Windows[®]XP/ ME/2000/98.

- Note: For maintaining the DDR SDRAM power during STR (ACPI S3) function, it is strongly recommended to use power supplies that have a +5VSB current of (>=) 2A. Please check the 5VSB's specification printed on the power supply's outer case.
- Note: The board requires a minimum of 250 Watt power supply to operate. Your system configuration (amount of memory, add-in cards, peripherals, etc.) may exceed this minimum power requirement. To ensure that adequate power is provided, use a 300 Watt (or higher) power supply.



Introduction

System Block Diagram



Figure 5: System Block Diagram

Section 2 FEATURES

Mainboard Features



• Supports 754-pin Socket for AMD Athlon 64 (ClawHammer) processors with 800MHz Hyper Transport FSB up to 3400+



VIA K8T800 AGPset: VIA K8T800 + VT8237



- Two 184-pin DDR DIMM sockets for 64-bit, Unbuffered, Single/Doubleside and DDR-266/333/400 DIMMs
- Supports up to 2GB memory size

BIOS

- Flash EEPROM with Award BIOS
 - ACPI v2.0 compliant
 - S3 (Suspend to DRAM) sleep-state support
 - SMBIOS (System Management BIOS) v2.2 compliant
 - Supports Power failure recovery
 - Supports "Cool'n Quiet" power saving technology
 - Able to wake the computer from specific states by LAN, Power switch, PME#, RTC alarm, USB, PS2 KB&Mouse, Modem ring on COM#1...

Onboard Devices

 LAN --> Embedded 10/100Mbps Fast Ethernet controller with onboard (Optional) VIA 6103 PHY

Integrated 1Gbps Fast Ethernet controller from onboard 3COM 3C940 LAN chip (for high-end model only)

- S-ATA --> Up to six Serial ATA devices with up to 150MB/sec transfer rate - 2 ports from VIA VT8237
 - Optional 4 ports from Silicon Image SIL3114 with RAID 0, 1, 10, 5 support

For more detailed information about S-ATA, refer to user's manual in the attached CD.

• IDE--> Embedded IDE controller with 2 ordinary IDE ports up to 4 IDE devices, supports ATA-133 with up to 133MB/sec bandwidth

Legacy IO Controller

- Winbond W83697HF LPC IO controller for floppy, printer, serial, game and CIR/SIR interface
- Supports Hardware Monitoring function

Audio

- Six channel audio with analog and digital output using Realtek ALC655 AC'97 CODEC
 - AC'97 v2.3 compliant
 - In 2-CH mode, supports Line-In (Light Blue), Line-Out (Lime) and Mic-In (Pink) at rear panel
 - In 6-CH mode, supports Rear speaker out (Light Blue), Front speaker out (Lime) and Center&Subwoofer speaker out (Pink) at rear panel
 - Supports CD-In, Aux-In and S/PDIF-in/out interface
 - Supports Line-out and Mic-In for front panel
 - Supports automatic "jack-sensing"

Peripheral Interfaces

🕗 At Rear Panel

- PS/2 keyboard and mouse ports
- One Parallel (printer) port
- Two Serial port

- Two RJ45 LAN connector (Optional)
- Four USB2.0 ports
- One S/PDIF-Out RCA jack
- One S/PDIF-Out Optical Connector
- Three Audio jacks

Onboard connector and pin-header

- One floppy drive connector
- Two IDE connectors
- Four extra USB2.0 ports
- One CD-IN and One AUX-IN connector
- One S/PDIF in/out connector
- One IR connector
- One Front Panel Audio connector
- One Game port connector
- Six S-ATA connectors (4 optional from Silicon Image Sil3114 chip)
- Three Fan connectors

Front Panel Controller

- Supports Reset & Soft-Off switches
- Supports HDD & Power LEDs
- Supports PC speaker

Expansion Slots

- One AGP slot supporting 1.5v 4X/8X AGP cards
 - AGP v3.0 compliant
- Five PCI slots with Bus Master support
 - PCI v2.2 compliant



 Magic Health – a BIOS H/W monitoring utility for voltage, temperature and fan-speed sensing displayed during POST

- EZ Boot A simple shortcut to select the boot device, e.g. hard drive, CD-ROM or floppy without entering CMOS setup
- Supports exclusive KBPO (Keyboard Power On) function
- Power BIOS for excellent over clocking capabilities through
 - subtle voltage tuning for CPU, Memory, AGP
 - subtle frequency tuning on FSB
- CPU Overheating Protection
- P80P for system debugging

Form Factor

• 305mm x 245 mm ATX size

Installation

Section 3



Installation

Mainboard Layout



Easy Installation Procedure

The following must be completed before powering on your new system:

- 3-1. CPU Installation
- 3-2. Jumper Settings
- 3-3. System Memory Configuration
- 3-4. Expansion Slots
- 3-5. Device Connectors

3-1 CPU Installation





Step 1

Place the back plate into the two holes around the CPU area behind the mainboard.





Step 3

Screw in the two pan head screws.



Step 2

Place the retention mechanism on the mainboard as shown.



<Figure 4>

Step 4

Open the socket by raising the actuation lever.

Installation



<Figure 5>

Step 5

Align pin 1 on the CPU with pin 1 on the CPU socket as shown in the illustration above. The CPU is keyed to prevent incorrect insertion. Do not force the processor into the socket. If it does not go in easily, check for mis-orientation and reinsert the CPU. Make sure the processor is fully inserted into the socket.



<Figure 6>

Step 6

Close the socket by lowering and locking the actuation lever.





Step 7

Apply thermal compound to the top of the CPU and install the heatsink as shown.



<Figure 8>

Step 8

Press the clips in the direction of the arrows shown in Figure 8 to secure the assembly to the CPU socket.



<Figure 9>

Step 9

Plug the CPU fan power into the mainboard's CPU fan connector.

The installation is complete.

Note: Thermal compound and qualified heatsink recommended by AMD are a must to avoid CPU overheat damage.

3-2 Jumper Settings



JCMOS: Clear CMOS data Jumper

If the CMOS data becomes corrupted or you forgot the supervisor or user password, clear the CMOS data to reconfigure the system back to the default values stored in the ROM BIOS.



Settings: 1-2: Normal (Default) 2-3: Clear CMOS

To CMOS Clear data, please follow the steps below.

- 1. Turn off the system.
- 2. Change the jumper from "1-2" to "2-3" position for a few seconds.
- 3. Replace the jumper on to the "1-2" position.
- 4. Turn on the system and hold down the key to enter BIOS setup.

3-3 System Memory Configuration

Memory Layout

The mainboard accommodates two PC2100/PC2700/PC3200 184-pin DIMMs (Dual Inline Memory Modules):

- Supports up to 2.0GB of 266/333/400MHz DDR SDRAM.
- Supports unbuffered DIMMs with a 64-bit data bus.
- Supports ECC checking with double-bit detect with single-bit correct.
- Supports configurations defined in the JEDEC DDR DIMM specification.

Figure 10 and Table 1 show two possible memory configurations.

<Figure 10>

DDR DIMM 1 DDR DIMM 2

<Table 1>

Total Memory	DDR DIMM 1	DDR DIMM 2
= 1GB Maximum	DDR SDRAM* 64MB, 128MB, 256MB, 512MB, 1GB* X 1	None
= 2GB Maximum	DDR SDRAM* 64MB, 128MB, 256MB, 512MB, 1GB* X 1	DDR SDRAM* 64MB, 128MB, 256MB, 512MB, 1GB* X 1

NOTES:

• Using non-compliant memory with higher bus speeds (overclocking) may severely compromise the integrity of the system.

DIMM Module Installation

Figure 11 displays the notch on the DDR DIMM memory module.

DIMMs have 184 pins and one notch that matches with the DDR DIMM socket. DIMM modules are installed by placing the chip firmly into the socket and pressing straight down as shown in figure 12 until the white clips close and the module fits tightly into the DIMM socket (figure 13).



Figure 11 - DIMM notch



Figure 12 - DIMM module clips before installation



Figure 13 - DIMM module clip after installation

To remove the DIMM module press down the white clips and the module is ejected from the socket.

3-4 Expansion Slots



AGP Slot

The mainboard is equipped with an AGP slot. Make sure you install a card that supports the 1.5V specification.

PCI Slots

The mainboard is equipped with 5 PCI slots.

Installing an Expansion Card

The steps below assume that the mainboard is already installed in the system chassis.

- 1. Make sure the PC and all other peripheral devices connected to its has been powered down.
- 2. Disconnect all power cords and cables.
- 3. Remove the system unit cover.
- 4. Remove the bracket of the slot that you intend to use. (You need to remove the screw in order to remove the bracket.)
- 5. Align the card above the slot then press it down firmly until it is completely seated in the slot.
- 6. Secure the card to the chassis with the screw you removed in step 4.
- 7. Replace the system unit cover.
- 8. Power on the PC.
- 9. Enter the BIOS step program to make the necessary settings.
- 10. Save the settings and restart the PC.
- 11. Install the software drivers of the expansion cards, if necessary.

Installation

AGP Card Installation Caution



- 1. AGP card component is blocked by DIMM socket lock.
- 2. AGP slot clicker is not locked.
- 3. AGP card edge connector is not inserted properly.





- 1. AGP card component is not blocked by DIMM socket lock.
- 2. AGP slot clicker is locked.
- 3. AGP card edge connector is inserted properly.
- 1. AGP slot clicker is not locked.
- 2. AGP card edge connector is not inserted properly.







- 1. AGP slot clicker is locked.
- 2. AGP card edge connector is inserted properly.





3-5 Device Connectors



Figure 14 - I/O Ports



JCPU_FAN/JPWR_FAN/JSYS_FAN:

CPU/Chassis Fan Power Connectors

JCPU_FAN: The CPU must be kept cool by using a heatsink with fan assembly.

JPWR_FAN: If you are installing an additional fan in the unit, connect to this fan connector.

JSYS_FAN: The chassis fan will provide adequate airflow throughout the chassis to prevent overheating the CPU.





S/PDIF: Sony/Philips Digital InterFace connector This connector links digital audio between the mainboard and your audio devices, such as CD player, sampler or DAT recorder. It allows the digital transmission of audio data in S/PDIF format.





FDD: Floppy drive Connector This mainboard is equipped with a floppy disk drive connector for connecting up to 2 floppy disk drives.

IDE1/IDE2: Ultra DMA-66/100/133 Primary/Secondary IDE Connector This mainboard is equipped with 2 IDE disk connectors for connecting up to 4 ATA-133 IDE drives. It supports PIO and DMA mode operations for maximum data transfer rate of 133MB/sec per channel.

When use two IDE drives, one must be set to Master mode and the other one to Slave mode. Refer to your disk drive user's manual for information about selecting the proper drive switch settings.



PW1: 20-pin ATX Power Connector

PW2: 4-pin ATX12V Power Connector

The mainboard is equipped with a standard 20-pin ATX main power connector and a 4-pin +12V power connector for connecting an ATX12V power supply. The plugs of the power cables are designed to fit in only one orientation. Find the proper orientation then insert the plugs into the connectors until they fit in place.

Caution: The PW1 and PW2 Power Connector must be used simultaneously or else this system will not boot-up.

The board requires a minimum of 250 Watt power supply to operate. Your system configuration (amount of memory, add-in cards, peripherals, etc.) may exceed this minimum power requirement. To ensure that adequate power is provided, use a 300 Watt or greater power supply.



CFPA: Front Panel Audio Connector When the jumpers are removed this connector can be used for front panel audio. The front panel line-out phone jack should have a "normal close" switch . Without a phone- plug inserted, the rear panel audio is enabled. With phone plug inserted, the rear panel audio will be disabled.



Settings

Pins (5-6) & (9-10) Short (default): Only the onboard rear panel audio jack can be used.

Pins (5-6) & (9-10) Open: Only front panel audio jack can be used.

In 2-Channel audio mode, Mic-In is shared for both front panel and rear panel. In 6-Channel audio mode, the Mic-In is dedicated for front panel use, and rear panel Mic-In function will switch to Center and Subwoofer support.



CD-IN/AUX-IN: CD Audio_IN Connector The CD-IN and AUX-IN connectors are used to receive audio form a CD-ROM drive, TV tuner or MPEG card.



AUX-IN AUX_IN_Right 1 AUX_IN_Left

Installation



GAME1: Game/MIDI connector This port works well with any application that is compatible with the standard PC joystick.





SATA1 / SATA2: Serial ATA Connectors
SATA3 / SATA4 / SATA5 / SATA6 (Optional):
Serial ATA Connectors support by Silicon Image
Sil3114 chip.

These connectors enable you to connect Serial ATA devices that conform to the Serial ATA specification.



There is an optional S-ATA with Game port bracket for external storage connection. This bracket connects to SATA3/SATA4/STAT5/SATA6 connector only.



CUSB3/CUSB4: Four USB 2.0 ports

This mainboard includes 4 additional onboard USB ports, identified by two 10-pin connector.

If you wish to use the additional USB ports, install the card-edge bracket to the system chassis then insert its cables to this 10-pin connector.

USB2.0 allows data transfer speed up to 480Mbps.



CAUTION!

Please make sure the USB cable has the same pin assignment. A different pin assignment may cause damage to the system. If you need the USB cable, please contact our retailer.



LED: 80 Port Debug LED

Provides two-digit POST code to show why the system fail to boot. Allows quick and easy optimization.



80 Port Debug 7-segment LED display (Refer to Appendix D for POST codes)

Installation



CFP



CIR/CSPK



CFP: Front Panel Connector

• HD_LED

This LED will light up whenever the hard drive is being accessed.

PWR_LED

This connects to the power button of the system chassis

• RST

This switch allows you to reboot without having to power off the system thus prolonging the life of the power supply or system.

• PW_ON

This is connected to the power button on the case. Using the Soft-Off by Pwr-BTTN feature, you can choose either Instant Off (turn off system immediately) or 4 sec delay (push the button for 4 seconds to turn off the system). When the system is in 4 sec delay mode, suspend mode is enabled by pushing the button momentarily.

CIR: IR connector

Connect your IrDA cable to the IR connector.

1.VCC	4.GND
2.CIRRX	5. IRTX
3.IRRX	

CSPK: Speaker

Connect to the system's speaker for beeping 1.VCC 3.GND 2.NC 4. Speaker

3-6 STR (Suspend To RAM) Function

This mainboard supports the STR (Suspend To RAM) power management scheme by maintaining the appropriate power states in the DDR SDRAM interface signals. The power source to the DDR SDRAM is kept active during STR (ACPI S3). Advanced Configuration Power Interface (ACPI) provides many Energy Saving Features for operating systems that support Instant ON and QuickStart[™] function.

- 1. To enable STR functionality to save system power :
 - a. Install ACPI certified add-on cards (such as AGP, LAN, and modem cards).
 - b. In BIOS, under Power Management Setup (refer to Section 4), select "ACPI Suspend Type: S3(STR)". If you have a USB mouse or keyboard, set "USB Resume from S3" to "Enabled".
 - c. Install Windows® XP/2000/ME/98SE.
 - d. Restart the system.
 - e. When in Windows, open the Control Panel Power Management application, and click the Advanced tab. In the Power buttons section, select "Stand By" from the drop-down lists.
- 2. To activate the STR function:
 - a. Click the START button and choose Shut Down.
 - b. In the Shut Down Windows dialog box, select the Stand By option to enter STR mode.

The following are the differences between STR power saving mode and Suspend (Power On Suspend) mode:

- a. STR is the most advanced Power Management mode.
- b. STR cuts all the power supplied to peripherals except to memory max. power saving.
- c. STR saves and keeps all on-screen data including any executed applications to DDR SDRAM.
- d. In STR mode, you must push the power button (connected to the onboard PW-On of CFP pin), click your USB mouse buttons, or press your USB keyboard keys to wake up your system to the last display.

3-7 CPU Overheating Protection

This mainboard is equipped with CPU Overheating Protection. It will automatically remove power to shutdown the system when CPU temperature reaches approximately 110°C. This is to prevent long term damage to the CPU from overheating. When this happens, the speaker produces a sustained beep sound and the system will not be able to power on. This protection is designed through hardware and no BIOS setup is required.

To power on your system again,

- Step 1: Unplug the ATX power cord (or turn off the ATX power supply switch)
- **Step 2**: Wait a few minutes for the CPU to cool.
- **Step 3**: Check that the CPU heatsink and cooling fan assembly is properly installed. **Be careful when touching the heatsink as it may be hot.**
- **Step 4**: Plug back the ATX power cord (or turn on the ATX power supply switch) and power -on the system.

If the beeping sound persists, repeat Step 1 to remove the power source.

Note: The CPU Overheating Protection will function only if the CPU is designed with an internal thermal diode. AMD Athlon XP or later CPUs is equipped with internal thermal diode.

Section 4 BIOS SETUP

Main Menu

The ROM BIOS contains a built-in Setup program which allows user to modify the basic system configuration and hardware parameters. The modified data is stored in a battery-backed CMOS, so that data will be retained even when the power is turned off. In general, the information saved in the CMOS RAM will stay unchanged unless there is a configuration change in the system, such as hard drive replacement or a device is added.

It is possible for the CMOS battery to fail causing CMOS data loss. If this happens you will need install a new CMOS battery and reconfigure your BIOS settings.

The BIOS setup screen and description are for reference only, and may not exactly match what you see on your screen. The contents of BIOS are subject to change without notice. Please visit our website for updates.

To enter the Setup Program :

Power on the computer and press the key during the POST (Power On Self Test). The BIOS CMOS SETUP UTILITY opens.

Phoenix - AwardBIOS CMOS Setup Utility		
► Standard CMOS Features	► Power BIOS Features	
Advanced BIOS Features	Load Fail-Safe Defaults	
Advanced Chipset Features	Load Optimized Defaults	
Integrated Peripherals	Set Supervisor Password	
▶ Power Management Setup	Set User Password	
PnP/PCI Configurations	Save & Exit Setup	
► PC Health Status	Exit Without Saving	
Esc : Quit F9 : Menu in BIOS ↑↓ → ← : Select Item F10 : Save & Exit Setup		
Time, Date, Hard Disk Type		

Figure 1: CMOS Setup Utility

The main menu displays all the major selection items. Select the item you need to reconfigure. The selection is made by moving the cursor (press any direction (arrow key) to the item and pressing the 'Enter' key. An on-line help message is displayed at the bottom of the screen as the cursor is moved to various items which provides a better understanding of each function. When a selection is made, the menu of the selected item will appear so that the user can modify associated configuration parameters.

4-1 Standard CMOS Setup

Choose "STANDARD CMOS FEATURES" in the CMOS SETUP UTILITY Menu (Figure 2). Standard CMOS Features Setup allows the user to configure system settings such as the current date and time, type of hard disk drive installed, floppy drive type, and display type. Memory size is auto-detected by the BIOS and displayed for your reference. When a field is highlighted (use direction keys to move the cursor and the <Enter> key to select), the entries in the field can be changed by pressing the <PgDn> or the <PgUp> key.

Phoenix - AwardBIOS CMOS Setup Utility Standard CMOS Features			
Date (mm:dd:yy) Timo (bb:mm:ss)	Wed, Apr 2 2003	Item Help	
TDE Deimaeu Mactor	11. 9.07	Menu Level 🕞	
 IDE Frimary Master IDE Primary Slave IDE Secondary Master IDE Secondary Slave 		Change the day, month, year and century	
Drive A Drive B	[1.44M, 3.5 in.] [None]		
Video Halt On	[EGA/VGA] [All , But Keyboard]		
Base Memory Extended Memory Total Memory	640K 65472K 1024K		
†↓→+:Move Enter:Select F5: Previous Values	+/-/PU/PD:Value F10:Save F6: Fail-Safe Defaults	ESC:Exit F1:General Help F7: Optimized Defaults	

Figure 2: Standard CMOS Setup
Notes:

- If the hard disk Primary Master/Slave and Secondary Master/Slave are set to Auto, the hard disk size and model will be auto-detected.
- The "Halt On:" field is used to determine when the BIOS will halt the system if an error occurs.
- Floppy 3 Mode support is a mode used to support a special 3.5-inch drive used in Japan. This is a 3.5-inch disk that stores 1.2 MB. The default setting for this is disabled.

4-2 Advanced BIOS Features

Selecting the "ADVANCED BIOS FEATURES" option in the CMOS SETUP UTILITY menu allows users to change system related parameters in the displayed menu. This menu shows all of the manufacturer's default values for the board.

Pressing the [F1] key displays a help message for the selected item.

Phoenix – AwardBIOS CMOS Setup Utility Advanced BIOS Features		
 Hard Disk Boot Priority [Press Enter] CPU Internal Cache [Enabled] External Cache [Enabled] CPU L2 Cache ECC Checking [Enabled] Quick Power On Self Test [Enabled] First Boot Device [Hard Disk] Third Boot Device [LS120] Boot Other Device [LS120] Boot Other Device [LS120] Boot Up Floppy Drive [Disabled] Boot Up Floppy Seek [Enabled] Boot Up Floppy Seek [Enabled] Boot Up NumLock Status [On] Typematic Rate Setting [Disabled] × Typematic Delay (Msec) 250 Security Option [Setup] APTC Mode [Enabled] MPS Version Control For OS[1,4] OS Select For DRAM > 64MB [Non-OS2] Video BIOS Shadow [Enabled] 		Item Help Menu Level ► Select Hard Disk Boot Device Priority
↑↓→+:Move Enter:Select +/-/PU/PD:Value F10:Save F5: Previous Values F6: Fail-Safe Defaults	e [SC:Exit F1:General Help 7: Optimized Defaults

Figure 3: BIOS Features Setup

Hard Disk Boot Priority

This item allows you to select the hard disk boot priority.

Phoenix - AwardBIOS CMOS Setup Utility Hard Disk Boot Priority	
1. Pri.Master:	Item Help
2. Fr1.Slave : 3. Sec.Master: 4. Sec.Slave : 5. USBHDD0 : 6. USBHDD1 : 7. USBHDD2 : 8. Bootable Add-in Cards	Menu Level ►► Use <↑> or <↓> to select a device , then press <+> to move it up , or <-> to move it down the list. Press <esc> to exit this menu.</esc>

CPU Internal Cache

This controls the status of the processor's internal cache area. The default is Enabled.

Options: Enabled, Disabled.

External Cache

This controls the status of the external (L2) cache area. The default is Enabled. Options: Enabled, Disabled.

CPU L2 Cache ECC Checking

This item allows you to enable/disable CPU L2 Cache ECC checking. Options: Enabled, Disabled.

Quick Power On Self Test

This category speeds up the Power On Self Test (POST). The default is Enabled. Enabled: This setting will shorten or skip of the items checked during POST. Disabled: Normal POST.

First /Second/Third/Other Boot Device

The BIOS attempts to load the operating system from the devices in the sequence selected in these items.

Options: Floppy, LS120, Hard Disk, CDROM, ZIP100, USB-FDD, USB-ZIP, USB-CDROM, Legacy LAN, Disabled.

Boot Other Device

When enabled, the system searches all other possible locations for an operating system if it fails to find one in the devices specified under the first, second, and third boot devices. The default is Enabled.

Options: Enabled, Disabled.

Swap Floppy Drive

This will swap your physical drive letters A & B if you are using two floppy disks. Options: Enabled, Disabled.

Boot Up NumLock Status

This controls the state of the NumLock key when the system boots. The default is On.

On: The keypad acts as a 10-key pad.

Off: The keypad acts like cursor keys.

Typematic Rate Setting

This determines the keystrokes repeat rate. The default is Disabled.

Enabled: Allows typematic rate and typematic delay programming.

Disabled: The typematic rate and typematic delay will be controlled by the keyboard controller in your system.

Typematic Rate (Chars/Sec)

This is the number of characters that will be repeated by a keyboard press. The default is 6.

Options: $6 \sim 30$ characters per second.

Typematic Delay (msec)

This setting controls the time between the first and the second character displayed by typematic auto-repeat. The default is 250.

Options: 250/500/750/1000 msec.

Gate A20 Option

This refers to the way the system addresses memory above 1 MB (extended memory).

Normal: The A20 signal is controlled by the keyboard controller or chipset hardware.

Fast: The A20 signal is controlled by Port 92 or chipset specific method.

Security Option

This category allows you to limit access to the System and Setup, or just to Setup.

- **System**: The system will not boot and the access to Setup will be denied if the correct password is not entered at the prompt.
- Setup: The system will boot; but the access to Setup will be denied if the incorrect password is not entered at the prompt.

APIC Mode

This item allows you to enable APIC (Advanced Programmable Interrupt Controller) functionality. APIC is a chip that provides symmetric multiprocessing (SMP) for its Pentium systems. The default is Disabled. Options: Enabled, Disabled.

Options. Enabled, Disabled.

MPS Version Control For OS

Specifies the Multiprocessor Specification (MPS). Version 1.4 supports multiple PCI bus configurations by incorporating extended bus definitions. Enable this for Windows NT or Linux. For older operating systems, select Version 1.1. Options: 1.1, 1.4.

OS Select For DRAM > 64 MB

Some operating systems require special handling. Use this option only if your system has greater than 64 MB of memory. The default is Non-OS2.

OS2: Select this if you are running the OS/2 operating system with greater than 64 MB of RAM.

Non-OS2: Select this for all other operating systems and configurations.

Video BIOS Shadow

This option allows video BIOS to be copied into RAM. Video Shadowing will increase the video performance of your system.

Options: Enabled, Disabled.

4-3 Advanced Chipset Features

Choose the "ADVANCED CHIPSET FEATURES" option in the CMOS SETUP UTILITY menu to display following menu.



Figure 4: Chipset Features Setup

VLink Data Rate

This item allows you to select VLink Data Rate. Options: 4X, 8X.

Init Display First

If two video cards are used (1 AGP and 1 PCI) this specifies which one will be the primary display adapter. The default is PCI Slot. Options: PCI Slot, AGP.

System BIOS Cacheable

This item allows the system to be cached in memory for faster execution. The default is Enabled.

Options: Disabled, Enabled.

DRAM Configuration

Scroll to DRAM Configuration and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility DRAM Configuration		
Current FSB Frequency		Item Help
DDR Timing Setting by x Max Memclock (Mhz)	[Auto] 200	Menu Level 🍉

Current FSB Frequency

Display the current CPU front side bus frequency information.

Current DRAM Frequency

Display the current DRAM frequency information.

DDR Timing Selecting by

This option sets DRAM access timing parameters. Select "Auto" to follow SPD (Serial Presence Detect) information on DRAM module or set to "Manual" to set by next item selection.

Options: Manual, Auto.

Max Memclock (Mhz)

This item sets the memory clock. By placing an artificial memory clock limit on the system. Memory is prevented from running faster than this frequency. Example, "200" will set to DDR400 speed.

Options: 100, 133, 166, 200.

AGP & P2P Bridge Control

Scroll to AGP & P2P Bridge Control and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility AGP & P2P Bridge Control		
AGP Aperture Size	[128M]	Item Help
AGP Driving Control	[Auto]	Menu Level 🕨 🕨
AGP Fast Write AGP Master 1 WS Write AGP Master 1 WS Read	[Disabled] [Disabled] [Disabled]	

AGP Aperture Size (MB)

This item defines the size of the aperture if you use an AGP graphics adapter. It refers to a section of the PCI memory address range used for graphics memory. Options: 32M, 64M, 128M, 256M.

AGP Mode

Chipset AGP Mode support. Options: 1X, 2X, 4X, 8X.

AGP Driving Control

This item allows you to adjust the AGP driving force. Choose "Manual" to key in a AGP Driving Value in the next selection. This field is recommended to set in "Auto" to avoid any error to your system. Options: Auto, Manual.

AGP Fast Write

Selecting Enabled allows to use Fast Write Protocol for 4X AGP card. Options: Enabled, Disabled.

AGP Master 1 WS Write

When Enabled, Writes to the AGP (Accelerated Graphics Port) are executed with one wait states.

Options: Enabled, Disabled.

AGP Master 1 WS Read

When Enabled, Reads to the AGP (Accelerated Graphics Port) are executed with one wait states.

Options: Enabled, Disabled.

LDT & PCI Bus Control

Scroll to LDT & PCI Bus Control and press < Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility LDT & PCI Bus Control		
Upstream LDT Bus Width	[16 bit]	Item Help
Downstream LDT bus wigth LDT Bus Frequency PCI1 Master 0 WS Write PCI2 Master 0 WS Write PCI1 Post Write PCI2 Post Write PCI2 Delay Transaction	[16 DIT] [800 MHz] [Enabled] [Enabled] [Enabled] [Enabled] [Disabled]	Menu Level ►►

Upstream LDT Bus Width

Options: 8 bit, 16 bit.

Downstream LDT Bus Width

Options: 8 bit, 16 bit.

LDT Bus Frequency

This item sets CPU Hyper Transport front size bus. Options: Auto, 800MHz, 600MHz, 400MHz, 200MHz.

PCI1/2 Master 0 WS Write

When Enabled, Writes to the PCI bus are commanded with zero wait states. Options: Enabled, Disabled.

PCI1/2 Post Write

Enables CPU to PCI bus POST write. Options: Enabled, Disabled.

PCI Delay Transaction

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select Enabled to support compliance with PCI specification version 2.2. Options: Enabled, Disabled.

4-4 Integrated Peripherals

Phoenix - AwardBIOS CMOS Setup Utility Integrated Peripherals		
► VIA OnChip IDE Device	e [Press Enter]	Item Help
► SuperIO Device	[Press Enter]	Menu Level ►
1↓→+:Move Enter:Select F5: Previous Values	+/-/PU/PD:Value F10:Save F6: Fail-Safe Defaults	ESC:Exit F1:General Help F7: Optimized Defaults

Figure 5: Integrated Peripherals

▶ VIA OnChip IDE Device

Scroll to VIA Onchip IDE Device and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility VIA OnChip IDE Device		
SATA Mode	[RAID]	Item Help
OnChip IDE Channel0	[Enabled]	Menu Level 🕨 🕨
OnChip IDE Channell IDE Prefetch Mode	lEnabled] [Enabled]	
Primary Master PIO Primary Slave PIO	[Auto] [Auto]	
Secondary Master PIO	[Auto]	
Primary Master UDMA	[Auto]	
Secondary Master UDMA	[Auto]	
Secondary Slave UDMA IDE HDD Block Mode	[Auto] [Enabled]	

SATA Mode

This allows you to set RAID mode for Serial ATA devices. Refer to section 5 for more RAID information.

Options: RAID, IDE.

IDE DMA transfer access

Automatic data transfer between system memory and IDE device with minimum CPU intervention. This improves data throughput and frees CPU to perform other tasks. Options: Enabled, Disabled.

OnChip IDE Channel0/1

The mainboard supports two channel of ordinary IDE interface. Select "Enabled" to activate each channel separately.

Options: Enabled, Disabled.

Note: If you do not use the onboard IDE connector, set the Onboard Primary PCI IDE and Onboard Secondary PCI IDE to "Disabled".

IDE Prefetch Mode

Selecting "Enabled" reduces latency between each drive read/write cycle, but may cause instability in IDE subsystems that cannot support such fast performance. If you are getting disk drive errors, try setting this value to Disabled. This field does not appear when the Internal PCI/IDE field, above, is Disabled. Options: Enabled, Disabled.

Primary/Secondary Master/Slave PIO

The four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 to 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

Options: Auto, Mode $0 \sim 4$.

Primary/Secondary Master/Slave UDMA

Select the mode of operation for the IDE drive. Ultra DMA-33/66/100/133 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver. If your hard drive and your system software both support Ultra DMA-33/66/100/133, select Auto to enable UDMA mode by BIOS. Options: Auto, Disabled.

IDE HDD Block Mode

Block mode is also called block transfer, multiple commands, or multiple sector read/ write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

Options: Enabled, Disabled.

▶ VIA Onchip PCI Device

Scroll to VIA Onchip PCI Device and press < Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility VIA OnChip PCI Device		
VIA AC97 Audio	[Auto]	Item Help
Midi Port Address	[Disabled]	Menu Level 🕨
VIA OnChip LAN VIA-6102 MAC Address Inn	[Auto] uut[Press Enter]	
Onboard Lan Boot ROM OnChip USB Controller	[Disabled] [All Enabled]	
OnChip EHCI Controller USB Keyboard Support USB Mouse Support	[Enabled] [Disabled] [Disabled]	

VIA AC97 Audio

This item allows you to disable the chipset on-chip support for AC97 Audio.

Options: Auto, Disabled.

Game Port Address

Select an address for the Game port. Options: 201, 209, Disabled.

Midi Port Address

Select an address for the Midi port. Options: 290, 300, 330, Disabled.

Midi Port IRQ

Select an interrupt for the Midi port. Options: 5, 10

VIA Onchip LAN (Optional)

Enables the onboard LAN feature. Options: Auto, Disabled.

VIA-6102 MAC Address Input (Optional)

Allwos you to input MAC address.

Onboard Lan Boot ROM (Optional)

Enable/disable the onboard LAN Boot ROM. The default is Disabled. Options: Enabled, Disabled.

Onchip USB Controller

Enables the USB controller. Options: Enabled, Disabled.

Onchip EHCI Controller

Enables the EHCI (USB2.0) controller. Options: Enabled, Disabled.

USB Keyboard Support

Enable/disable support for USB keyboard under DOS. Options: Enabled, Disabled.

USB Mouse Support

Enable/disable support for USB mouse under DOS. Options: Enabled, Disabled.

Super IO Device

Scroll to Super IO Deivce and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility SuperIO Device		
Onboard FDC Controller	[Enabled]	Item Help
Unboard Serial Port 1	13F8/1R041	
Onboard Serial Port 2	[2F8/IRQ3]	Menu Level 🕨 🕨
UART Mode Select	[Normal]	
RxD , TxD Active	[Hi,Lo]	
IR Transmission Delav	[Enabled]	
UR2 Duplex Mode	[Half]	
Use IR Pins	[TR-Rx2Tx2]	
Onboard Parallel Port	[378/TR07]	
Parallel Port Mode	[SPP]	
FPP Mode Select	[FPP1.71	
ECP Mode Use DMA	[3]	

Onboard FDC Controller

Select "Enabled" if you wish to use onboard floppy disk controller (FDC). If you install an external FDC or the system has no floppy drive, select "Disabled "in this field.

Options: Enabled, Disabled.

Onboard Serial Port 1/2

Select an address and corresponding interrupt for the first and second serial ports. Options: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto.

UART Mode Select

This field configures the 2nd serial port for IR application. Select the required IR protocol or select "Normal" to disable IR mode. Options: Normal, IrDA and ASKIR.

RxD, TxD Active

When the above UART Mode Select is in IR mode, this field configures the receive and transmit signals generated from the IR port. Options: Hi-Hi, Hi-Lo, Lo-Hi, and Lo-Lo.

IR Transmission delay

This item allows you to enabled/disable IR transmission delay. Options: Enabled, Disabled.

UR2 Duplex Mode

This item allows you to select IR half/full duplex function. Options: Half, Full.

Use IR Pins

This item allows you to select IR transmission routes, one is RxD2, TxD2 (COM Port) and the other is IR-Rx2Tx2.

Options: IR-Rx2Tx2; RxD2, TxD2.

Onboard Parallel Port

This field allows the user to configure the LPT port. Options: 378/IRQ7, 278/IRQ5, 3BC/IRQ7, Disabled.

Parallel Port Mode

This field allows the user to select the parallel port mode. Options: SPP, EPP, ECP, ECP+EPP, Normal.

EPP Mode Select

This field allows the user to select the EPP mode for parallel port mode. Options: EPP1.9, EPP1.7.

ECP Mode USE DMA

This field allows the user to select DMA1 or DMA3 for the ECP mode. Options: DMA1, DMA3.

4-5 Power Management Setup

Choose the "Power Management setup" in the CMOS Setup Utility to display the following screen. This menu allows the user to modify the power management parameters and IRQ signals. In general, these parameters should not be changed unless it's absolutely necessary.

Phoenix - AwardBIOS CMOS Setup Utility Power Management Setup		
ACPI function	[Enabled]	Item Help
HUPI Suspend Type Power Management Option HDD Power Down Suspend Mode Video Off Option Video Off Method MODEM Use IRQ Soft-Off by PWRBIN Run VGABIOS if S3 Resume Ac Loss Auto Restart • IRQ/Event Activity Detect	ISTRUSJ [User Define] [Disable] [Disable] [Suspend -> Off] [V/H SYNC+Blank] [3] [Instant-Off] [Yes] [Off] [Press Enter]	Menu Level ►
1↓→+:Move Enter:Select +/- F5: Previous Values F6	/PU/PD:Value F10:Save : Fail-Safe Defaults	ESC:Exit F1:General Help F7: Optimized Defaults

Figure 6: Power Management

ACPI Function

Enables the APCI function. Options: Enabled, Disabled.

ACPI Suspend Type

This item allows you to select S1(Power-On-Suspend) or S3(Suspend-To-RAM) function.

Options: S1(POS), S3(STR), S1&S3.

Power Management Option

Use this to select your Power Management selection. The default is User define.

Max.saving: Maximum power savings. Inactivity period is 1 minute in each mode.

Min. saving: Minimum power savings. Inactivity period is 1 hour in each mode.

User define: Allows user to define PM Timers parameters to control power saving mode.

HDD Power Down

Powers down the hard disk drive after a preset period of system inactivity. Options: Disabled, 1min ~ 15min.

Suspend Mode

Automatically, shuts off all devices except the CPU after a preset period of system inactivity.

Options: Disabled, 1, 2, 4, 6, 8, 10, 20, 30, 40 min and 1 hour.

Video Off Option

When enabled, this feature allows the VGA adapter to operate in a power saving mode.

Always On: Monitor will remain on during power saving modes.

Suspend->Off: Monitor blanked when the systems enters the suspend mode.

Video Off Method

This option allows you to select how the video will be disabled by the power management. The default is V/H Sync + Blank

V/H Sync + Blank:	System turns off vertical and horizontal synchronization ports and writes blanks to the video buffer.
DPMS Support:	Select this option if your monitor supports the Display Power Management Signaling (DPMS) standard of the Video Electronics Standards Association (VESA). Use the software supplied by your video subsystem to select video power management values.
Blank Screen:	System only writes blanks to the video buffer.

MODEM Use IRQ

Select the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system. Default is IRQ 3. Options: N/A, 3, 4, 5, 7, 9, 10, 11

Soft-Off by PWRBTN

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state when the system has "hung." The default is Instant-off. Options: Delay 4 Sec, Instant-Off.

Run VGABIOS if S3 Resume

This determines whether or not to enable the system to run the VGA BIOS when resuming from S3(STR) or S1&S3.

Options: Auto, Yes, No.

AC Loss Auto Restart

Configures the system to auto-restart or remain off after a power interrupt.

Off: System remains off after a power interrupt.

On: System always restarts after a power interrupt.

Auto: Depends on whether the system was safely shutdown before power failure.

IRQ/Event Activity Detect

Scroll to IRQ/Event Activity Detect and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility IRQ/Event Activity Detect		
PS2KB Wakeup Select [Hot key] PS2KB Wakeup from S2(St/S5[Ctrl+[1]	Item Help	
PS2MS Wakeup from S3/S4/S5[Disabled]	Menu Level 🛛 🕨	
PowerOn by PCI Card [Disabled]	When Select Password,	
PowerOn by OnBoard LAN [Disabled] Modem Ring Resume [Disabled]	Please press ENTER key to change Password	
RTC Alarm Resume [Disabled]	Max 8 numbers.	
x Resume Time (hh:mm:ss) 0°: 0: 0 ▶ IRQs Activity Monitoring [Press Enter]		

PS2KB Wakeup Select

This item allows you to select Hot Key or Password to wake-up the system by PS2 Keyboard. If Password is selected, press <Enter> key to change a password with max 8 numbers.

Options: Hot key, Password.

PS2KB Wakeup form S3/S4/S5

This item allows you to set a Hot Key to wake-up the system by PS2 Keyboard. Options: Disabled, Ctrl+F1, Ctrl+F2, Ctrl+F3, Ctrl+F4, Ctrl+F5, Ctrl+F6, Ctrl+F7, Ctrl+F8, Ctrl+F9, Ctrl+F10, Ctrl+F11, Ctrl+F12, Power, Wake, Any key. Note: Power and Wake are Windows98 Keyboard button.

PS2MS Wakeup form S3/S4/S5

This item allows you to wake-up the system by PS2 Mouse. Options: Enabled, Disabled.

USB Resume from S3

This item allows a USB device to wake-up the system from S3 suspend state. Options: Enabled, Disabled.

PowerOn by PCI Card

An input signal from PME on the PCI card awakens the system from soft-off state. Options: Enabled, Disabled.

PowerOn by OnBoard LAN

This item allows you to power on the system by onboard LAN from soft-off state. Options: Enabled, Disabled.

Modem Ring Resume

When set to "Enabled", any event occurring to the Modem Ring will awaken the system from suspend state. Options: Enabled, Disabled.

RTC Alarm Resume

When set to Enable rtc alarm resume, you can set the date (of month) and time (hh: mm:ss), that will awaken a system which has been powered down.

4-6 PNP/PCI Configuration

This page lets the user to modify the PCI IRQ signals when various PCI cards are inserted.

WARNING: Conflicting IRQ's may cause system unable to locate certain devices.

Phoenix - AwardBIOS CMOS Setup Utility PnP/PCI Configurations				
PNP OS Installed Reset Configuration Data Resources Controlled By × IRQ Resources PCI/VGA Palette Snoop Assign IRQ For VGA Assign IRQ For USB	<pre>[No] [Disabled] [Auto(ESCD)] Press Enter [Disabled] [Enabled] [Enabled] [Enabled]</pre>	Item Help Menu Level ► Select Yes if you are using a Plug and Play capable operating system Select No if you need the BIOS to configure non-boot devices		
-1↓→+:Move Enter:Select +/- E5: Previous Values – Ef	-/PU/PD:Value F10:Save 5: Fail-Safe Defaults	ESC:Exit Fl:General Help F7: Optimized Defaults		

Figure 7: PNP/PCI Configuration Setup

PNP OS Installed

Select "Yes" if you have a PNP OS installed on your system. The default is No. Options: Yes, No.

Reset Configuration Data

This setting allows you to clear ESCD data. The default is Disabled

- Disabled: Normal Setting.
- **Enabled**: If you have ever plugged Legacy cards to the system and their data remains recorded into ESCD (Extended System Configuration Data), you can set this field to "Enabled" in order to clear ESCD data.

Resources Controlled By

Determines what controls system PNP/PCI resources. The default is Auto (ESCD).

- **Manual:** PNP Card's resources are controlled manually. The "IRQ Resources" field becomes available and you can set which IRQ-X and DMA-X are assigned to PCI and onboard devices.
- Auto: BIOS assigns the interrupt resource automatically.

PCI/VGA Palette Snoop

This item is designed to overcome problems that may be caused by some nonstandard VGA cards.

Options: Enabled, Disabled.

Assign IRQ For VGA

This item requests BIOS to assign an IRQ for the VGA. Selecting "Disabled" will free the IRQ for use by other devices.

Options: Enabled, Disabled.

Assign IRQ For USB

This item requests BIOS to assign an IRQ for the USB port. If you have not connected any USB device, selecting "Disabled" will free the IRQ for use by other devices.

Options: Enabled, Disabled.

Interrupt requests are shared as shown below:

	INT A	INT B	INT C	INT D
PCI 1		v		
PCI 2			v	
PCI 3				v
PCI4	v			
PCI 5		v		
AGP Slot	v			
AC97			v	
Onboard USB1	v			
Onboard USB2	v			
Onboard USB3		v		
Onboard USB4		v		
USB2.0			v	
Onboard S-ATA (Optional)			v	
Onboard LAN (Optional)	v			

IMPORTANT!

When using PCI cards on shared IRQ slots, make sure its drivers support "Shared IRQ", or that the cards do not need IRQ assignments. IRQ conflicts between the two PCI groups will make the system unstable or cards inoperable.

4-7 PC Health Status



Figure 8: PC Health Status

Show PC Health in POST

When this function is enabled the PC Health information is displayed during the POST (Power On Self Test).

Options: Disabled, Enabled.

CPU Warning Temperature

Sets the temperature at which the computer will respond to an overheating CPU. Options: Disabled, $50^{\circ}C/122^{\circ}F \sim 70^{\circ}C/158^{\circ}F$.

Current System/CPU Temperature

Displays the current system/CPU temperature.

Current CPU/Chassis FAN Speed

Displays the current speed of the CPU and chassis fan speed in RPMs.

Vcore

The voltage level of the CPU(Vcore).

VDIMM

The voltage level of the DRAM.

Vagp

The voltage level of power supplied to AGP card.

VBAT(V)

The voltage level of the battery.

+ 5V, +12V, Vio, 5VSB(V)

The voltage level of the switching power supply.

Shutdown Temperature

This is the temperature that the computer will turn off the power to combat the effects of an overheating system. (requires ACPI to be enabled in Power Management BIOS and ACPI compliant operating system.) The default is Disabled. Options available are 60° C/140°F to 75° C/167°F in increments of 5°C.

4-8 Power BIOS Features

This page lets you adjust various parameters to obtain improved performance for overclocking.

Warning: Overclocking requires expert knowledge and risks permanent damage to system components. We recommend you leave these parameters at their default values for proper operation.

Phoenix - AwardBIOS CMOS Setup Utility Power BIOS Features					
Auto Detect PCI Clk	[Enabled]	Item Help			
CPU Clock	[200MHz]	Menu Level 🔹			
Vcore Default Voltage Current Voltage Adjust Voltage	[Default]				
DIMM Default Voltage Add Voltage New Voltage	2.50 V [+0.00 V]				
AGP Default Voltage Add Voltage New Voltage	1.50 V [+0.00 V]				
↑↓→+:Move Enter:Select F5: Previous Values	+/-/PU/PD:Value F10:Save F6: Fail-Safe Defaults	ESC:Exit F1:General Help F7: Optimized Defaults			

Figure 9: Frequency/Voltage Control

Auto Detect PCI Clk

When enabled the mainboard automatically disables the clock source for a PCI slot which does not have a module in it, reducing EMI (ElectroMagnetic Interference). Options: Enabled, Disabled.

Spread Spectrum

If you enable spread spectrum, it can significantly reduce the EMI (ElectroMagnetic Interference) generated by the system.

Options: Disabled, Enabeld.

CPU Clock

Enables you to set the CPU front side bus speed at increments of 1MHz step. Press <Enter> to display the following screen:

CPU Clock	
Min= 200 Max= 250 Key in a DEC number :	
↑↓:Move ENTER:Accept	ESC:Abort

Key in the DEC (decimalism) number for the CPU clock.

Note: Overclocking failure will cause no display on monitor. At this instant, press *<Insert>* key to revert back to the initial or default setting to boot up your system.

CPU Vcore Voltage

This item allows you to set the CPU Vcore voltage.

Options: Default, 1.350V to 1.550V in 0.025V increment and 1.550V to 1.750V in 0.050V increment. We recommend that you leave this at the default value.

DIMM Voltage

This item allows you to adjust the DIMM slot voltage.

Options: +0.00V to +0.30V in 0.10V increments. We recommend that you leave this at the default value.

AGP Voltage

This item allows you to adjust the AGP slot voltage.

Options: +0.00V to +0.30V in 0.10V increments. We recommend that you leave this at the default value.

4-9 Defaults Menu

Selecting "Defaults" from the main menu shows you two options which are described below

Load Fail-Safe Defaults

When you press <Enter> on this item you get a confirmation dialog box:

```
Load Fail-Safe Defaults (Y/N)? N
```

Pressing 'Y' loads the BIOS default values for the most stable, minimal-performance system operations.

Load Optimized Defaults

When you press <Enter> on this item you get a confirmation dialog box:

```
Load Optimized Defaults (Y/N)? N
```

Pressing 'Y' loads the default values that are factory settings for optimal performance system operations.

4-10 Supervisor/User Password Setting

This function lets you set either Supervisor or User Password, or both, to prevent unauthorized changes to BIOS menus.

supervisor password: full rights to enter and change options of the setup menus.user password: only enter but no rights to change options of the setup menus.

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

ENTER PASSWORD:

Type the password, up to eight characters in length, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable a password, just press <Enter> when you are prompted to enter the password. A message will confirm the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED.

When a password has been enabled, you will be prompted to key in each time you enter Setup. This prevents an unauthorized person from changing any part of your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time your system is rebooted. This would prevent unauthorized use of your computer.

You can determine when the password is required within the Advanced BIOS Features Menu and its Security option. If the Security option is set to "System", the password will be required both at boot and at entry to Setup. If set to "Setup", prompting only occurs when trying to enter Setup.

4-11 Exiting BIOS

Save & Exit Setup

Pressing <Enter> on this item asks for confirmation:

Save to CMOS and EXIT (Y/N)? Y

Pressing "Y" stores the selections made in the menus in CMOS - a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS. After saving the values the system is restarted again.

Exit Without Saving

Pressing <Enter> on this item asks for confirmation:

Quit without saving (Y/N)? Y

This allows you to exit Setup without storing in CMOS any change. The previous selections remain in effect. This exits the Setup utility and restarts your computer.

Section 5 S-ATA RAID CONFIGURATION

Introduction

This section gives a brief introduction on the RAID-related background knowledge and a brief introduction on VIA SATA RAID Host Controller. For more detailed information, please refer to user's manual in the attached CD.

RAID Basics

RAID (Redundant Array of Independent Disks) is a method of combining two hard disk drives into one logical unit. The advantage of an Array is to provide better performance or data fault tolerance. Fault tolerance is achieved through data redundant operation, where if one drives fails, a mirrored copy of the data can be found on another drive. This can prevent data loss if the operating system fails or hangs. The individual disk drives in an array are called "members". The configuration information of each member is recorded in the "reserved sector" that identifies the drive as a member. All disk members in a formed disk array are recognized as a single physical drive to the operating system.

Hard disk drives can be combined together through a few different methods. The different methods are referred to as different RAID levels. Different RAID levels represent different performance levels, security levels and implementation costs. The RAID levels which the VIA SATA RAID Host Controller supports are RAID 0, 1 and JBOD. The table below briefly introduced these RAID levels.

RAID Level	No. of Drivers	Capacity	Benefits
RAID 0 (Striping)	2	Number drivers *Smallest size	Highest performance without data protection
RAID 1 (Mirroring)	2	Smallest size	Data protection
JBOD (Spanning)	2	Sum of All drivers	No data protection and performance improving, but disk capacity fully used.

RAID 0 (Striping)

RAID 0 reads and writes sectors of data interleaved between multiple drives. If any disk member fails, it affects the entire array. The disk array data capacity is equal to the number of drive members times the capacity of the smallest member. The striping block size can be set from 4KB to 64KB. RAID 0 does not support fault tolerance.

RAID 1 (Mirroring)

RAID 1 writes duplicate data onto a pair of drives and reads both sets of data in parallel. If one of the mirrored drives suffers a mechanical failure or does not respond, the remaining drive will continue to function. Due to redundancy, the drive capacity of the array is the capacity of the smallest drive. Under a RAID 1 setup, an extra drive called the "spare drive" can be attached. Such a drive will be activated to replace a failed drive that is part of a mirrored array. Due to the fault tolerance, if any RAID 1 drive fails, data access will not be affected as long as there are other working drives in the array.

JBOD (Spanning)

A spanning disk array is equal to the sum of the all drives when the drives used are having different capacities. Spanning stores data onto a drive until it is full, then proceeds to store files onto the next drive in the array. When any disk member fails, the failure affects the entire array. JBOD is not really a RAID and does not support fault tolerance.

Key Features

The VIA SATA RAID solution uses the VT8237 chip as a RAID controller, which is a 2-channel SATA and 1-channel ATA133 solution. The RAID software is a Windows-based software utility with graphical user interface that provides an easy-operating tool to configure and manage disk drives or disk arrays connected to the VT8237 controller. Listed below are the main features and benefits of VIA SATA RAID:

- 1. Support two SATA hard disk drives.
- 2. Supports hard disk drive larger than 137 GB (48-bits LBA).
- 3. Supports RAID 0, 1 and JBOD.
- 4. 4 KB to 64 KB striping block size support.
- 5. Bootable disk or disk array support.
- 6. Windows-based RAID configure and management software tool. (Compatible with BIOS)
- 7. Real-time monitoring of device status and error alarm with popup message box and beeping.
- 8. Supports hot-swap failed disk drive in RAID 1 array.
- 9. Mirroring automatic background rebuilds support.
- 10. ATA SMART function support.
- 11. Microsoft Windows 98, Me, NT4.0, 2000, XP operating systems support.
- 12. Event log for easy troubleshooting.
- 13. On-line help for easy operation for RAID software.

BIOS Configuration Utility

BIOS Configuration Utility

When the system powers on, wait for the following screen to appear and press 'Tab' key to enter BIOS configuration utility.



The main interface of BIOS configuration utility is as below:

VIA	Tech. RAID BIOS	Ver 0.94
► <u>Create Array</u> ► Delete Array ► Create/Delete Sj ► Select Boot Arre ► Serial Number U:	pare ay iew	Create a BAID array with the hard disks attached to VIA IDE controller F1 : View Array/disk Status 1,1 : Move to next item Enter: Confirm the selection ESC : Exit
Channel Channel0 Master Channel1 Master	Drive Name Maxtor 64060M0 Maxtor 64060M0	Array Name Mode Size(GB) Status AIA 133 57.26 Hdd AIA 133 57.26 Hdd AIA 133 57.26 Hdd

Create Disk Array

1. Use the arrow keys to navigate the main menu. Use the up and down arrow keys to select the **Create Array** command and press <Enter> to call out the list of creation steps.

VIA	Tech. RAID BIOS	Ver 0.94			
 Auto Setup For Data Security Array Mode RAID 1 (Mirroring) Select Disk Drives Start Create Process 		Create a RAID array with the hard disks attached to VIA IDE controller F1 : View Array/disk Status ↑,↓ : Move to next item Enter: Confirm the selection ESC : Exit			tus on
Channel	Drive Name	Array Name	Mode	Size(GB)	Status
ChannelØ Master Channel1 Master	Maxtor 6¥060M0 Maxtor 6¥060M0		ATA 133 ATA 133	57.26 57.26	Hdd Hdd

2. Select Array Mode and press <Enter>, a list of array modes will appear. High-light the target array mode that you want to create, and press <Enter> to confirm the selection. If RAID 1 is selected, an option list will popup and enable the users to select Create only or Create and duplicate. Create only will allow BIOS to only create an array. The data on the mirroring drive may be different from the source drive. Create and duplicate lets BIOS copy the data from the source to the mirroring drive.

	VIA Tech.	RAID BIOS	Ver 0.94			
► RAID Ø RAID 1 ► RAID Ø ► RAID S	for performan for data prot /1 PAN for capaci	ice section ity	Create a the hard VIA IDE F1 : V ↑,↓ : M Enter: C ESC : E	RAID ard disks at controll liew Arra love to n confirm t xit	ray with ttached to er y/disk Sta ext item he selecti	tus on
Channe Channe 10 Channe 11	l Dri Master Maxto Master Maxto	ve Name r 64060M0 r 64060M0	Array Name	Mode ATA 133 ATA 133	Size(GB) 57.26 57.26	Status Hdd Hdd

3. After array mode is selected, there are two methods to create a disk array. One method is "Auto Setup" and the other one is "Select Disk Drives". Auto Setup allows BIOS to select the disk drives and create arrays automatically, but it does not duplicate the mirroring drives even if the user selected Create and duplicate for RAID 1. It is recommended all disk drives are new ones when wanting to create an array. Select Disk Drives lets the user select the array drives by their requirements. When using Select Disk Drives, the channel column will be activated. Highlight the target drives that you want to use and press <Enter> to select them. After all drives have been selected, press <Esc> to go back to the creation steps menu.

VIA	Tech. RAID BIOS	Ver 0.94			
 ▶ Auto Setup For Performance ▶ Array Mode RAID Ø (Stripping) ▶ Select Disk Drives ▶ Block Size 64K ▶ Start Create Process 		Create a RAID array with the hard disks attached to UIA IDE controller F1 : Uiew Array/disk Status 1,4 : Move to next item Enter: Confirm the selection ESC : Exit			o atus ion
Channe 1	Drive Name	Array Name	Mode	<pre>\$ize(GB)</pre>	Status
[*]Channel0 Master [] <u>Channel1 Master</u>	Maxtor 6Y060M0 Maxtor 6Y060M0		ATA 133 ATA 133	57.26 57.26	StripeØ Hdd

4. If user selects a RAID 0 array in step 2, the block size of the array can also be selected. Use the arrow key to highlight **Block Size** and press <Enter>, then select a block size from the popup menu. The block size can be 4KB to 64KB.

VIA	Tech. RAID BIOS	Ver 0.94			
 ▶ Auto Setup For ▶ Array Mode RAID ▶ Select Disk Dri ▶ Select Size 64K ▶ Start Create Pr 		Create a RAID array with the hard disks attached to UIA IDE controller F1 : View Array∕disk Status ↑,↓ : Move to next item Enter: Confirm the selection ESC : Exit			o atus ion
Channe 1	Drive Name	Array Name	Mode	Size(GB)	Status
[*]ChannelØ Master [*]Channel1 Master	Maxtor 6Y060M0 Maxtor 6Y060M0		ATA 133 ATA 133	57.26 57.26	StripeØ Stripe1

- Use the arrow key to highlight Start Create Process and press <Enter>. A warning message will appear, Press Y to finish the creation, or press N to cancel the creation.
- 6. Important note: All existing content in the hard drive will be destroyed after array creation.

Delete Disk Array

A RAID can be deleted after it has been created. To delete a RAID, use the following steps:

- 1. Select **Delete Array** in the main menu and press <Enter>. The channel column will be activated.
- 2. Select the member of an array that is to be deleted and press <Enter>. A warning message will show up, press Y to delete or press N to cancel.



Deleting a disk array will destroy all the data on the disk array except RAID 1 arrays. When a RAID is deleted, the data on these two hard disk drives will be reserved and become two normal disk drives.

View Serial Number of Hard Drive

Highlight **Serial Number View** and press <Enter>. Use arrow key to select a drive, the selected drive's serial number can be viewed in the last column. The serial number is assigned by the disk drive manufacturer.

View Array Status

Press the **F1** key to show the array status on the lower screen. If there are no disk arrays then nothing will be displayed on the screen.

VIA	Tech. RAID BIOS	Ver 0.94	
 Create Array Delete Array Create/Delete Spare Select Boot Array Serial Number View 		Create a RAID the hard disks VIA IDE contro F1 : View Arn 1.↓ : Move to Enter: Confirm ESC : Exit	array with attached to ller ray/disk Status next item the selection
Array Name	Array Mode	Block Size(GB)	Size(GB)
ARRAY Ø	Mirror	N/A	37.27
ARRAY 1	Mirror	N/A	37.27

Duplicate Critical RAID 1 Array

When booting up the system, BIOS will detect if the RAID 1 array has any inconsistencies between user data and backup data. If BIOS detects any inconsistencies, the status of the disk array will be marked as critical, and BIOS will prompt the user to duplicate the RAID 1 in order to ensure the backup data consistency with the user data.

Critical RAID 1 Duplicate now Continue to boot	Critical Status The RAID 1 array needs to be duplicated to ensure data consistancy. Fault Hdd Found: Channel 1 Device 0 Fault
Remaining members of the failed array	
Channel Drive Name Array Name Channell Device0 IC35L0400NVN07-0 Array0 Channel0 Device0 IC35L0400VVN07-0 Array0	Mode Size(GB) Status ATA 100 38.34 Mirror ATA 100 38.34 Source
Note: 1)Press <esc> to Exit. 2)After Execute,Press <tab> immediately can int</tab></esc>	to Utility Window!

If user selects **Continue to boot**, it will enable duplicating the array after booting into OS.

Rebuild Broken RAID 1 Array

When booting up the system, BIOS will detect if any member disk drives of RAID has failed or is absent. If BIOS detects any disk drive failures or missing disk drives, the status of the array will be marked as broken.

If BIOS detects a broken RAID 1 array but there is a spare hard drive available for rebuilding the broken array, the spare hard drive will automatically become the mirroring drive. BIOS will show a main interface just like a duplicated RAID 1. Selecting **Continue to boot** enables the user to duplicate the array after booting into operating system.

If BIOS detects a broken RAID 1 array but there is no spare hard drive available for rebuilding the array, BIOS will provide several operations to solve such problem.

Broken RAID 1	A disk member of a mirroring
Destroy the Mirroring Relationship Choose replacement drive and rebuild Continue to boot	array has failed or is not responding. The array is stilling functional,but fault tolerance is disabled.
Remaining members of the failed array	
Channel Drive Name Array Na Channel0 Device0 IC35L040AVVA07-0 Array	ame Mode Size(GB) Status NØ ATA 100 38.34 Broken
Note: 1)Press <esc> to Exit. 2)After Execute,Press <tab> immediately can i</tab></esc>	into Utility Window!

1. Power off and Check the Failed Drive:

This item turns off the computer and replaces the failed hard drive with a good one. If your computer does not support APM, you must turn off your computer manually. After replacing the hard drive, boot into BIOS and select **Choose replacement drive and rebuild** to rebuild the broken array.

2. Destroy the Mirroring Relationship:

This item cancels the data mirroring relationship of the broken array. For broken RAID 1 arrays, the data on the surviving disk will remain after the destroy operation.

3. Choose Replacement Drive and Rebuild:

This item enables users to select an already-connected hard drive to rebuild the broken array. After choosing a hard drive, the channel column will be activated.

Broken ROTD 1	Critical Status
Power off and check the failed drive Destroy the Mirroring Relationship Choose replacement drive and rebuild Continue to boot	The contents on the disk you have selected will be deleted.
Remaining members of the failed array	
I I I I I I I I I I I I I I I I I I I	
Channel Drive Name Array N. ()Channel0 Device1 IC35L040AVVA07-0	ame Mode Size(GB) Status ATA 100 38.34 Hdd
Chappell Device1 TC35L060AVV007-0	ATA 100 38 36 Hdd
Note:	
1)Press (ESU) to Exit.	
2)Hfter Execute,Press <tab> immediately can</tab>	into Utility Window!

Highlight the target hard drive and press <Enter>, a warning message will appear. Press **Y** to use that hard drive to rebuild, or press **N** to cancel. Please note selecting option **Y** will destroy all the data on the selected hard drive.

4. Continue to boot:

This item enables BIOS to skip the problem and continue booting into OS.
Section 6 DRIVER INSTALLATION

Easy Driver Installation



Insert the bundled CD-disk, the main menu screen will appear. The main menu displays buttons that link you to the supported drivers, utilities and software.

- Step 1: Click "SERVICE PACK 4IN1 DRIVER" to install all components recommended.
- Step 2: Click "AC'97 AUDIO DRIVER" to install audio driver.
- Step 3: Click "VIA 6103 LAN DRIVER" to install VIA LAN driver. (Optional)
- Step 4: Click "3COM LAN DRIVER" to install 3COM LAN driver. (Optional)
- Step 5: Click "USB V2.0 DRIVER" to install USB2.0 driver.
- Step 6: If your mainboard is equipped with a Silicon Image device, please install its driver from 3.5 floppy. (Optional)
 The "SATARAID INSTALLATION UTILITY" item is for installing the Silicon Image Raid Administrator. (Optional)
- Step 7: Click "VIA SATA RAID DRIVER" to install VIA Serial ATA driver.

Realtek Sound Manager Quick User-guide

Introduction

To obtain the best performance from your audio system, run the "Sound Manager" utility to adjust the settings to suit your needs. This section of the manual is intended to provide a quick user-guide to setup "Sound Manager". For more detailed information, refer to "Sound Manager manual" in the CD.



<Figure 1>

1. Right-click "Sound Effect" button on the task bar and select "Sound Manager".

AC97 Audio Co	onfiguratio	ı						<u> </u>	Sound Effect:
Sound Effect	Equalizer	Speaker Configuration	n Speaker Test	S/PDIF-In	S/PDIF-Out	Connector S	ensing	General	
Environm	nent								
	<n.< th=""><th>one></th><th></th><th>•</th><th></th><th>Edit</th><th></th><th></th><th></th></n.<>	one>		•		Edit			
Karaoke				Other					
	Voice C channe	ancellation (only for 2 Is mode)			🗖 Auto G	ain Control			<figure 2=""></figure>
	KEY	Reset			E	qualizer			
								OK	

- 2. Select "Sound Effect" page to set the desired audio environment from the pull-down menu. There are in total 23 kinds of sound effect.
 - a. For Karaoke function, "Voice Cancellation (only for 2 channels mode)" removes the human voice. "Key" lets you adjusts the key pitch.
 - b. "Auto Gain Control" avoids saturation when adjusting the equalizer.



3. There are 10 bands of equalizer control, check "ON" when you want to adjust the equalizer.

AC97 Audio Configuration	Speaker
Sound Effect Equalizer Speaker Configuration Speaker Test S/PDIF-In S/PDIF-Out Connector Sensing General	Configuration
No. of Speaker	oonnguration.
C Headphone	<figure 4=""></figure>
C [2-channel mode for stereo speaker output]	
C 4-channel mode for 4 speaker output	
C 6-channel mode for 5.1 speaker output	
Synchronize the phonejack switch with the Mic In Mic In	
OK	

4. This page displays the mainboards's phone jack function when a corresponding audio mode (no. of speaker) is selected.

Figure 4 above shows the phone jack setup for 2 channel mode.

Drivers Installation



5. For 6 channel mode, the audio combination is shown above.



6. To test the speaker , select the **"Speaker Test"** page and click directly on the speakers shown on the screen.

AC97 Audio Configuration		_ 🗆 ×	SPDIF-In:
Sound Effect Equalizer Speaker Configuration	Speaker Test S/PDIF-In	S/PDIF-Out Connector Sensing General	
Status :	Unlock	Auto Lock	
Sampling rate :			<figure 7=""></figure>
Data Validation :			
□ Rea	time SPDIF-In monitor		
		OK	

- 7. This page shows S/PDIF IN function on your system.
 - a. Click "Auto Lock" to detect S/PDIF input and display its information.
 - b. Check "Real-time S/PDIF-In monitor" to listen to the S/PDIF IN signal through Line-out connector.

1	AC97 Audio Configuration	SPDIF-Out:
	Sound Effect Equalizer Speaker Configuration Speaker Test S/PDIF-In S/PDIF-Out Connector Sensing General	
	C No output	
	s Hobupuk	
	Output digital only	<figure 8=""></figure>
	C Output digital and analog	
	C S/PDIF-In to S/PDIF-Out pass through mode	

8. This page lets you choose the type of audio source that will appear on the S/PDIF-out connector.

This board is equipped with Jack Sensing capability. If an audio device is plugged into the wrong connector, a warning message will appear to remind users to check the connection.

AC97 Audio Configuration			Connector
Sound Effect Equalizer Speaker Confi	guration Speaker Test S/PDIF-In S	/PDIF-Out Connector Sensing General	Sensina:
Line Out Connector	Line In Connector	Mic In Connector	J
Device Type :	Device Type :	Device Type :	
?	2	2	<figure 9=""></figure>
Unknown	Unknown	Unknown	
Correction		Correction	
Option Start	Please stop other audio applications bef	ore starting.	
		ОК	

9. Push "Start" button to start the sensing. Please remember to terminate all audio applications before starting the sensing.



10. EZ-Connection shows the result of the detection.

"Audio Connector" column reflects the settings used in the "Speaker Configuration" page.

"Current Connection" column shows the type of device detected. If the results do not match, an exclamation mark will appear on the right side.

AC97 Audio Configuration Sound Effect Equalizer Speaker Config	guration Speaker Test S/PDIF-In S	S/PDIF-Out Connector Sensing General	Connector Sensing:
Line Out Connector	Line In Connector	Mic In Connector	J
Device Type :	Device Type :	Device Type :	<figure 11=""></figure>
Power Speaker Correction Option Start	Not Connected. Please stop other audio applications be	Stereo Headphone / Passive Speaker Correction fore starting.	
		OK	

11. After closing EZ-Connector, this page will show the latest connector status as above.

AC97 Audio Configuration	- IX	General:
Sound Effect Equalizer Speaker Configuration Speaker Test S/PDIF-In S/PDIF-Out Connector Sensing G	eneral	
Information		
Audio Driver Version : 5.10.0.5190		
DirectX Version : DirectX 7		
Audio Controller : VIA 3059 audio controller		<figure 12=""></figure>
AC97 Codec : ALC655		
☞ Show icon in system tray		
Language: Auto		
0	K	

12. This page displays information regarding the audio hardware and software. To remove "Sound Manager" icon from Windows Task bar, uncheck "Show icon in system tray".



Appendix A

A-1 Realtek Media Player User's Guide

Realtek Media Player Platform



Functional Descriptions

A. Playback Windows Display

Playback windows displays the following mode information:

- 1. Playback Time Display
- 2. Voice Cancellation Mode Display
- 3. Pitch Mode Display
- 4. Surround Sound Mode Display

B. Playback Function Controls

There are 8 selectable functions for the playback:

1. Volume control	High/Low Adjustment Bar.
2. Pitch control	4-step High/Low Adjustment Bar.
3. Repeat mode	Choice of Repeat, All Repeat, Random or No Repeat Mode.
4. Mute	Mute On/Off Mode select.
5. Voice cancellation	Voice Cancellation On/Off Mode select for Karaoke.

6. Surround mode

A total of 26 Surround Sound mode select as shown in the table below.

Surround mode	Surround mode
Generic	Stone corridor
Padded	Alley
Room	Forrest
Bathroom	City
Living room	Mountain
Stone	Quarry
Auditorium	Plain
Concert	Parking lot
Cave	Sewer pipe
Arena	Under water
Hangar	Drug
Carpet	Dizzy
Hallway	Psychological

- 7. Skin change
- 8. Open

Media Player Skin Type select.

Open file formats including MP3, CDA, MDI, WAV & WMA support.

C. Playback Controls

The playback controls include "Play", "Pause", "Stop", "Previous", "Backward", "Forward", & "Next".

D. Seeking bar

Display Animated Playback Status

E. Title/Play List Windows

Display Currently Selected Title(s)

F. Title/Play List Edit Controls

There title/play list controls include "Add", "Del", "Clear", "Load", & "Store".

Add to the Title/Play List.
 Del Remove form the Title/Play List.
 Clear Clear the Title/Play List.
 Load Title/Play List.
 Store Save Title/Play List.

G. Title/Play List Scroll bar

Scroll Up/Down the Title/Play List.

H. Recording Function Controls

The recording function controls include "Input", "Save, "New", "Rec", "Stop", & "Play".

1. Input	Input soruce select.
2. Save	Save to file.
3. New	Open new file & select format includes Sampling Rate, Sampling bit, Mono or Stereo.
4. Rec	Start Rec.
5. Stop	Stop Rec.
6. Play	Playback Rec file.

I. REC/Playback Time Display

Displays REC/Playback Time.

J. Platform Display Panel Controls

The platform display panel control include "Minimize" & "Close".

- 1. Minimize Minimize Platform Display Panel.
- 2. Close Close/Exit Platform Display Panel.

K. Equalizer Control Panel

The Equalizer Control Panel include "On/Off" & "Preset".

- 1. On/Off Enable/Disable Equalizer.
- 2. Preset Clear Equalizer setting to default value.

Appendix B

B-1 Update Your System BIOS

Download the xxxx.EXE file corresponding to your model from our website to an empty directory on your hard disk or floppy. Run the downloaded xxxxx.EXE file and it will self extract. Copy these extracted files to a bootable DOS floppy disk. Note: The DOS floppy disk should contain NO device drivers or other programs.

- 1. Type "A:\AWDFLASH and press <Enter> Key.
- 2. You will see the following setup screen.
- 3. Please key in the xxxxx.bin BIOS file name.



4. If you want to save the previous BIOS data to the diskette, please key in [Y], otherwise please key in [N].



5. Key in File Name to save previous BIOS to file.



6. To confirm and proceed, please key in [Y] to start the programming.

FLASH MEMORY WRITER V7.88 (C)Award Software 2000 All Rights Reserved
For xxxx-W83627-6A69LPA9C-0 DATE: 05/11/2000 Flash type - xxxxx E82802AB /3.3V
File Name to Program : xxxxx.bin Checksum : 938EH File Name to Save : xxxxx.bin
Error Message: Are you sure to program (y/n)

7. The BIOS update is finished.



Appendix C

C-1 EEPROM BIOS Remover

Do not remove the BIOS chip, unless instructed by a technician and only with a PLCC IC extractor tool.



The BIOS socket is fragile may be damaged if an improper method to replace the BIOS chip is applied.



Appendix D

D-1 POST CODES (Optional)

POST (hex)	DESCRIPTION
CFh	Test CMOS R/W functionality.
C0h	Early chipset initialization:
	- Disable shadow RAM
	- Disable L2 cache (socket 7 or below)
	 Program basic chipset registers
C1h	Detect memory
	 Auto-detection of DRAM size, type and ECC.
	- Auto-detection of L2 cache (socket 7 or below)
C3h	Expand compressed BIOS code to DRAM
C5h	Call chipset hook to copy BIOS back to E000 & F000 shadow RAM.
01h	Expand the Xgroup codes locating in physical address 1000:0
02h	Reserved
03h	Initial Superio_Early_Init switch.
04h	Reserved
05h	1. Blank out screen
	2. Clear CMOS error flag
06h	Reserved
07h	1. Clear 8042 interface
	2. Initialize 8042 self-test
08h	1. Test special keyboard controller for Winbond 977 series Super I/O
	chips.
	2. Enable keyboard interface.
09h	Reserved
0Ah	1. Disable PS/2 mouse interface (optional).
	2. Auto detect ports for keyboard & mouse followed by a port &
	interface swap (optional).
	3. Reset keyboard for Winbond 977 series Super I/O chips.
0B-0Dh	Reserved
0Eh	Test F000h segment shadow to see whether it is R/W-able or not. If test
	fails, keep beeping the speaker.
0Fh	Reserved
10h	Auto detect flash type to load appropriate flash R/W codes into the run
	time area in F000 for ESCD & DMI support.
11h	Reserved
12h	Use walking 1's algorithm to check out interface in CMOS circuitry.
	Also set real-time clock power status, and then check for override.
13h	Reserved
14h	Program chipset default values into chipset. Chipset default values are
	MODBINable by OEM customers.
15h	Reserved
16h	Initial Early_Init_Onboard_Generator switch.
17h	Reserved

18h	Detect CPU information including brand, SMI type (Cyrix or Intel) and CPU level (586 or 686).
19-1Ah	Reserved
1Bh	Initial interrupts vector table. If no special specified, all H/W
	interrupts are directed to SPURIOUS INT HDLR & S/W
	interrupts to SPURIOUS soft HDLR.
1Ch	Reserved
1Dh	Initial EARLY PM INIT switch.
1Eh	Reserved
1Fh	Load keyboard matrix (notebook platform)
20h	Reserved
21h	HPM initialization (notebook platform)
22h	Reserved
23h	1. Check validity of RTC value:
	e.g. a value of 5Ah is an invalid value for RTC minute.
	2. Load CMOS settings into BIOS stack. If CMOS checksum fails, use
	default value instead.
	3. Prepare BIOS resource map for PCI & PnP use. If ESCD is valid,
	take into consideration of the ESCD's legacy information.
	4. Onboard clock generator initialization. Disable respective clock
	resource to empty PCI & DIMM slots.
	5. Early PCI initialization:
	-Enumerate PCI bus number
	-Assign memory & I/U resource
24.261	-Search for a valid VGA device & VGA BIOS, and put it into C000:0.
24-261	
1°/h	
27h 28h	Posorved
27h 28h 20h	Reserved
27h 28h 29h	Reserved 1. Program CPU internal MTRR (P6 & PII) for 0-640K memory address.
27h 28h 29h	 Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU.
27h 28h 29h	 Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup.
27h 28h 29h	 Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller.
27h 28h 29h	 Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed.
27h 28h 29h	 Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS.
27h 28h 29h 2A-2Ch	 Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved
27h 28h 29h 2A-2Ch 2Dh	 Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language
27h 28h 29h 2A-2Ch 2Dh	 Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed
27h 28h 29h 2A-2Ch 2Dh 2E-32h	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Rese
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Test 8254
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch 3Dh	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Reserved Reserved Test 8254 Reserved
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch 3Dh 3Eh	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Reserved Test 8254 Reserved Test 8259 interrupt mask bits for channel 1.
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch 3Dh 3Eh 3Fh	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Test 8254 Reserved Test 8259 interrupt mask bits for channel 1. Reserved
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch 3Dh 3Eh 3Fh 40h	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Test 8254 Reserved Test 8259 interrupt mask bits for channel 1. Reserved Reserved Test 8259 interrupt mask bits for channel 2.
27h 28h 29h 2A-2Ch 2Dh 2E-32h 33h 34-3Bh 3Ch 3Dh 3Eh 3Fh 40h 41h	 Initialize INT 09 burrer Reserved Program CPU internal MTRR (P6 & PII) for 0-640K memory address. Initialize the APIC for Pentium class CPU. Program early chipset according to CMOS setup. Example: onboard IDE controller. Measure CPU speed. Invoke video BIOS. Reserved Initialize multi-language Put information on screen display, including Award title, CPU type, CPU speed Reserved Reserved Reserved Reserved Test 8254 Reserved Test 8259 interrupt mask bits for channel 1. Reserved Reserved Test 8259 interrupt mask bits for channel 2. Reserved

43h	Test 8259 functionality.
44h	Reserved
45-46h	Reserved
47h	Initialize EISA slot
48h	Reserved
49h	1. Calculate total memory by testing the last double word of each 64K
	2 Program writes allocation for AMD K5 CPU
44-4Dh	2. Trogram writes anotation for AwiD KS CI C. Reserved
4Fh	1 Program MTRR of M1 CPU
4211	 Initialize L2 cache for P6 class CPU & program CPU with proper
	cacheable range.
	3. Initialize the APIC for P6 class CPU.
	4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical.
4Fh	Reserved
50h	Initialize USB
51h	Reserved
52h	Test all memory (clear all extended memory to 0)
53-54h	Reserved
55h	Display number of processors (multi-processor platform)
56h	Reserved
57h	1. Display PnP logo
	2. Early ISA PnP initialization
	-Assign CSN to every ISA PnP device.
58h	Reserved
59h	Initialize the combined Trend Anti-Virus code.
5Ah	Reserved
5Bh	(Optional Feature) Show message for entering AWDFLASH.EXE
	from FDD (optional)
5Ch	Reserved
5Dh	1. Initialize Init_Onboard_Super_IO switch.
	2. Initialize Init_Onbaord_AUDIO switch.
5E-5Fh	Reserved
60h	Okay to enter Setup utility; i.e. not until this POST stage can users enter
(1. (1)	the CMOS setup utility.
61-64h	Reserved
65h	Initialize PS/2 Mouse
66h	Reserved
0/n	Prepare memory size information for function call: INT 15n ax=E820n
080	Reserved
09fi 6 A b	Pasawad
0All 6Dh	Reserved
овп	configuration table.
6Ch	Reserved
6Dh	1. Assign resources to all ISA PnP devices.
	2. Auto assign ports to onboard COM ports if the corresponding item in Setup is set to "AUTO".

6Eh	Reserved
6Fh	1. Initialize floppy controller
	2. Set up floppy related fields in 40:hardware.
70-72h	Reserved
73h	(Optional Feature) Enter AWDFLASH.EXE if :
	-AWDFLASH is found in floppy drive.
	-ALT+F2 is pressed
74h	Reserved
75h	Detect & install all IDE devices: HDD, LS120, ZIP, CDROM
76h	Reserved
77h	Detect serial ports & parallel ports.
78h-79h	Reserved
7Ah	Detect & install co-processor
7B-7Eh	Reserved
7Fh	1. Switch back to text mode if full screen logo is supported.
	-If errors occur, report errors & wait for keys
	-If no errors occur or F1 key is pressed to continue:
	 Clear EPA or customization logo.
80h-81h	Reserved
82h	1. Call chipset power management hook.
	2. Recover the text fond used by EPA logo (not for full screen logo)
	3. If password is set, ask for password.
83h	Save all data in stack back to CMOS
84h	Initialize ISA PnP boot devices
85h	1. USB final Initialization
	2. NET PC: Build SYSID structure
	3. Switch screen back to text mode
	4. Set up ACPI table at top of memory.
	5. Invoke ISA adapter ROMs
	Assign IRQs to PCI devices
	7. Initialize APM
	8. Clear noise of IRQs.
86-92h	Reserved
93h	Read HDD boot sector information for Trend Anti-Virus code
94h	1. Enable L2 cache
	2. Program boot up speed
	3. Chipset final initialization.
	4. Power management final initialization
	5. Clear screen & display summary table
	6. Program K6 write allocation
	7. Program P6 class write combining
95h	1. Program daylight saving
	2. Update keyboard LED & typematic rate
96h	1. Build MP table
	2. Build & update ESCD
	3. Set CMOS century to 20h or 19h
	4. Load CMOS time into DOS timer tick
	5. Build MSIRQ routing table.
FFh	Boot attempt (INT 19h)