



User's Manual

An Intel Socket 478 Processor Based
Mainboard (400/533MHz)

Supports PC800/PC1066 (RIMM3200/RIMM4200)
RDRAM Memory Modules

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Frequently Asked Questions For POST Port Only

Below is a list of some basic POST Codes, possible problems, and solutions. For more detailed information about POST Codes, refer to Appendix E in this manual.

POST CODE	Problem	Solution
FFh or CFh	<ol style="list-style-type: none">1. BIOS chip inserted incorrectly2. Incorrect BIOS update version3. Mainboard problem4. Add-on card inserted incorrectly.	<ol style="list-style-type: none">1. Reinsert the BIOS chip2. Download the correct BIOS version update from the manufacturer's Web site.3. Replace mainboard4. Remove and replace the add-on card
C1h - C5h	<ol style="list-style-type: none">1. Memory module inserted incorrectly2. Memory compatibility problem3. Memory module damaged	<ol style="list-style-type: none">1. Reinsert memory module2. Replace memory with correct type3. Replace memory module
2Dh	<ol style="list-style-type: none">1. Error occurred in VGA BIOS2. VGA card inserted incorrectly	<ol style="list-style-type: none">1. Replace VGA card2. Reinsert the VGA card
26h	Overclock error	Clear CMOS or press the insert key to power on the system
07h - 12h	<ol style="list-style-type: none">1. Init keyboard controller error2. RTC error	<ol style="list-style-type: none">1. Ensure that the keyboard and mouse are connected correctly.2. Replace the RTC battery.

Table of Contents

	<i>Page</i>
Section 1	<i>Introduction</i>
	Components Checklist 1-1
	<i>Overview</i>
	System Overview 1-2
	Chipset Components 1-3
	Intel Pentium 4 Processors 1-4
	Direct Rambus 1-5
	Bandwidth Overview 1-6
	Accelerated Graphics Port 1-6
	Ultra ATA66/100 1-7
	IEEE 1394 (Optional) 1-7
	Hardware Monitoring 1-7
	Mainboard Form-Factor 1-8
	I/O Shield Connector 1-9
	Power-On/Off (Remote) 1-9
	System Block Diagram 1-10
Section 2	<i>Features</i>
	Mainboard Features 2-1
Section 3	<i>Installation</i>
	Mainboard Detailed Layout 3-2
	<i>Easy Installation Procedure</i>
	CPU Installation 3-3
	Jumper Settings 3-5
	System Memory Configuration 3-6
	Device Connectors 3-12
	STR (Suspend To RAM) Function 3-17
	850E Platform AGP Card 3.3V protection 3-18

Section 4**Award BIOS Setup**

Main Menu	4-1
Standard CMOS Setup	4-2
Advanced BIOS Features	4-3
Advanced Chipset Features	4- 8
Integrated Peripherals	4-10
Power Management Setup	4-15
PNP/PCI Configuration Setup	4-20
PC Health Status	4-23
Frequency/Voltage Control	4-25
Defaults Menu	4-27
Supervisor/User Password Setting	4-28
Exit Selecting	4-29

Section 5**Driver Installation**

Easy Driver Installation.....	5-1
ALC650 Configuration Setup (6 Channel).....	5-2

Appendix**Appendix A**

Avance Media Player User's Guide	A-1
--	-----

Appendix B

Update Your System BIOS	B-1
-------------------------------	-----

Appendix C

EEPROM BOIS Remover	C-1
---------------------------	-----

Appendix D

GHOST 7 Quick User's Guide (Optional)	D-1
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Appendix E

POST Codes	E-1
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Section 1 INTRODUCTION

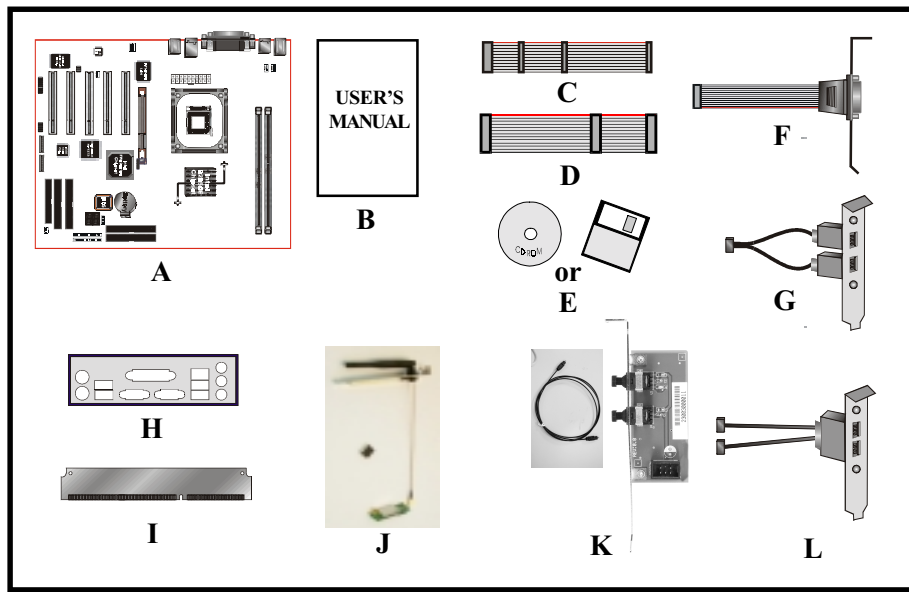
Components Checklist

Package Contents

- A. (1) Mainboard
- B. (1) User's manual
- C. (1) Floppy ribbon cable
- D. (1) ATA-66/100 hard drive ribbon cable
- E. (1) Driver and utility

Optional Item

- F. (1) Game port cable
- G. (1) USB2.0 Cable
- H. (1) I/O Shield
- I. (1) Continuity Module (C-RIMM)
- J. (1) Bluetooth Module
- K. (1) SPD650 card
- L. (1) IEEE 1394 2 port cable



Introduction

System Overview

This board is designed with Intel® 850E chipset. The Intel® 850E chipset includes MCH(FW82850E), ICH4(FW82801DB) and FWH three chips. The Intel® 850E chipset designed for Intel's FC-PGA2 socket 478 package architecture and support the 4X capability of the AGP 2.0 Interface Specification and 400/533MHz Direct RDRAM. The 400/533MHz, 32bit, double clocked Direct RDRAM interface provides 3.2/4.2GB/s access to main memory. A new chipset component interconnect, the hub interface, is designed into the Intel® 850E chipset to provide more efficient communication between chipset components.

Support of AGP 4X, 400/533MHz Direct RDRAM and the hub interface provides a balanced system architecture for the Pentium® 4 or later Socket 478 architecture processor minimizing bottlenecks and increasing system performance. By increasing memory bandwidth to 1.06GB/s through the use of AGP 4X, the Intel® 850E chipset will deliver the data throughput necessary to take advantage of the high performance provided by the powerful Pentium® 4 or later Socket 478 architecture processor.

The Intel® 850E chipset architecture removes the requirement for the ISA expansion bus that was traditionally integrated into the I/O subsystem of Intel chipsets. This removes many of the conflicts experienced when installing hardware and drivers into legacy ISA systems. The elimination of ISA will provide true plug-and play for the Intel® 850E platform.

Intel® 850E chipset contains three core components: the Memory Controller Hub (MCH), the I/O Controller Hub (ICH) and the Firmware Hub (FWH). The MCH integrates the 400/533MHz, Pentium® 4 processor bus controller, AGP 2.0 controller, 400/533MHz direct RDRAM controller and a high-speed hub interface for communication with the ICH4. The ICH4 integrates an UltraATA/66/100 controller, USB host controller, LPC interface controller, FWH interface controller, PCI interface controller, and a hub interface for communication with the MCH. The Intel® 850E chipset will provide the data buffering and interface arbitration required to ensure that system interfaces operate efficiently and provide the system bandwidth necessary to obtain peak performance the Pentium® 4 or later Socket 478 architecture.

Chipset Components

The Intel® 850E chipset consists of the Memory Controller Hub (MCH), the I/O Controller Hub (ICH4) and the Firmware Hub (FWH).

◆ **Memory Controller Hub (MCH)**

The MCH provides the interconnect between the Direct RDRAM and the system logic. It integrates:

- Support for single processor with a data transfer rate of 400/533MHz.
- 400/533MHz Direct RDRAM interface supporting 2GB of Direct RDRAM.
- 2X, 4X, 1.5V AGP interface (Only support 1.5V on AGP interface).
- Downstream hub link for access to the ICH4.

◆ **I/O Controller Hub (ICH4)**

The I/O controller Hub provides the I/O subsystem with access to the rest of the system. Additionally, it integrates many I/O functions. The ICH4 integrates:

- Upstream hub link for access to the MCH
- 2 Channel Ultra ATA/33/66/100 Bus Master IDE controller
- USB controller
- SMBus controller
- FWH interface
- LPC interface
- PCI 2.2 interface
- Integrated System Management Controller
- Integrated LAN Controller

◆ **Firmware Hub (FWH)**

The FWH component is a key element to enabling a new security and manageability infrastructure for the PC platform. The device operates under the FWH interface and protocol. The hardware features of this device include a unique Random Number Generator (RNG), register-based locking, and hardware-based locking.

Introduction

Intel Pentium 4 processors

Formally known as the Willamette, the Pentium™ 4 is the next generation IA-32 processor from Intel. This next generation design is based upon a new micro-architecture that brings higher clock speeds and performance than previous processors could deliver. Among other advanced features the Pentium 4 offers Streaming SIMD extensions 2, Advanced Dynamic Execution, Hyper Pipelined Technology, and a data transfer rate of 400/533MHz system bus.

Streaming SIMD Extensions 2

Building upon the foundations of core features of their previous line of processors the Pentium 4, this new version introduces Streaming SIMD Extensions 2 technology commonly referred to as SSE2. But what does this mean? SIMD stands for Single Instruction Multiple Data. Usually, processors process one data element in one instruction, called Single Instruction Single Data, or SISD. In contrast, with Single Instruction Single Data (SISD), SIMD has the ability to process more than one piece of data element during one instruction.

This technology is useful for 3D graphics applications that handle considerable amounts of floating-point numbers. With SIMD applications such as 3D graphics will be able to processor more data per instruction when equates to better performance. This technology adds 144 new instructions to the CPU core that can be used in a wide variety of applications. Software programmers can for example, take advantage of these new instructions and write more optimized code that take advantage of newer SIMD double-precision floating-point, integer, and cache ability instructions. In theory this will enable better next generation services such as Interactive Digital TV to be produced.

Advanced Dynamic Execution

Advanced Dynamic Execution describes the improved implementation and abilities over the older P6 processor line's out-of-order decoupled super scalar execution. Dynamic execution allows instructions to the processor to be executed without the need to do so in order. The ability to do this can add a significant performance increase versus ordered execution.

Hyper Pipelined Technology & 400/533MHz System Bus

Hyper Pipelined Technology doubles the pipeline depth the Pentium 4 delivers to 20 stages. This significantly increases the performance and frequency capabilities.

Pentium 4 also introduces a 400/533MHz system bus as opposed to the 100 and 133MHz bus seen in previous Pentium III processors. This allows 3.2Gbytes per second of throughput while the Pentium III had a limited 1.06Gbyte/s throughput. Willamette will reportedly be introduced in the 0.18-micron using aluminum.

For more information about all the cool new features the Pentium 4 delivers check out the Intel website at <http://www.intel.com>

Direct Rambus

The Direct Rambus (RDRAM) initiative will provide the memory bandwidth necessary to obtain optional performance from the Pentium 4 or later 478-pin socket processor as well as a high-performance AGP graphics controller. The MCH RDRAM interface supports 400/533MHz operation; the latter delivers 3.2/4.2GB/s of theoretical memory bandwidth; twice the memory bandwidth of 100/133MHz SDRAM system. Coupled with the greater bandwidth, the RDRAM protocol, which is heavily pipelined, provides substantially more efficient data transfer. The RDRAM memory interface can achieve greater than 95% utilization of the 3.2/4.2GB/s theoretical maximum bandwidth. The Figure 1 is the example picture for RIMM Module to plug in the RIMM socket.

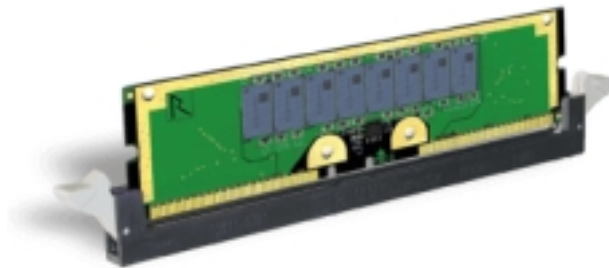


Figure 1: RIMM Module

Introduction

Bandwidth Overview

Table 1 provides a summary of the bandwidth requirements for the Intel® 850E chipset.

Interface	Clock Speed (MHz)	Samples Per Clock	Data Rate (Mega-samples/s)	Data Width (Bytes)	Bandwidth (MB/s)
CPU Bus	100/133	4	400/533	8	3200/4264
RDRAM	400/533	2	800/1066	4	3200/4264
AGP 2.0	66.6	4	266	4	1066
Hub Link	66.6	4	266	1	266
PCI 2.2	33.3	1	33.3	4	133

Table 1: Intel® 850E platform Bandwidth Summary

Accelerated Graphics Port (AGP or A.G.P.)

Typically, 3D graphics rendering requires a tremendous amount of memory, and demands ever increasing throughput speed as well. As 3D products for the personal computer become more and more popular, these demands will only increase. This will cause a rise in costs for both end users and manufacturers. Lowering these costs as well as improving performance is the primary motivation behind AGP. By providing a massive increase in the bandwidth available between the video card and the processor, it will assist in relieving some of these pressures for quite sometime.

The board provides the AGP 2.0 interface. The AGP Interface Specification revision 2.0 enhances the functionality of the original AGP Interface Specification (revision 1.0) by allowing 4X data transfers (4 data samples per clock) and 1.5 volt (power supply) operation. The AGP 2.0 interface, along with SDRAM memory technology, allows graphics controllers to access main memory at over 1GB/s (**1.5 volt AGP Card supports only**).

Ultra ATA/66/100

The board provides an Ultra ATA/66/100 Bus Master IDE controller. This controller supports Ultra ATA/66/100 protocols which are ideal for supporting demanding applications such as real-time video, multimedia, and a high performance operating system. A new IDE cable is required for Ultra ATA/66/100. This cable is an 80-pin conductor cable, which is backwards compatible with ATA/33 connectors.

IEEE1394 (Optional)

IEEE 1394 is a high-speed serial bus developed by Apple and Texas Instruments that allows users to connect up to 63 devices to the serial bus on a PC. IEEE is sometimes called the IEEE 1394 standard, the i.Link connector, FireWire, and the High Performance Serial Bus (HPSB).

IEEE 1394 provides transfer rates up to 400Mbits/sec. IEEE 1394b provides up to 3200Mbits/sec transfer speeds. IEEE 1394 provides enhanced PC connectivity for consumer electronics audio/video (A/V) appliances, storage peripherals, portable devices such as digital cameras, and inter-PC communications.

IEEE 1394 supports hot swapping, multiple speeds on the same bus, and isochronous data transfer providing much needed bandwidth for multimedia operations.

Hardware Monitoring

Hardware monitoring enables you to monitor various aspects of the system operation and status. The features include CPU temperature, voltage and fan speed in RPMs.

Introduction

Mainboard Form-Factor

The board is designed with an ATX form factor. The ATX form factor is essentially a Baby-AT baseboard rotated 90 degrees within the chassis enclosure and a new mounting configuration for the power supply. With these changes the processor is relocated away from the expansion slots, allowing them to hold full length add-in cards. The ATX form factor provides a double-height aperture at the rear of the chassis which can be used to host a wide range of onboard I/O devices, enabling PC manufacturers to add new I/O features (for example, TV input, TV output, joystick, modem, and LAN). Other advantages include:

- Smaller form factor promotes an overall smaller system size.
- I/O shield does not need to be retooled in an ATX 2.01 form factor or later. This mainboard should be used in an ATX 2.01 (or later) compliant case.
- A smaller power supply can be used.
- High integration on mainboard reduces the system cost.

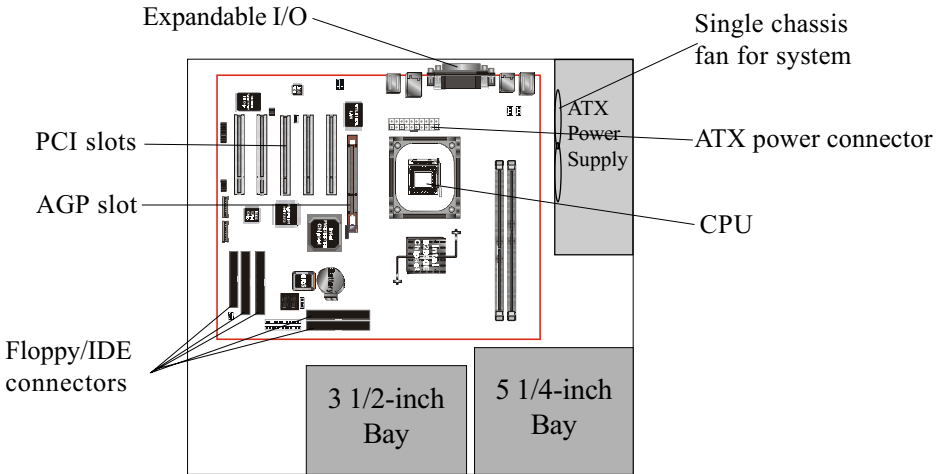


Figure 2: Summary of ATX chassis features

I/O Shield Connector

The board is equipped with an I/O back panel (Figure 3). Ensure that your computer case has the appropriate I/O cutout.

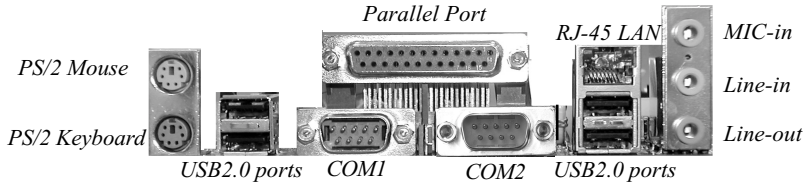


Figure 3: I/O ports

Power-On/Off (Remote)

The board has a single 20-pin connector for ATX power supplies (Figure 4). For ATX power supplies that support the **Remote On/Off** feature, this should be connected to the mainboard front panel PW_ON connector for the computer power On/Off button.

The board has been designed with "Soft Off" function. You can turn off the system two ways: pressing the front panel power On/Off button, using the "Soft Off" function (incorporated in the mainboard's onboard circuit controller) that can be controlled by an operating system such as Windows[®]ME/2000/98/95.

Note: For maintaining the RDRAM power during STR (ACPI S3) function, it is strongly recommend to use ATX power supplies that have a +5VSB current of (\geq) 1A (1000mA). Please check the 5VSB's specification that has been printed on the ATX power supply's outer case.

Note: The board requires a minimum of 250 Watt ATX power supply to operate. Your system configuration (amount of memory, add-in cards, peripherals, etc.) may exceed the minimum power requirement but to ensure that adequate power is provided, use a 300 Watt (or greater) ATX power supply.

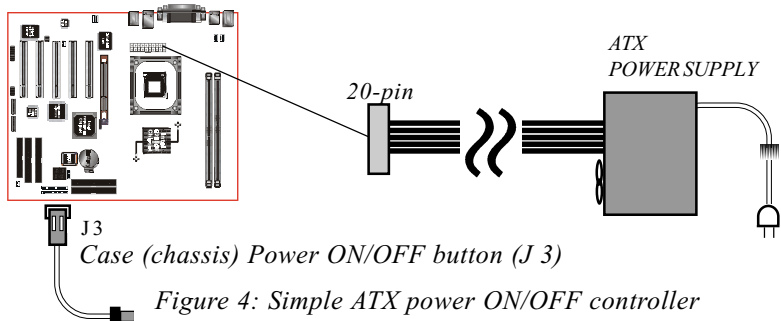


Figure 4: Simple ATX power ON/OFF controller

Introduction

System Block Diagram

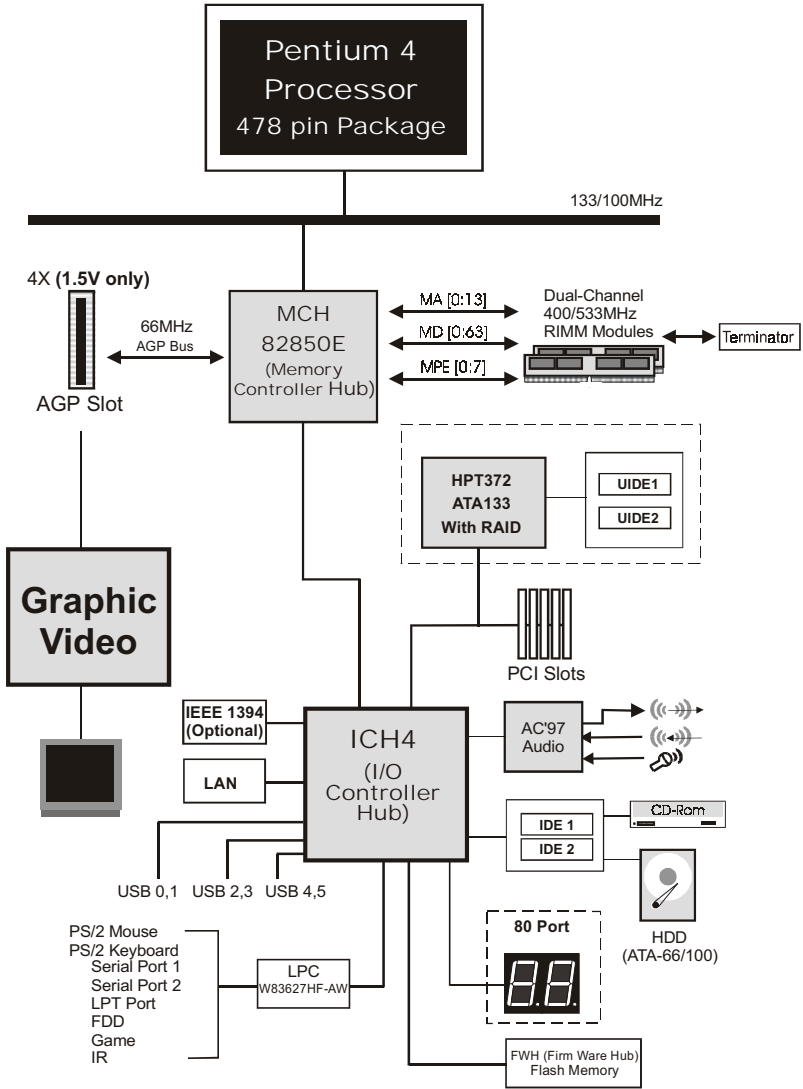


Figure 5: System block diagram

Mainboard Features

◆ **PROCESSOR**

- Socket 478 Intel® Pentium® 4 processor from 1.4 to 2.4GHz

◆ **CHIPSET**

- Intel 82850E Chipset (82850E + ICH4)

◆ **FRONT SIDE BUS**

- 400/533MHz

◆ **DRAM MODULE**

- 232-pin RIMM x 2 (32-bit) for PC800/PC1066 (RIMM3200/RIMM4200) RDRAM
- DRAM Size: 128MB to 2GB (2 channel max. 32 Devices)

◆ **EXPANSION SLOT**

- 32-bit PCI x 5
- 4X AGP x 1 (1.5V only)

◆ **ONBOARD I/O**

- Winbond W83627HF-AW LPC I/O integrated with keyboard and mouse, FDD, fast IR, power on function, and parallel, serial, and game ports

◆ **ONBOARD PCI / IDE**

- Intel 82801DB(ICH4) controller
- PCI Rev. 2.2 compliant
- ACPI-compliant power management
- LPC Interface 1.0 compliant

Features

- PCI Bus IDE Port with PIO/Ultra DMA-66/100 x 2 (up to 4 devices)
- Extra IDE Port by HPT372 with Ultra DMA-100/133 and IDE RAID x 2 (up to 4 devices)
 - * Supports JBOD function (Just a Bunch of Disks). JBOD are a group of hard disks in a computer that are not configured in a RAID.

◆ Onboard LAN

- Integrate 10/100Mb fast Ethernet controller in Realtek RTL8100B Lan by RJ-45 connector

◆ I/O CONNECTOR

- PS/2 mouse and keyboard
- COM1, COM2
- LPT (printer)
- Audio-in/out, MIC
- Game port by extra cable
- RJ-45 jack
- USB connector x 6 (two optional via mainboard front panel USB connector). USB supports USB 2.0 specification. The USB3 can support Bluetooth Module.

◆ IEEE 1394 (Optional)

- Integrated OHCI 1.1 1394 controller
- Supports two 400Mbps 1394a ports

◆ BIOS

- Award Plug & Play BIOS

◆ Built-in AC 97 Digital Audio by Realtek ALC650 (6 channel)

- Compliant with AC97 2.2 specification
- Six-channel, slot selectable DAC (Digital Analog Converter) output for multi-channel applications
- Supports digital SPDIF function
- Supports game and MIDI port

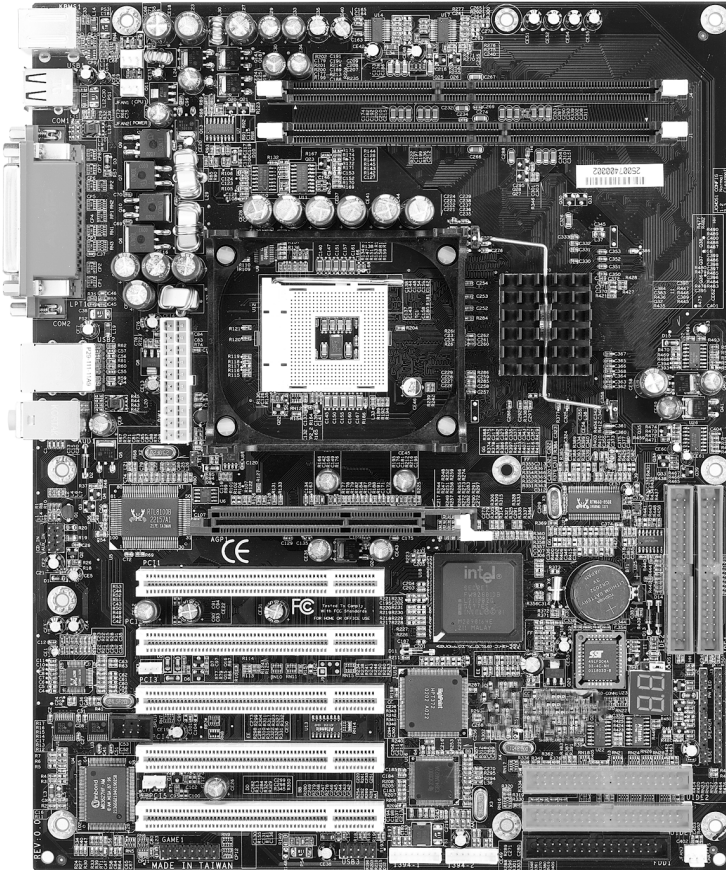
◆ EXTENDED FUNCTION

- Supports hardware monitoring function by W83627HF-AW
- Supports exclusive KBPO (Keyboard Power On) function
- Supports Wake-On-LAN function
- Supports STR (Suspend To RAM) power saving function
- Supports CPU clock and ratio settings via BIOS
- Supports CPU Vcore and memory, and AGP voltage settings via BIOS
- Supports Asynchronous Transfer Mode between PCI & FSB
- Supports “Magic Health” and “Easy Boot” Function
- Supports AGP card 1.5V protection
- 80 Port onboard design with 7-segment LED display

◆ FORM FACTOR

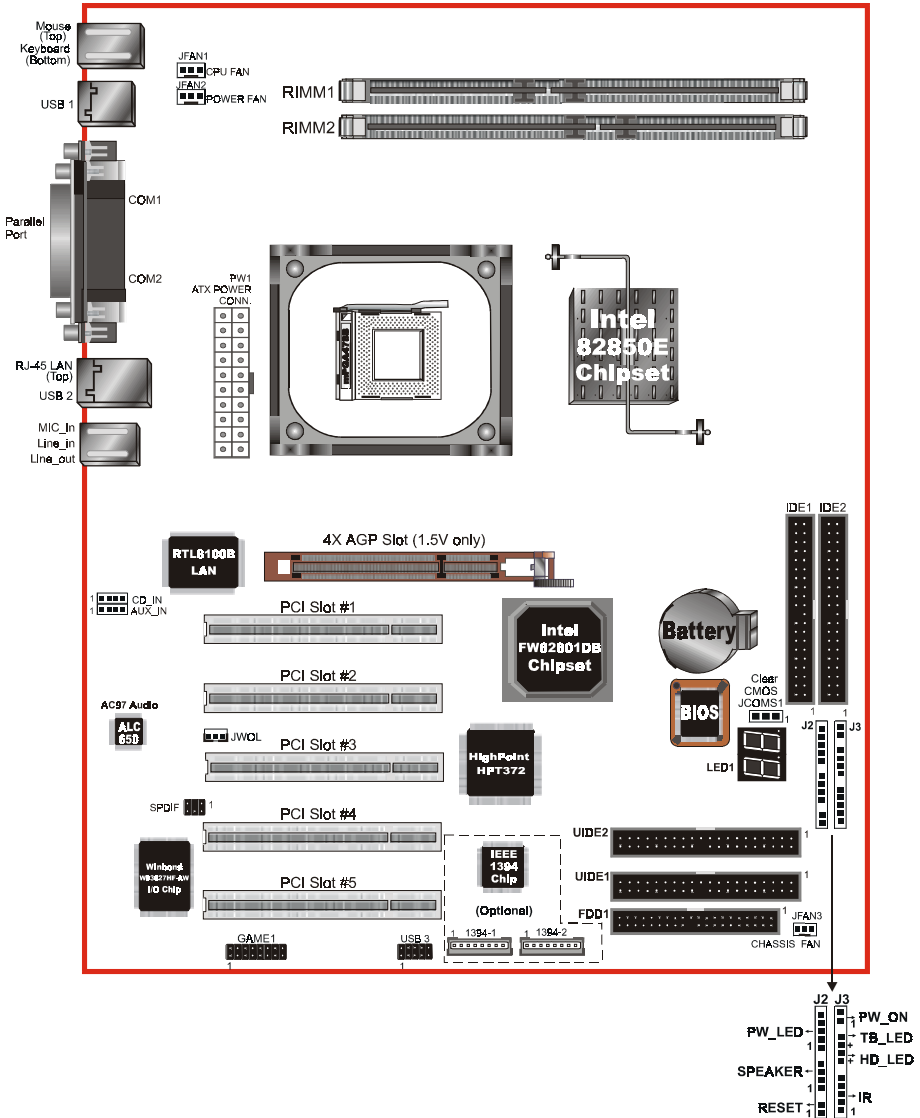
- 305 mm x 245 mm ATX size

Section 3
INSTALLATION



Installation

Mainboard Layout



Easy Installation Procedure

The following must be completed before powering on your new system:

- 3-1. CPU Installation**
- 3-2. Jumper Settings**
- 3-3. System memory Configuration**
- 3-4. Device Connectors**
- 3-5. STR Function**
- 3-6. 850E platform AGP Card 3.3V Protection**

Section 3-1 CPU Installation

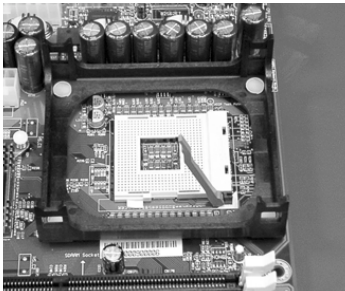


Figure 1

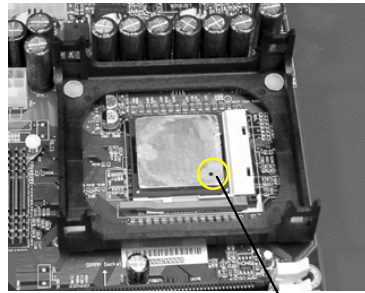


Figure 2

Pin 1

Step 1

Open the socket by raising the actuation lever.

Step 2

Align pin 1 on the CPU with pin 1 on the CPU socket as shown in the illustration above. The CPU is keyed to prevent incorrect insertion. Don't force the processor into the socket. If it does not go in easily, check for mis-orientation and reinsert the CPU.

Make sure the processor is fully inserted into the socket.

Installation

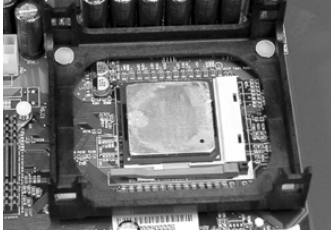


Figure 3

Step 3

Close the socket by lowering and locking the actuation lever.



Figure 4

Step 4

Apply thermal compound to the top of the CPU and install the heatsink as shown.

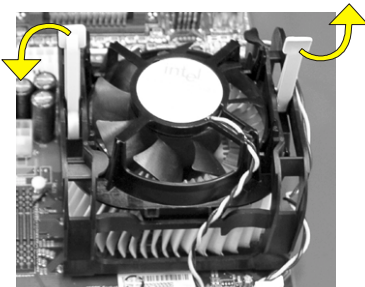


Figure 5

Step 5

Install the cooling fan assembly. Press the two clips in the direction of the arrows shown in Figure 5 to secure the assembly to the CPU socket.

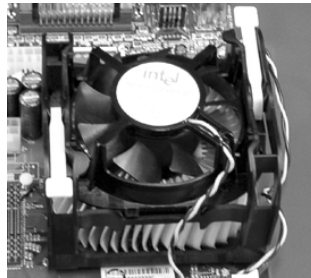


Figure 6

Step 6

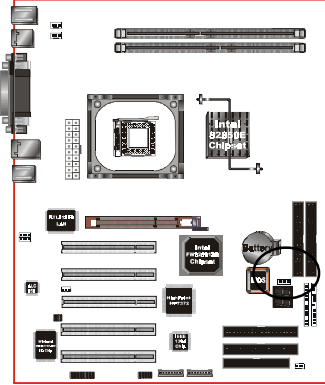
Plug the CPU fan into the CPU fan connector (FAN1).

The installation is complete.

NOTES:

- Damage to Intel Pentium™ 4 processors might result if installed with incorrect CPU fan and heatsink assemblies. Use Intel's design thermal solution shown in the illustrations above: an active heatsink; an extruded aluminum heatsink base; and a fan attached to the top on the fin array.
- Apply heatsink thermal compound or paste to the CPU to avoid CPU overheating and damage.
- In accordance with Intel Corp. specifications, do not install a CPU over 50 times to avoid bending the pins and damaging the CPU.

Section 3-2 Jumper Settings



JCMOS1



1

CMOS Clear

1-2 Normal (Default)

2-3 Clear CMOS

Installation

Section 3-3 System RIMM Memory Module Configuration

Memory Layout

The board supports two channels (2) 232-pin RIMMs (Rambus Interface Memory Module) as shown in Figure 7. The RIMMs can be RIMM and C-RIMM (Continuity RIMM) only. RIMM modules have Rambus channel signals as their memory interface. A RIMM module may contain up to a maximum of 16 RDRAM devices. All RDRAM devices on a RIMM must have the same timing characteristics. Empty RIMM sockets must be populated with continuity modules (C-RIMM). These modules have no memory on them and are used to propagate the channel to the next RIMM socket. Figure 9, 10 & 11 provide a general diagram of a RIMM module and installations of RIMM/C-RIMM modules. **The board must be populated 2 RIMM modules at the same time, that will boot-up the system.**

- The board supports a maximum of 16 devices on a RDRAM channel. A Channel is defined as the two RIMM Slots on the motherboard added together. Thus the motherboard has two channel. See Figure 7 for two channel RDRAM interconnections.
- No support for EDO/SDRAM/DDR DIMM Modules.
- The board supports 32-bit RDRAM configurations.
- The RIMM modules and continuity RIMM (C-RIMM) spec. For more detailed “RIMM Modules spec.” information you may visit the following Web Site: <http://www.rimm.com>.
- Direct Rambus Channel operating at a clock rate of 400/533MHz which enables a data rate of 800/1066MHz (data is clocked on both clock edges).

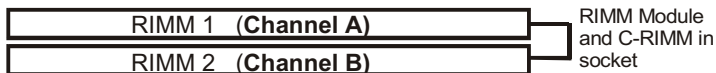


Figure 7

The figure 8 below shows the RSL interconnections between 850E (MCH) and two RDRAM channel. This figure describes the logical interconnections, and is not a physical representation of RDARM devices on a motherboard.

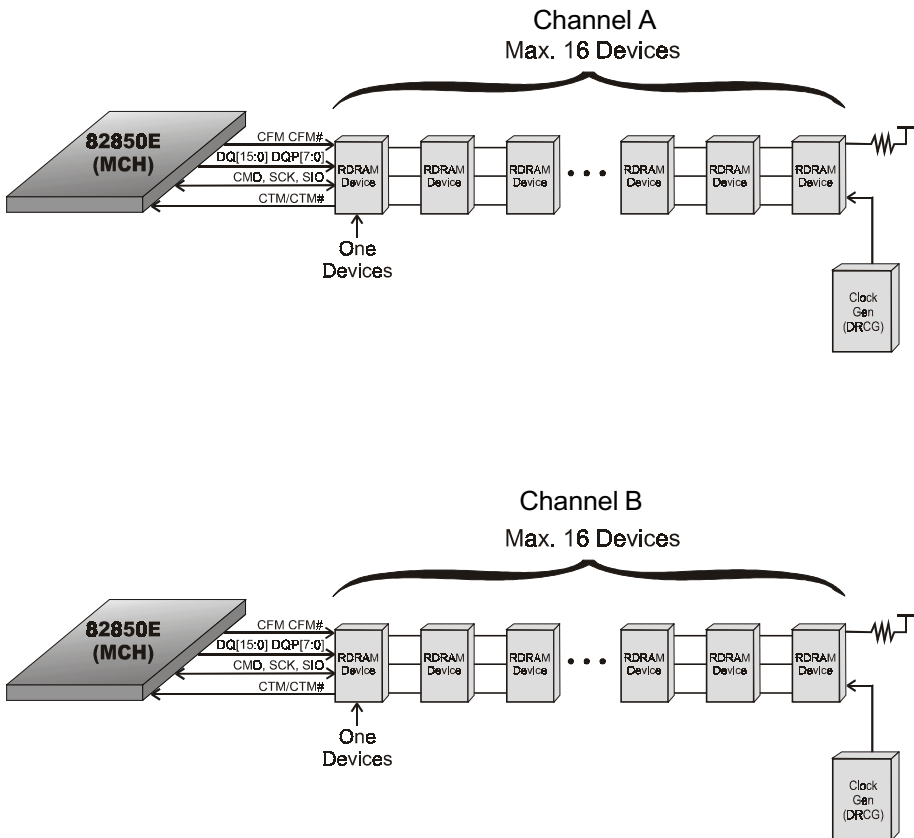


Figure 8: MCH/RDRAM Interconnections

Installation

The table 1 below shows the onboard RIMM memory socket population.

RIMM Socket# of Onboard		System Accept	Max. of RDRAM devices on a Channel	Total Memory
RIMM1	RIMM2	Status	Device	Size
RIMM	RIMM	OK	16 x 2	2048MB (2GB) *
RIMM	C-RIMM	OK	16	1024MB (1GB) *
C-RIMM	RIMM	OK	16	1024MB (1GB) *
Any RIMM socket is empty or the RIMM Module is not properly inserted.		Failure, System can't boot and no display.		

Table 1: Onboard RIMM memory socket population.

Note : Two Channel must be populated the same size of RIMM memory modules.

Important Notice

For the safe of reducing the high heat of RDRAM, it is strongly recommended that user must install the Chassis cooling fan in the system.

RIMM/C-RIMM Module Installation

This section describes some basic RIMM/C-RIMM installations as described in table 1. Note that continuity Modules are required in empty sockets. Figure 9, 10 and 11 display common installations. RIMM or RDRAM signals are daisy-chained through each device on RIMM and memory module.

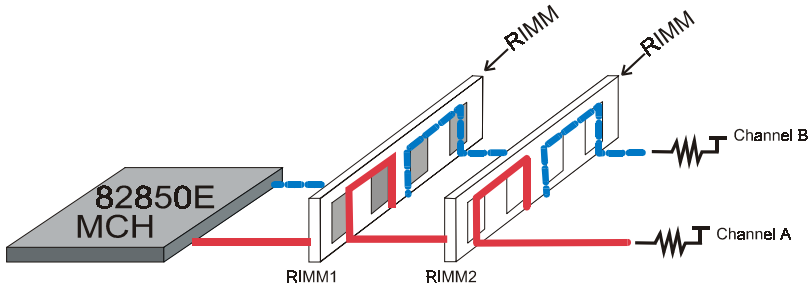


Figure 9: Right Base Configuration

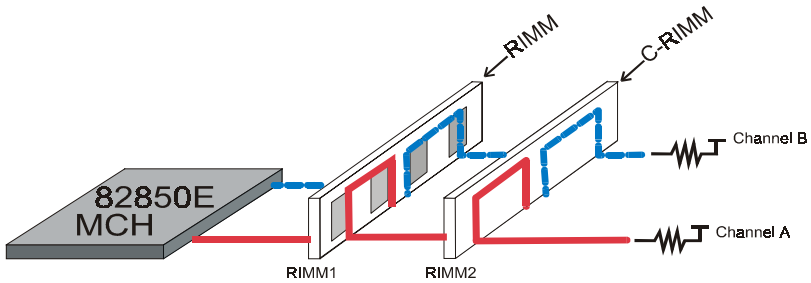


Figure 10: Right Base Configuration

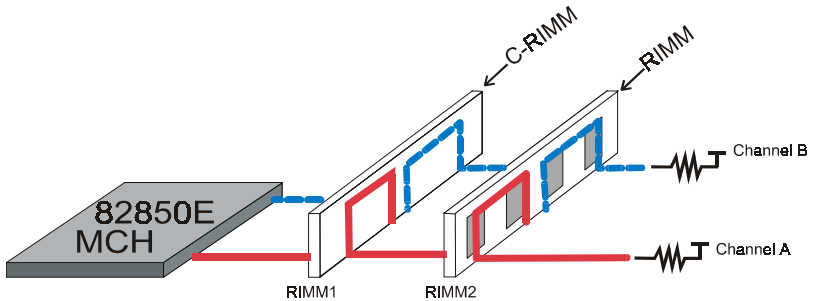


Figure 11: Right Base Configuration

Installation

Figure 12 displays the notch marks and what they should look like on your RIMM memory module.

RIMMs have 232-pins and one notch that will match with the onboard RIMM socket. RIMM modules are installed by placing the chip firmly into the socket at a 90 degree angle and pressing straight down (figure 13) until it fits tightly into the RIMM socket (figure 14).

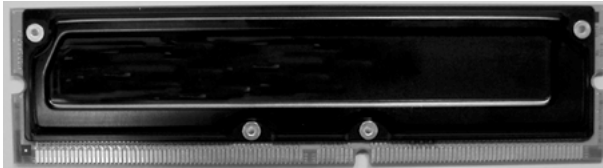


Figure 12



Figure 13

RIMM Module clip before installation

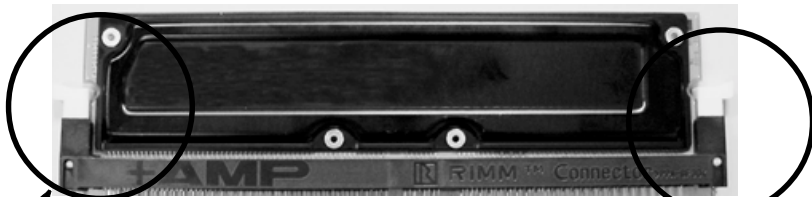


Figure 14

RIMM Module clip after installation

To remove the RIMM module simply press down both of the white clips on either side and the module will be released from the socket.

Figure 12a displays the notch marks and what they should look like on your C-RIMM memory module.

C-RIMMs have 232-pins and one notch that will match with the onboard C-RIMM socket. C-RIMM modules are installed by placing the chip firmly into the socket at a 90 degree angle and pressing straight down (figure 13a) until it fits tightly into the RIMM socket (figure 14a).

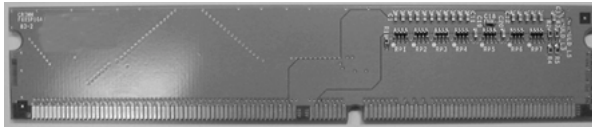


Figure 12a

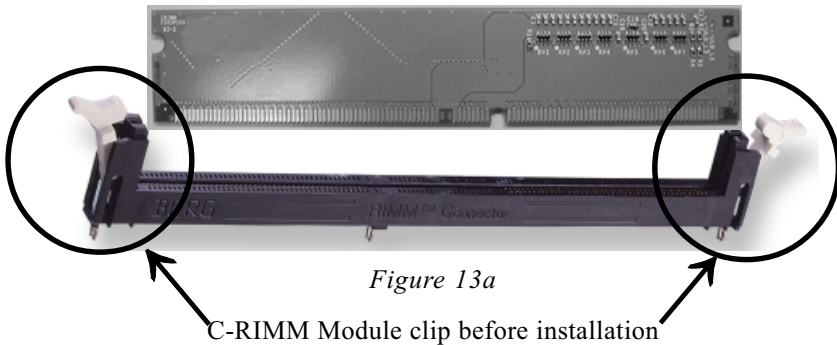


Figure 13a

C-RIMM Module clip before installation

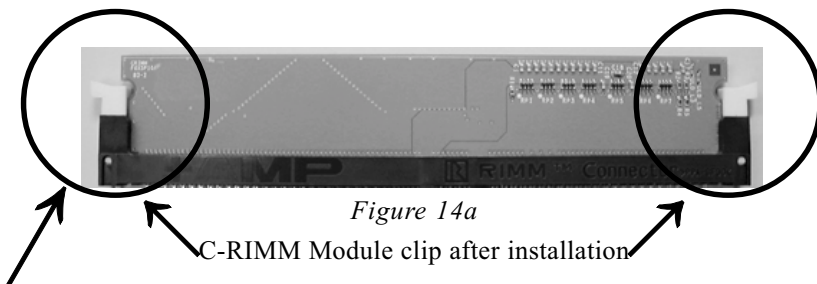


Figure 14a

C-RIMM Module clip after installation

To remove the C-RIMM module simply press down both of the white clips on either side and the module will be released from the socket.

Installation

Section 3-4 Device Connectors

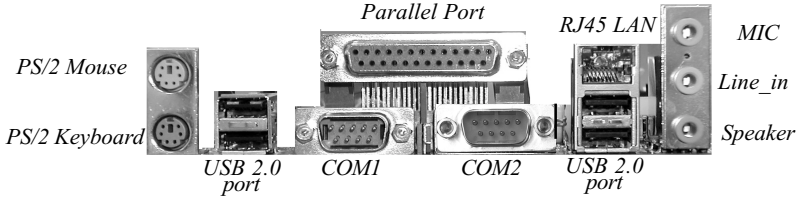
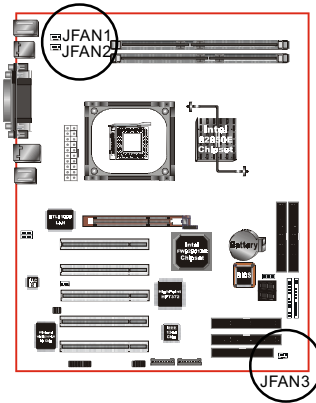


Figure 15: I/O ports



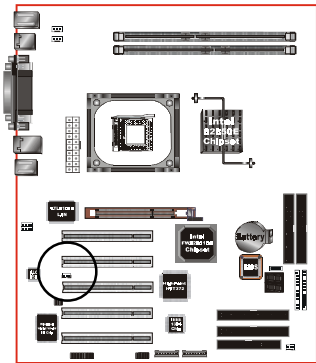
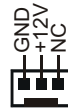
JFAN1 / JFAN2 / JFAN3:

- The plug-in for CPU/Power/Chassis Fan power

JFAN1:
CPU Fan

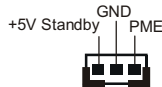
JFAN2:
Power Fan

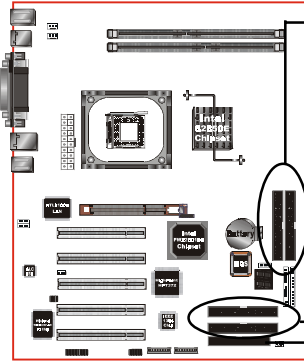
JFAN3:
Chassis Fan



JWOL: WOL (Wake On LAN) Connector

Reserved for NIC (Network Interface Card) to wake the system.

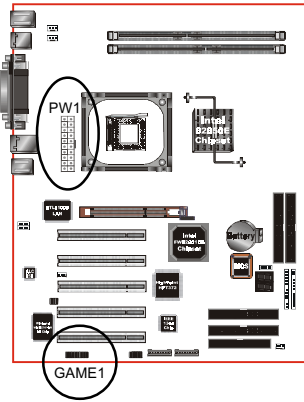




IDE1/2: Ultra DMA-66/100 Primary / Secondary IDE Connector (Blue)

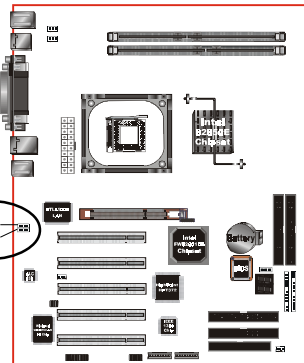
UIIDE1/2: Ultra DMA-66/100/133 & RAID Primary/Secondary IDE Connector (Red)
Supported by HTP372 chipset

FDD1: Floppy Controller Connector (Black)

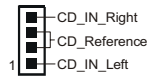


PW1: ATX Power Connector (20-pin power connector)

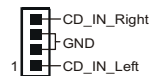
GAME1: Game port connector



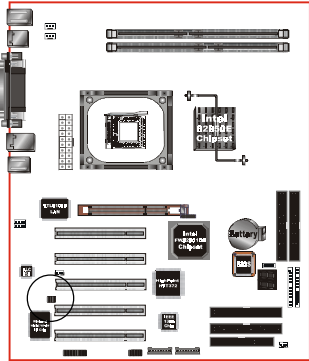
CD_IN: CD Audio_IN Connector



AUX_IN: Auxiliary Line_IN Connector

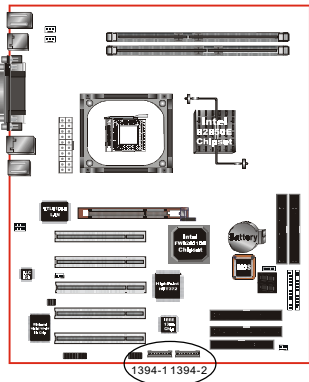
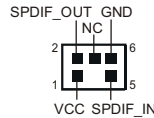


Installation



SPDIF: Sony/Philips Digital Interface

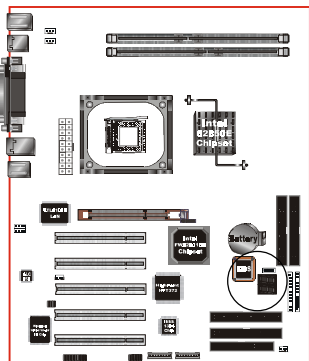
This connector is the digital link between the mainboard and your audio devices, such as CD player, sampler or DAT recorder. It allows the digital transmission of audio data in SPDIF format.



1394-1 / 1394-2: (Optional)

400Mbps 1394a (FireWire) Connectors

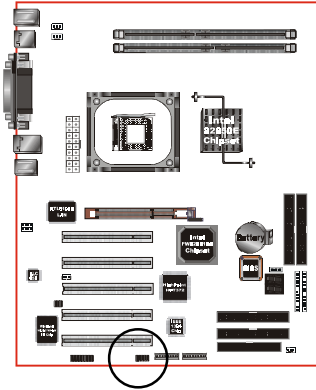
1394-1 and 1394-2 enable you to connect two IEEE 1394 ports for use with external devices that conform to the IEEE 1394 specification.



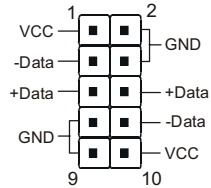
LED1: 80 Port Debug LED



80 port Debug 7-segment LED display (Refer to Appendix E for POST codes)



USB3: USB port header for two USB2.0 ports. The USB3 is used to connect bluetooth module for wireless connection.



USB port header pin descriptions.

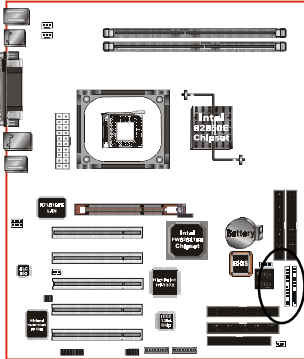
PIN#	Wire color	Signal Name	Comment
1	Red	Vcc	Cable Power
2	Black	Ground	Case Ground
3	White	-Data	Data
4	Black	Ground	Cable Ground
5	Green	+Data	Data
6	Green	+Data	Data
7	Black	Ground	Cable Ground
8	White	-Data	Data
9	Black	Ground	Case Ground
10	Red	Vcc	Cable Power

CAUTION !

Please make sure the USB cable has the same pin assignment. The different pin assignment may be caused damage of system.

If you need our USB cable, please contact our retailer.

Installation



◆ Power On/Off

(This is connected to the power button on the case. Using the Soft-Off by Pwr-BTTN feature, you can choose either Instant Off (turns system off immediately), or 4 sec delay (you need to push the button down for 4 seconds before the system turns off). When the system is in 4 sec delay mode, suspend mode is enabled by pushing the button momentarily.)

◆ Turbo LED indicator

◆ IDE LED indicator

LED ON when Onboard PCI IDE Hard disks is activate

◆ IR Connector

- | | |
|----------|---------|
| 1. VCC | 4. GND |
| 2. CIRRX | 5. IRTX |
| 3. IRRX | |

◆ KeyLock

Keyboard lock switch & Power LED connector

- | | |
|-----------------|------------|
| 1. Power LED(+) | 4. KeyLock |
| 2. N/C | 5. GND |
| 3. GND | |

* The power LED lights when the system is powered on and blinks in SLEEP Mode or STR Mode.

◆ Speaker

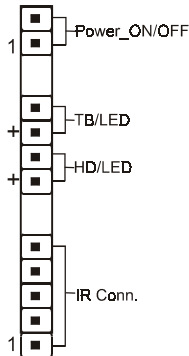
Connect to the system's speaker for beeping

- | | |
|------------|--------|
| 1. Speaker | 3. GND |
| 2. N/C | 4. VCC |

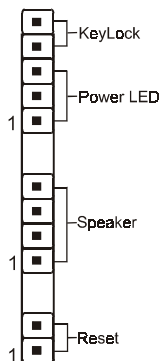
◆ Reset

Closed to restart system.

J3



J2



3-5 STR (Suspend To RAM) Function

This mainboard supports the STR (Suspend To RAM) power management scheme by maintaining the appropriate power states in the DDR SDRAM interface signals. The power source to the DDR SDRAM must be kept active during STR (ACPI S3). Advanced Configuration Power Interface (ACPI) provides many Energy Saving Features for operating systems that support Instant ON and QuickStart™ function.

1. Use the STR functionality to save system power, you are recommended to confirm the following requirements:
 - a. Install ACPI qualified add-on cards (such as AGP, LAN, and modem cards).
 - b. In BIOS under Power Management Setup (refer to Section 4), select “ACPI Suspend Type: S3(STR)” and “USB Wake Up From S3: Enabled” (if you have a USB mouse or keyboard device).
 - c. Install Windows® XP/2000/ME/98SE.
 - d. Restart the system.
 - e. Open the Control Panel Power Management application, and click the Advanced tab. In the Power buttons section, select “Stand By” from the drop-down lists.
2. To enable the STR function, click the START button and choose Shut Down. In the Shut Down Windows dialog box, select the Stand By option to enter STR mode.

The following lists the differences between STR power saving mode and Green (or Suspend) mode:

- a. STR is the most advanced Power Management mode.
- b. STR cuts all the power supplied to peripherals except to memory - max. power saving.
- c. STR saves and keeps all on-screen data including any executed applications to DDR SDRAM.
- d. In STR mode, you must push the power button (connected to the onboard J3 pin), click your USB mouse buttons, or press your USB keyboard keys to wake up your system to the last display.

Installation

3-7 850E Platform AGP Card 3.3V Protection

The Intel® 850E chipset supports 1.5 volt AGP graphics cards only. Using a 3.3 volt AGP card in an Intel® 850E chipset-based board might damage the chipset on an 845E equipped mainboard. However, this mainboard features a protection function that prevents the system from powering on when a 3.3V AGP card is inadvertently inserted into the AGP slot.

If this happens, we recommend you to follow these steps:

Step 1: Remove the 3.3V AGP card from the AGP slot.

Step 2: Unplug the ATX power cable.

Step 3: Insert a 1.5V AGP card into the AGP slot.

Step 4: Wait for 5 ~ 7 seconds and then plug in the ATX power cord again (or turn on the ATX power switch) to turn on your system.

Note: There should be an interval of 5 ~ 7 seconds between unplugging and plugging in the power cord, or turning the ATX power supply on and off.

Section 4 AWARD BIOS SETUP

Main Menu

Award's ROM BIOS provides a built-in Setup program which allows user to modify the basic system configuration and hardware parameters. The modified data is stored in a battery-backed CMOS, so that data will be retained even when the power is turned off. In general, the information saved in the CMOS RAM will stay unchanged unless there is a configuration change in the system, such as hard drive replacement or a device is added.

It is possible for the CMOS battery to fail causing CMOS data loss. If this happens you will need install a new CMOS battery and reconfigure your BIOS settings.

To enter the Setup Program :

Power on the computer and press the key during the POST (Power On Self Test). The BIOS CMOS SETUP UTILITY opens.

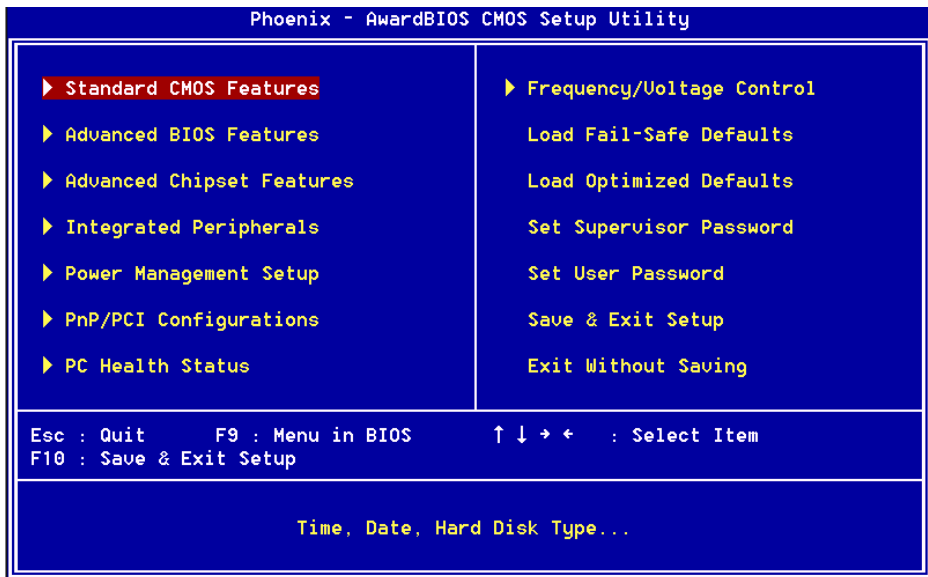


Figure 1: CMOS Setup Utility

BIOS

The main menu displays all the major selection items. Select the item you need to reconfigure. The selection is made by moving the cursor (press any direction (arrow key) to the item and pressing the 'Enter' key. An on-line help message is displayed at the bottom of the screen as the cursor is moved to various items which provides a better understanding of each function. When a selection is made, the menu of the selected item will appear so that the user can modify associated configuration parameters.

4-1 Standard CMOS Setup

Choose "STANDARD CMOS FEATURES" in the CMOS SETUP UTILITY Menu (Figure 2). Standard CMOS Features Setup allows the user to configure system settings such as the current date and time, type of hard disk drive installed, floppy drive type, and display type. Memory size is auto-detected by the BIOS and displayed for your reference. When a field is highlighted (use direction keys to move the cursor and the <Enter> key to select), the entries in the field can be changed by pressing the <PgDn> or the <PgUp> key.

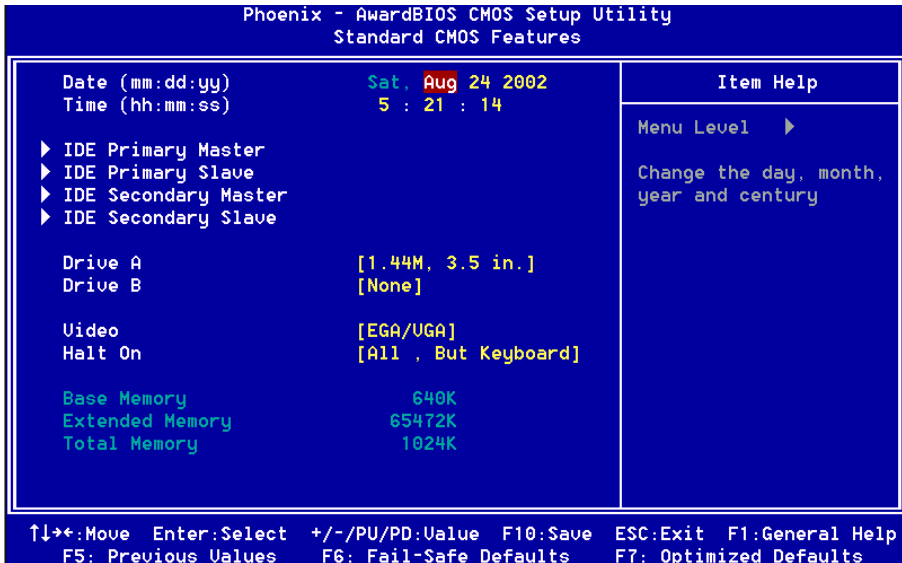


Figure 2: Standard CMOS Setup

Notes:

- If the hard disk Primary Master/Slave and Secondary Master/Slave are set to Auto, then the hard disk size and model will be auto-detected.
- The “Halt On:” field is used to determine when to halt the system by the BIOS if an error occurs.
- Floppy 3 Mode support is a mode used to support a special 3.5-inch drive used in Japan. This is a 3.5-inch disk that stores 1.2 MB. The default setting for this is disabled.

4-2 Advanced BIOS Features

Selecting the “ADVANCED BIOS FEATURES” option in the CMOS SETUP UTILITY menu allows users to change system related parameters in the displayed menu. This menu shows all of the manufacturer’s default values for the board.

Pressing the [F1] key displays a help message for the selected item.

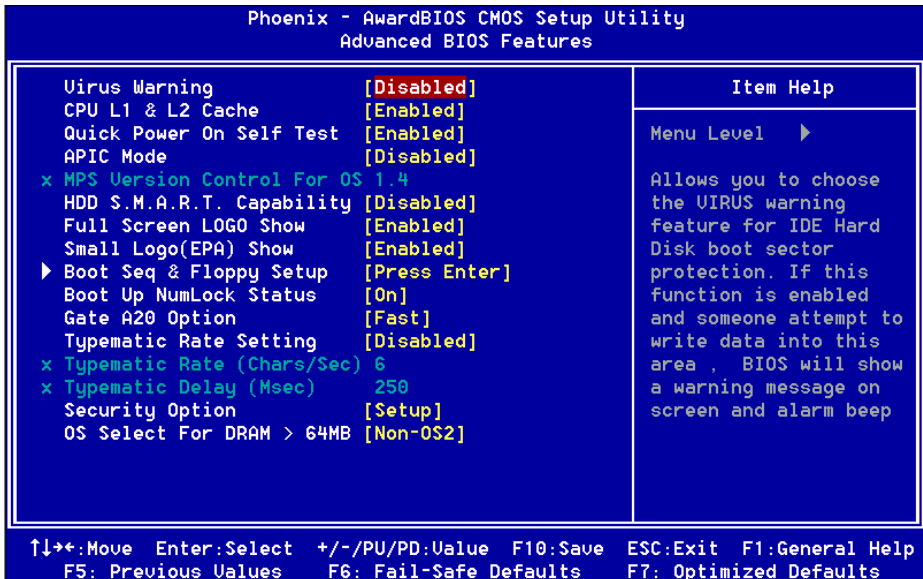


Figure 3: BIOS Features Setup

BIOS

Virus Warning

During and after system boot up, any attempt to write to the boot sector or partition table of the hard disk drive halts the system and an error message appears. You should then run an anti-virus program to locate the virus. Keep in mind that this feature protects only the boot sector, not the entire hard drive. The default is Disabled.

Enabled: Activates automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector.

Disabled: No warning message appears when anything attempts to access the boot sector.

Note: Many disk diagnostic programs that access the boot sector table can trigger the virus warning message. If you plan to run such a program, we recommend that you first disable the virus warning.

CPU L1 & L2 Cache

This controls the status of the processor's internal Level One and Level Two cache. The default is Enabled.

Enabled: This activates the processor's internal cache thereby increasing performance.

Disabled: This deactivates the processor's internal cache thereby lowering performance.

Quick Power On Self Test

This category speeds up the Power On Self Test (POST). The default is Enabled.

Enabled: This setting will shorten or skip of the items checked during POST.

Disabled: Normal POST.

APIC Mode

This item allows you to enable APIC (Advanced Programmable Interrupt Controller) functionality. APIC is an Intel chip that provides symmetric multiprocessing (SMP) for its Pentium systems. The default is Disabled.

Options: Enabled, Disabled.

MPS Version Control For OS

Specifies the Multiprocessor Specification (MPS). Version 1.4 supports multiple PCI bus configurations by incorporating extended bus definitions. Enable this for Windows NT or Linux. For older operating systems, select Version 1.1. The default is 1.4. Options: 1.1, 1.4.

HDD S.M.A.R.T. Capability

The S.M.A.R.T. (Self-Monitoring, Analysis, and Reporting Technology) system is a diagnostics technology that monitors and predicts device performance. S.M.A.R.T. Software resides on both the disk drive and the host computer.

The disk drive software monitors the internal performance of the motors, media, heads, and electronics of the drive. The host software monitors the overall reliability status of the drive. If a device failure is predicted, the host software, through the Client WORKS S.M.A.R.T applet, warns the user of the impending condition and advises appropriate action to protect the data. The default is Disabled.

Options: Enabled, Disabled.

Full Screen LOGO Show

This option allows you determine Full Screen LOGO display when POST.

Options: Enabled, Disabled.

Small Logo (EPA) Show

This option allows you determine EPA Logo display at screen top-right corner.

Options: Enabled, Disabled.

Boot Up NumLock Status

This controls the state of the NumLock key when the system boots. The default is On.

On: The keypad acts as a 10-key pad.

Off: The keypad acts like cursor keys.

Gate A20 Option

This refers to the way the system addresses memory above 1 MB (extended memory). The default is Normal.

Normal: The A20 signal is controlled by the keyboard controller or chipset hardware.

Fast: The A20 signal is controlled by Port 92 or chipset specific method.

Typematic Rate Setting

This determines the keystrokes repeat rate. The default is Disabled.

Enabled: Allows typematic rate and typematic delay programming.

Disabled: The typematic rate and typematic delay will be controlled by the keyboard controller in your system.

BIOS

Typematic Rate (Chars/Sec)

This is the number of characters that will be repeated by a keyboard press. The default is 6.

Options: 6 ~ 30 characters per second.

Typematic Delay (msec)

This setting controls the time between the first and the second character displayed by typematic auto-repeat. The default is 250.

Options: 250/500/750/1000 msec.

Security Option

This category allows you to limit access to the System and Setup, or just to Setup. The default is Setup.

System: The system will not boot and the access to Setup will be denied if the correct password is not entered at the prompt.

Setup: The system will boot; but the access to Setup will be denied if the incorrect password is not entered at the prompt.

OS Select For DRAM > 64 MB

Some operating systems require special handling. Use this option only if your system has greater than 64 MB of memory. The default is Non-OS2.

OS2: Select this if you are running the OS/2 operating system with greater than 64 MB of RAM.

Non-OS2: Select this for all other operating systems and configurations.

► Boot Seq & Floppy Setup

Scroll to Boot Seq & Floppy Setup and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility	
Boot Seq & Floppy Setup	
First Boot Device	[Floppy]
Second Boot Device	[HDD-0]
Third Boot Device	[LS120]
Boot Other Device	[Enabled]
Swap Floppy Drive	[Disabled]
Boot Up Floppy Seek	[Enabled]
Report No FDD For WIN 95	[No]

Item Help
Menu Level ►►
Select Your Boot Device Priority

First /Second/Third/Other Boot Device

The BIOS attempts to load the operating system from the devices in the sequence selected in these items.

Options: Floppy, LS120, HDD-0, SCSI, CDROM, HDD-1, HDD-2, HDD-3, ZIP100, USB-FDD, USB-ZIP, USB-CDROM, USB-HDD, LAN, Disabled.

Boot Other Device

When enabled, the system searches all other possible locations for an operating system if it fails to find one in the devices specified under the first, second, and third boot devices. The default is Enabled.

Options: Enabled, Disabled.

Swap Floppy Drive

This will swap your physical drive letters A & B if you are using two floppy disks. The default is Disabled.

Enabled: Floppy A & B will be swapped under the O/S.

Disabled: Floppy A & B will be not swapped.

Boot Up Floppy Seek

If this item is enabled, it checks the size of the floppy disk drives at start-up time. You don't need to enable this item unless you have a legacy diskette drive with 360K capacity. The default is Disabled.

Options: Enabled, Disabled.

Report No FDD For Win 95

If you are running a system with no floppy drive and using Windows 95, select Yes for this item to ensure compatibility with the Windows 95 logo certification. Otherwise, select No.

Yes: The system has no floppy drive and you are using Windows 95.

No: The system has an operating system other than Windows 95.

BIOS

4-3 Advanced Chipset Features

Choose the “ADVANCED CHIPSET FEATURES” option in the CMOS SETUP UTILITY menu to display following menu.

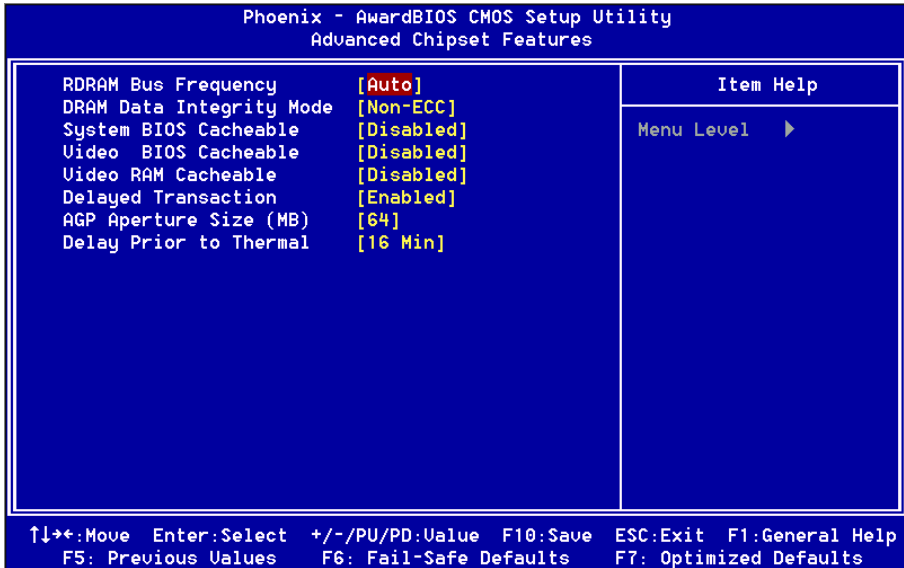


Figure 4: Chipset Features Setup

DRAM Bus Frequency

This item allows you select DRAM Bus Frequency.
The Choice: AUTO, 400MHz, 533MHz, 300MHz.

DRAM Data Integrity Mode

Use this option to configure the type of DRAM in your system.
The choice: No-ECC, ECC.

System BIOS Cacheable

This item allows the system to be cached in memory for faster execution. The default is Enabled.
Options: Disabled, Enabled.

Video BIOS Cacheable

This item allows the video to be cached in memory for faster execution. The default is Disabled.
Options: Disabled, Enabled.

Video RAM Cacheable

This option allows the CPU to cache read/writes of the video RAM.

Options: Disabled, Enabled.

Delayed Transaction

The mainboard's chipset has an embedded 32-bit post write buffer to support delay transactions cycles. Select Enabled to support compliance with PCI specification version 2.1. The default is Enabled.

Options: Disabled, Enabled.

AGP Aperture Size (MB)

This item defines the size of the aperture if you use an AGP graphics adapter. It refers to a section of the PCI memory address range used for graphics memory. The default is 64 MB.

Options: 4, 7, 16, 32, 64, 128, 256 MB.

Delay Prior to Thermal

Set this item to enable the CPU Thermal function to engage after the specified time. The default is 16 minutes.

Options: 4, 8, 16, 32 minutes.

4-4 Integrated Peripherals

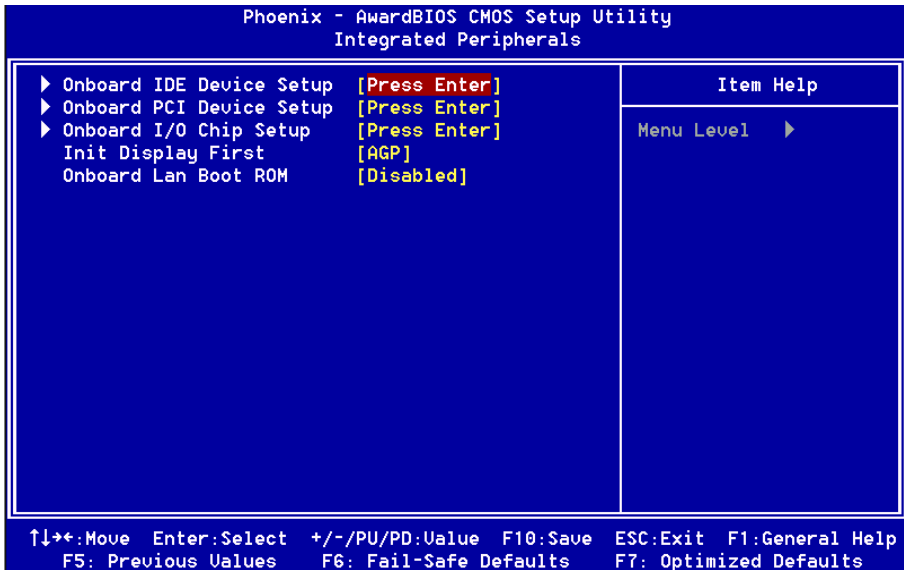


Figure 5: Integrated Peripherals

Notes:

- If you do not use the Onboard IDE connector, then you will need to set Onboard Primary PCI IDE: Disabled and Onboard Secondary PCI IDE: Disabled
- The Onboard PCI IDE cable should be equal to or less than 18 inches (45 cm.).

Init Display First

If two video cards are used (1 AGP and 1 PCI) this specifies which one will be the primary display adapter. The default is PCI Slot.

Options: PCI Slot, AGP.

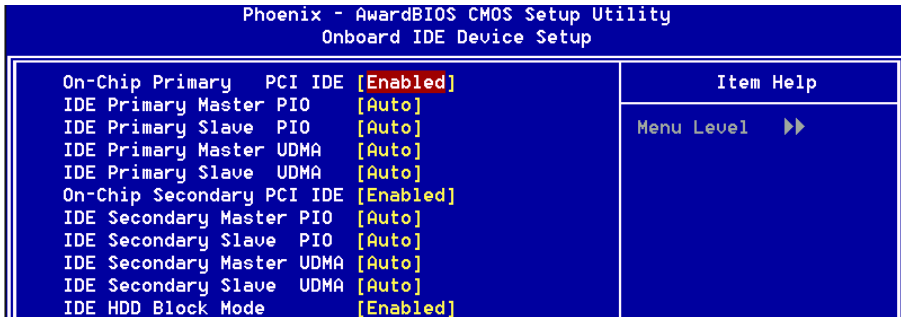
Onboard Lan Boot ROM

Enables and disables the onboard LAN Boot ROM. The default is Disabled.

Options: Enabled, Disabled.

► **Onboard IDE Device Setup**

Scroll to Onboard IDE Device Setup and press <Enter>. The following screen appears:



On-Chip Primary PCI IDE

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select Enabled (default) to activate each channel separately. Options: Enabled, Disabled.

IDE Primary/Secondary Master/Slave PIO

The four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device. The default is Auto. Options: Auto, Mode 0 ~ 4.

IDE Primary/Secondary Master/Slave UDMA

This allows you to select the mode of operation for the Ultra DMA-33/66/100 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If your hard drive and your system software both support Ultra DMA-33/66/100, select Auto to enable UDMA mode by BIOS or you can select mode by manual.

Options: Auto, Disabled.

IDE HDD Block Mode

IDE Block Mode allows the controller to access blocks of sectors rather than a single sector at a time. The default is Enabled.

Enabled: Enable IDE HDD Block Mode. Provides higher HDD transfer rates.

Disabled: Disable IDE HDD Block Mode.

BIOS

► Onboard PCI Device Setup

Scroll to Onboard PCI Device Setup and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility		
Onboard PCI Device Setup		
USB Controller	[Enabled]	Item Help
EHCI(USB2.0) Controller	[Disabled]	
USB Keyboard Support	[Disabled]	Menu Level ►►
USB Mouse Support	[Disabled]	
AC97 Audio	[Auto]	
HighPoint device	[AUTO]	
Game Port Address	[201]	
Midi Port Address	[Disabled]	
x Midi Port IRQ	10	

USB Controller

Enables the all USB controller.

Options: Disabled, Enabled.

EHCI (USB2.0) Controller

Enables the EHCI (USB2.0) controller.

Options: Disabled, Enabled.

USB Keyboard Support

Your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard Device. The default is Auto detect.

Options: Enabled, Disabled.

AC97 Audio

This item allows you to decide to enable or disable the chipset family to support AC97 Audio. The function setting AC97 Audio Codec states. The system default is Auto.

Options: Auto, Disabled.

High Point IDE RAID

This item enables the onboard PCI device High Point IDE RAID.

Options: Auto (default), Enabled, Disabled.

Game Port Address

Select an address for the Game port.

Options: 201 (default), 209, Disabled.

Midi Port Address

Select an address for the Midi port.

Options: 290, 300, 330 (default), Disabled.

Midi Port IRQ

Select an interrupt for the Midi port. Options: 5, 10 (default).

► Onboard I/O Chip Setup

Scroll to Onboard I/O Chip Setup and press <Enter>. The following screen appears:

Phoenix - AwardBIOS CMOS Setup Utility		Onboard I/O Chip Setup	
		Item	Help
Onboard FDC Controller	[Enabled]		
Onboard Serial Port 1	[3F8/IRQ4]		
Onboard Serial Port 2	[2F8/IRQ3]		
UART Mode Select	[Normal]		
RxD , TxD Active	[Hi,Lo]		
IR Transmission Delay	[Enabled]		
UR2 Duplex Mode	[Half]		
Use IR Pins	[IR-Rx2Tx2]		
Onboard Parallel Port	[378/IRQ7]		
Parallel Port Mode	[SPP]		
EPP Mode Select	[EPP1.7]		
ECP Mode Use DMA	[3]		
		Menu Level	▶▶

Onboard FDC Controller

Select Enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install and-in FDC or the system has no floppy drive, select Disabled in this field.

Options: Enabled, Disabled.

Onboard Serial Port 1/2

Select an address and corresponding interrupt for the first and second serial ports. Options: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto.

UART Mode Select

This field allows the users to configure what IR mode the 2nd serial port should use. The default is Normal.

Options: Normal, IrDA and ASKIR.

RxD, TxD Active

This field configures the receive and transmit signals generated from the IR port. The default is Hi Lo (when UART Mode Select is not set to Normal).

Options: Hi Hi, Hi Lo, Lo Hi, and Lo Lo.

BIOS

IR Transmission delay

This item allows you to enable/disable IR transmission delay.

Options: Enabled, Disabled.

UR2 Duplex Mode

This item allows you to select IR half/full duplex function.

Options: Half, Full.

Use IR Pins

This item allows you to select IR transmission routes, one is Rx2, Tx2 (COM Port) and the other is IR-Rx2Tx2.

Options: IR-Rx2Tx2, Rx2, Tx2.

Onboard Parallel Port

This field allows the user to configure the LPT port.

Options: 378/IRQ7, 278/IRQ5, 3BC/IRQ7, Disabled.

Parallel Port Mode

This field allows the user to select the parallel port mode.

Options: SPP, EPP, ECP, ECP+EPP.

EPP Mode Select

This item allows you to determine the IR transfer mode of onboard I/O chip.

Options: EPP1.9, EPP1.7.

ECP Mode USE DMA

This field allows the user to select DMA1 or DMA3 for the ECP mode.

Options: DMA1, DMA3.

4-5 Power Management Setup

Choose the “POWER MANAGEMENT SETUP” in the CMOS SETUP UTILITY to display the following screen. This menu allows the user to modify the power management parameters and IRQ signals. In general, these parameters should not be changed unless it’s absolutely necessary.

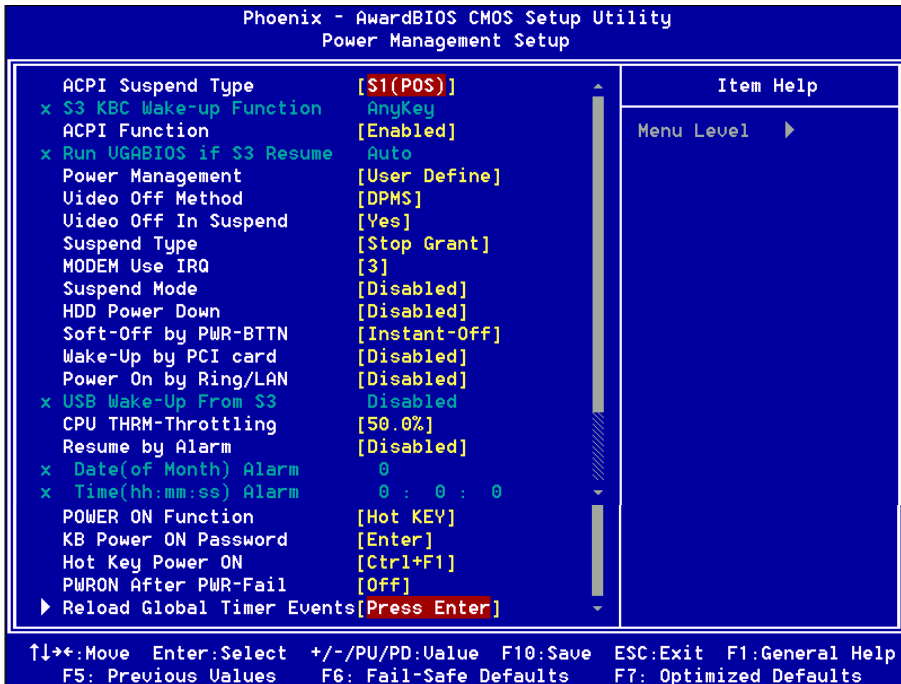


Figure 6: Power Management

ACPI Suspend Type

This item allows you to select S1(POS) or S3(STR) function. When set to “S3 (STR)” or “S1&S3” the following two fields become available.

Options: S1(POS), S3(STR), S1&S3.

S3 KB Wake-up Function

This determines whether or not to enable keyboard/mouse activity to awaken the system from S3(STR) or S1&S3.

Options: AnyKey or Mouse, By PowerOn Func., AnyKey, Mouse.

BIOS

ACPI Function

This item allows you to enable/disable the Advanced Configuration and Power Management (ACPI).

Options: Enabled, Disabled.

Run VGABIOS if S3 Resume

This determines whether or not to enable the system to run the VGA BIOS when resuming from S3(STR) or S1&S3.

Options: Auto, Yes, No.

Power Management

Use this to select your Power Management selection. The default is User define.

Max. saving: Maximum power savings. Inactivity period is 1 minute in each mode.

Min. saving: Minimum power savings. Inactivity period is 1 hour in each mode.

User define: Allows user to define PM Timers parameters to control power saving mode.

Video Off Method

This option allows you to select how the video will be disabled by the power management. The default is V/H Sync + Blank

V/H Sync + Blank: System turns off vertical and horizontal synchronization ports and writes blanks to the video buffer.

DPMS Support: Select this option if your monitor supports the Display Power Management Signaling (DPMS) standard of the Video Electronics Standards Association (VESA). Use the software supplied for your video subsystem to select video power management values.

Blank Screen: System only writes blanks to the video buffer.

Video Off In Suspend

Lets you enable the video to power off in suspend mode.

No: Video power off not controlled by power management.

Yes: Video powers off after time shown in suspend mode setting.

Suspend Type

Determines CPU status during power saving mode.

Stop Grant: CPU goes into idle mode during power saving mode.

PwrOn suspend: CPU and system remain powered on in suspend mode.

MODEM Use IRQ

Name the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system. Default is IRQ 3.

Options: N/A, 3, 4, 5, 7, 9, 10, 11

Suspend Mode

enabled and after the set time of system inactivity, all devices except the CPU will be shut off.

Options: Enabled, Disabled.

HDD Power Down

When enabled and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

Options: Enabled, Disabled.

Soft-Off by PWRBTN

Use this to select your soft-off function. The default is Instant Off.

Instant Off: Turns off the system instantly.

Delay 4 Second : Turns off the system after a 4 second delay. If momentary press of button, the system will go into Suspend Mode. Press the power button again to make system back to work.

CPU THRM-Throttling

This item sets the percentage of time that the CPU is idled if CPU throttling is initiated by excess heat. The default setting is 50%.

Options: 12.5%, 25.0%, 37.5%, 50.0%, 62.5%, 75.0%, 87.5%.

PowerOn by PCI Card

An input signal form PME on the PCI card awakens the system from a soft off state.

Options: Enabled, Disabled.

Power On by Ring/LAN

When enabled, any modem or LAN activity awakens the system from power savings mode.

Options: Enabled, Disabled.

USB Wake-Up From S3

When enabled, any USB activity awakens the system from power savings mode.

Options: Enabled, Disabled.

BIOS

CPU THRM-Throttling

Select the CPU THRM-Throttling rate.

Options: 12.5%, 25.0%, 37.5%, 50.0%, 62.5%, 75.0%, 87.5%.

Resume by Alarm

When enabled, you can set the date and time in the following two fields. Any event occurring at the specified date or time awakens the system from power savings mode.

POWER ON Function

Enables computer power on by keyboard, mouse, or hotkey activity. The default is Hot KEY.

Password: Requires you to enter a password when using the keyboard to power on. Set the password in the next field “KB Power ON Password.”

Hot KEY: Enables you to use a hot key combination to power on the computer. Set the hot key combination in the “Hot Key Power ON” field.

Any KEY: Enables you to set any keyboard activity to power on the computer.

BUTTON ONLY: Requires you to push the computer power button to power on the system.

Keyboard 98: Enables you to set the Windows 98 key to power on the system.

Keyboard Power ON Password

Press “Enter” to create a password that is required when you use the keyboard to power on the system. You must set the POWER ON Function to “Password” to be prompted for a password at power on.

Hot Key Power ON

Enables you to set a hot key combination to be used for powering on the system. The default is Ctrl-F1.

Options: Ctrl-F1 ~ Ctrl F12.

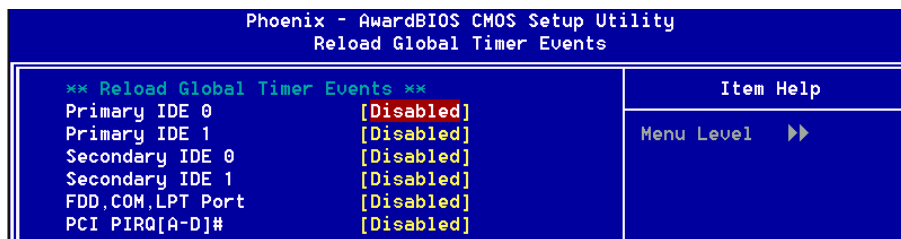
PWRON After PWR-Fail

This item enables your computer to automatically restart or return to its last operating status after power returns from a power failure.

Off: The system stays off after a power failure.

Former-Sts: The system returns to the state it was in just prior to the power failure.

**** Reload Global Timer Events ****



Primary/Secondary IDE 0/1

Any activity occurring on these channels awakens the system from power savings mode.

FDD, COM, LPT Port

When enabled, any event occurring on these ports awakens the system from power savings mode.

PCI PIRQ[A-D]#

When enabled, any event occurring on these PCI slots awakens the system from power savings mode.

4-6 PNP/PCI Configuration

The PNP/PCI configuration program is for the user to modify the PCI/ISA IRQ signals when various PCI/ISA cards are inserted in the PCI or ISA slots.

WARNING: Conflicting IRQ's may cause the system to not find certain devices.

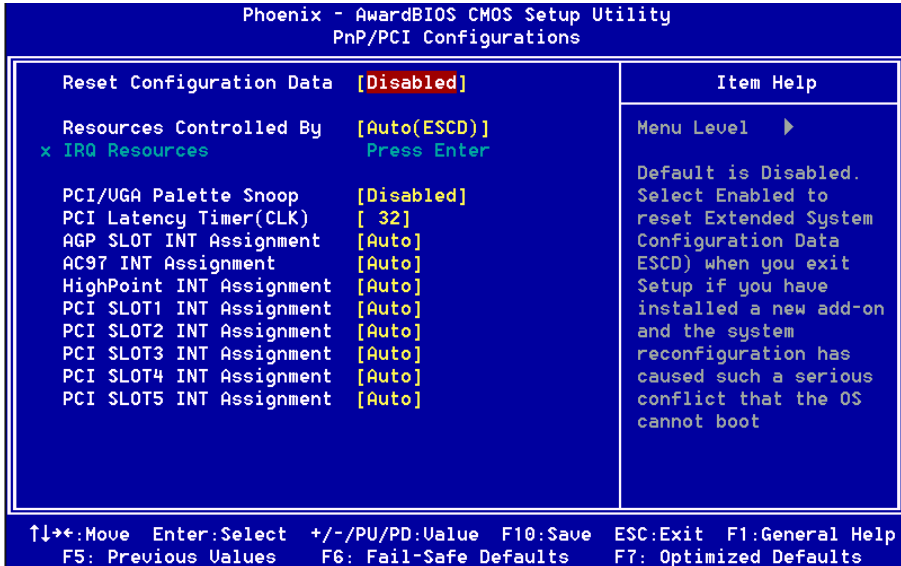


Figure 7: PNP/PCI Configuration Setup

Reset Configuration Data

This setting allows you to clear ESCD data. The default is Disabled

Disabled: Normal Setting.

Enabled: If you have plugged in some Legacy cards to the system and they were recorded into ESCD (Extended System Configuration Data), you can set this field to Enabled in order to clear ESCD.

Resources Controlled By

Determines what controls system PNP/PCI resources. The default is Auto (ESCD).

Manual: PNP Card's resources are controlled manually. The "IRQ Resources" field becomes available and you can set which IRQ-X and DMA-X are assigned to PCI/ISA PNP or Legacy ISA Cards.

Auto: If your ISA card and PCI cards are all PNP cards, BIOS assigns the interrupt resource automatically.

PCI/VGA Palette Snoop

This item is designed to overcome problems that can be caused by some nonstandard VGA cards. This board includes a built-in VGA system that does not require palette snooping so you must leave this item disabled.

Options: Enabled, Disabled.

PCI Latency Timer (CLK)

The latency timer defines the minimum amount of time, in PCI clock cycles, that the bus master can retain the ownership of the bus.

Options: 0-255.

AGP SLOT INT Assignment

This setting enables the user to specify what IRQ will be assigned to the AGP devices in the AGP slot.

Options available: Auto, 3, 4, 5, 7,9 ,10, 11, 12, 14 & 15. The default is Auto.

AC97 INT Assignment

This setting enable the user to specify what IRQ will be assigned to the AC97 device .

Options available: Auto, 3, 4, 5, 7,9 ,10, 11, 12, 14 & 15. The default is Auto.

HighPoint INT Assignment

This setting enable the user to specify what IRQ will be assigned to the HighPoint device .

Options available: Auto, 3, 4, 5, 7,9 ,10, 11, 12, 14 & 15. The default is Auto.

PCI Slot 1 ~ Slot 5 INT Assignment

These settings enables the user to specify what IRQ will be assigned to PCI devices in the chosen slot.

Options available: Auto, 3, 4, 5, 7,9 ,10, 11, 12, 14 & 15. The defaults are Auto.

BIOS

Interrupt requests are shared as shown below:

	INT A	INT B	INT C	INT D	INT E	INT F	INT G	INT H
AGP Slot	v							
AC97/MC97		v						
Slot 1			v					
Slot 2				v				
Slot 3					v			
Slot 4						v		
Slot 5							v	
Onboard LAN			v					
Onboard USB1	v							
Onboard USB2		v						
Onboard USB3			v					
USB 2.0				v				
SM BUS		v						
HighPoint								v

IMPORTANT!

If using PCI cards on shared slots, make sure that the drivers support "Shared IRQ" or that the cards don't need IRQ assignments. Conflicts will arise between the two PCI groups that will make the system unstable or cards inoperable.

4-7 PC Health Status

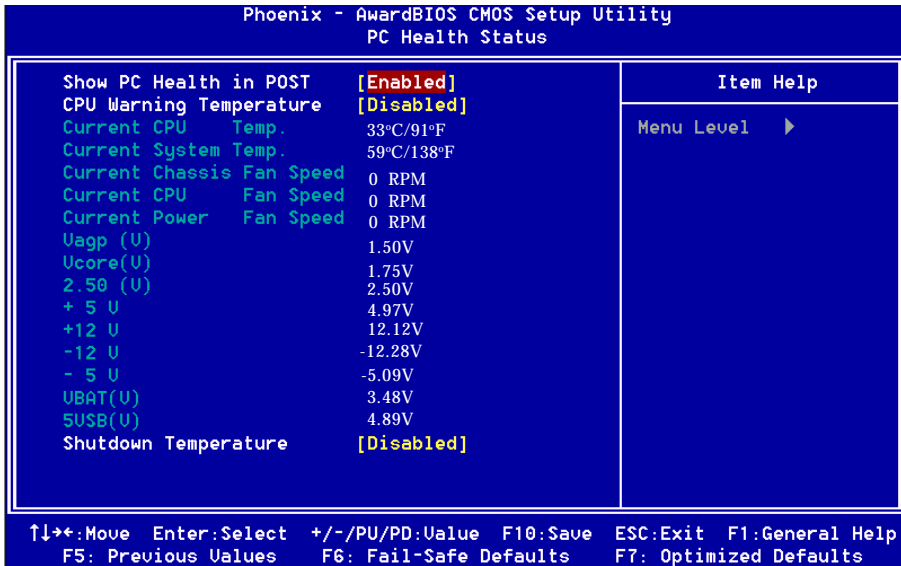


Figure 8: PC Health Status

Show PC Health in POST

When this function is enabled the PC Health information is displayed during the POST (Power On Self Test).

CPU Warning Temperature

Sets the temperature at which the computer will respond to an overheating CPU. The default is Disabled. Options: Disabled, 50°C/122°F ~ 70°C/158°F.

Current CPU Temperature

Displays the current CPU temperature.

Current System Temperature

Displays the current system temperature.

Current CPU/Chassis/Power FAN Speed

Displays the current speed of the CPU, chassis, and power fan speed in RPMs.

Vagp (V)

The voltage level of Power supplied to AGP card.

BIOS

Vcore (V)

The voltage level of the CPU(Vcore).

2.50 (V)

The voltage level of the RDRAM.

± 5V, ± 12V, VBAT(V), 5VSB(V)

The voltage level of the switching power supply.

ACPI Shutdown Temperature

This is the temperature that the computer will turn off the power to combat the effects of an overheating system. (requires ACPI to be enabled in Power Management BIOS and ACPI compliant operating system.) The default is Disabled.

Options available are 60°C/140°F to 75°C/167°F in increments of 5°C.

4-8 Frequency/Voltage Control

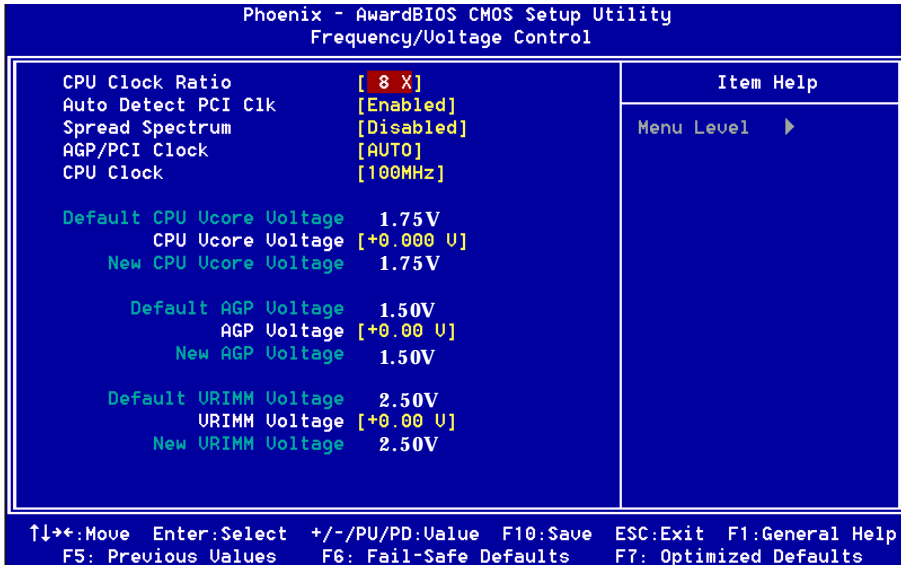


Figure 9: Frequency/Voltage Control

CPU Clock Ratio

Use this item to select a multiplier for the system frontside bus (FSB) frequency. The value of the multiplier must be set so that:

$$\text{Multiplier} \times \text{Frontside Bus Frequency} = \text{CPU Clock Speed}$$

For example, if you have a processor that is rated to run at 450 MHz and the system is running a frontside bus frequency of 100 MHz, you should select a multiplier of 4.5 so that:

$$4.5 \text{ (Multiplier)} \times 100 \text{ MHz (frontside bus)} = 450 \text{ MHz (CPU clock)}$$

Auto Detect PCI Clk

When enabled the mainboard automatically disables the clock source for a PCI slot which does not have a module in it, reducing EMI (ElectroMagnetic Interference). The default is Enabled.

BIOS

Spread Spectrum Modulated

If you enable spread spectrum, it can significantly reduce the EMI (ElectroMagnetic Interference) generated by the system.

AGP/PCI Clock

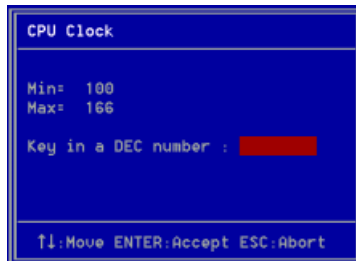
Enables you to set the host clock to work concurrently with the PCI bus and the AGP bus. The default is AUTO, if FSB > 109MHz then AGP/PCI fixed to 66MHz/33MHz.

When the FSB is 133MHz, the options will display Auto, AGP-FSB*2/4 PCI-FSB/4, AGP-66MHz PCI-33MHz.

When the FSB is 100MHz, the options will display Auto, AGP-FSB*2/3 PCI-FSB/3, AGP-66MHz PCI-33MHz.

CPU Clock

Enables you to set the CPU frontside bus speed. The default is 100 MHz. Pressing Enter displays the following screen:



Key in the DEC (decimalism) number for the CPU FSB/SPEED.

In the following items, “Default Voltage” indicates the original factory value, and “New Voltage” indicates the value that you assign.

CPU Vcore Voltage

This item allows you to set the CPU Vcore voltage. The default is -0.075V. Options: -0.100V to +0.350V in 0.025V increments. We recommend that you leave this at the default value.

AGP Voltage

This item allows you to set the AGP slot voltage. The default is +0.00V. Options: +0.00V to +0.70V in 0.10V increments. We recommend that you leave this at the default value.

DIMM Voltage

This item allows you to set the DIMM slot voltage. The default is +0.00V. Options: +0.00V to +0.70V in 0.10V increments. We recommend that you leave this at the default value.

4-9 Defaults Menu

Selecting “Defaults” from the main menu shows you two options which are described below

Load Fail-Safe Defaults

When you press <Enter> on this item you get a confirmation dialog box:

Load Fail-Safe Defaults (Y/N) ? N

Pressing ‘Y’ loads the BIOS default values for the most stable, minimal-performance system operations.

Load Optimized Defaults

When you press <Enter> on this item you get a confirmation dialog box:

Load Optimized Defaults (Y/N) ? N

Pressing ‘Y’ loads the default values that are factory settings for optimal performance system operations.

4-10 Supervisor/User Password Setting

These items are used to install a password. A Supervisor password takes precedence over a User password, and the Supervisor limits the activities of a User. You can set either a supervisor or user password, or both of them:

Supervisor password: authorized to enter and change the options of the setup menus.

User password: authorized to enter, but not authorized to change the options of the setup menus.

When you select Set User/Supervisor Password, the following message appears prompting you to type a password:

ENTER PASSWORD:

Type the password, up to eight characters in length, and press <Enter>. The password typed now clears any previously entered password from CMOS memory. You will be prompted to confirm the password. Type the password and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable a password, press <Enter> when you are prompted to enter the password. A message will confirm the password is disabled:

PASSWORD DISABLED.

Once the password is disabled, the system will boot and you can enter Setup freely. When a password has been enabled, you will be prompted to enter it every time you try to enter Setup. This prevents an unauthorized person from changing any part of your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time your system is rebooted. This prevents unauthorized use of your computer.

You determine when the password is required within the BIOS Features Setup menu “Security” option. If the Security option is set to “System”, the password will be required both at boot and at entry to Setup. If set to “Setup”, prompting only occurs when trying to enter Setup.

4-11 Exiting BIOS

Save & Exit Setup

Pressing <Enter> on this item asks for confirmation:

Save to CMOS and EXIT (Y/N)? Y

Pressing “Y” stores the selections made in the menus in CMOS – a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS. After saving the values the system is restarted again.

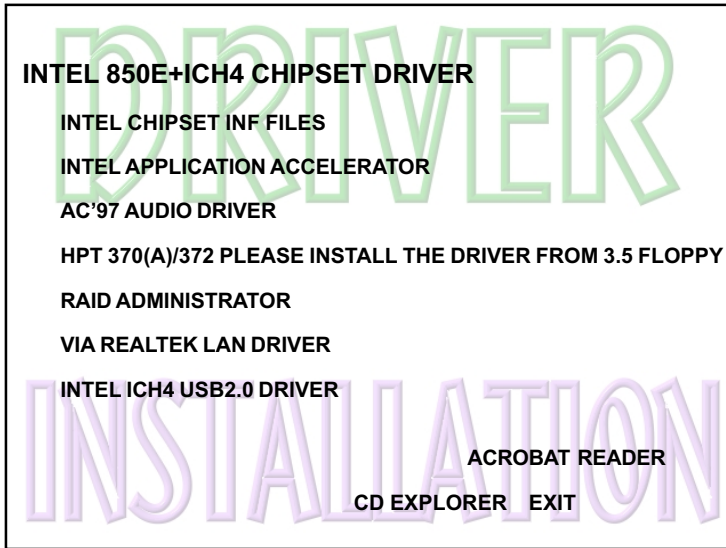
Exit Without Saving

Pressing <Enter> on this item asks for confirmation:

Quit without saving (Y/N)? Y

This allows you to exit Setup without storing in CMOS any change. The previous selections remain in effect. This exits the Setup utility and restarts your computer.

Easy Driver Installation



Insert the bundled CD-disk.

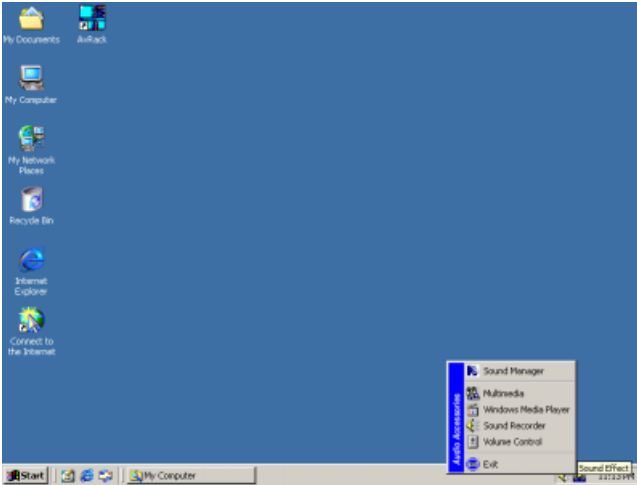
- Step 1 :** Click “**INTEL CHIPSET INF FILES**”. Install all components recommended.
 - Step 2 :** Click “**INTEL APPLICATION ACCELERATOR**”. to install ultra storage driver.
 - Step 3 :** Click “**AC'97 AUDIO DRIVER**” to install Audio Driver.
 - Step 4 :** Click “**VIA REALTEK LAN DRIVER**” to install LAN driver.
 - Step 5 :** Click “**INTEL ICH4 USB 2.0 DRIVER**” to install USB Driver.
- Note :** If you install the “**HIGH POINT 370(A)/372**” Driver, please install the driver from 3.5 floppy.

The “**RAID ADMINISTRATOR**” item is for install Raid Administrator.

Drivers Installation

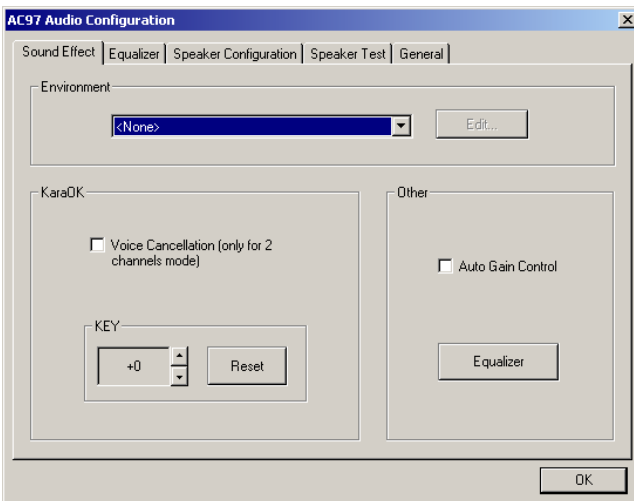
ALC650 Configuration Setup (6 Channel)

- ◆ To enable ALC650 Function



<Figure 1>

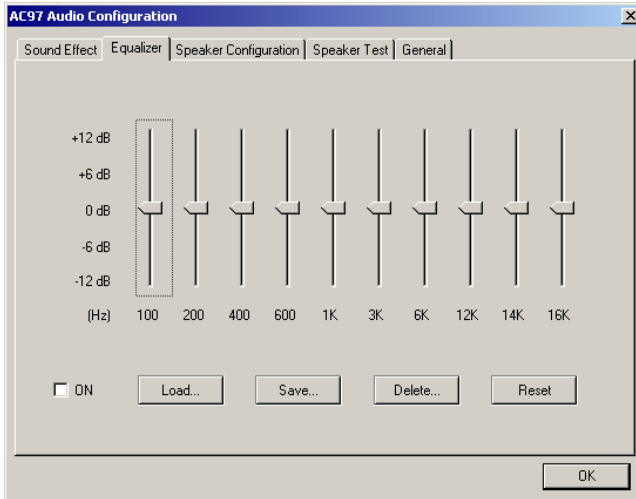
1. Right-click **Sound Effect** button in the tool bar display currently selected Titles. Select **Sound Manager**.



Sound Effect:

<Figure 2>

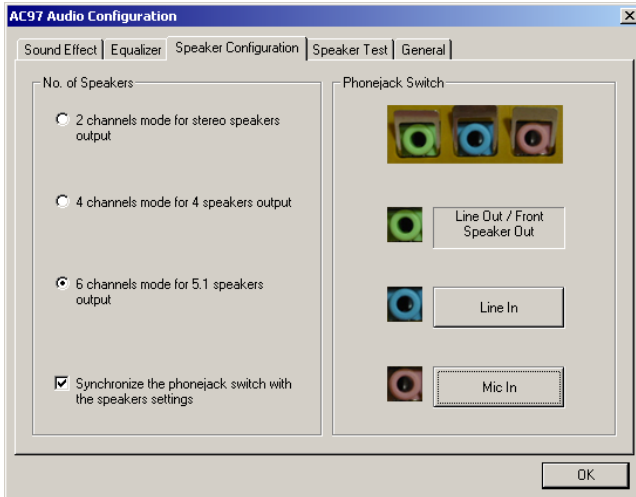
2. Click **Sound Effect** button and select **Environment** from the drop-down menu.



Equalizer:

<Figure 3>

3. Click **Equalizer** and setup the value of dB.

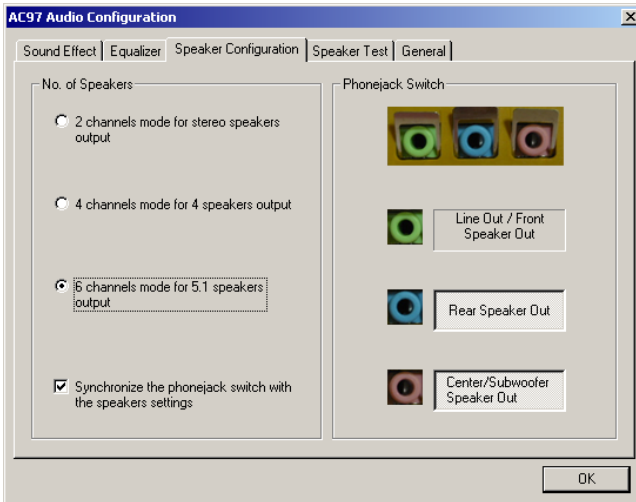


Speak Configuration:

<Figure 4>

4. Click **Line in** and **Mic in** buttons to enable 6 channel function as this is required for the ALC650.

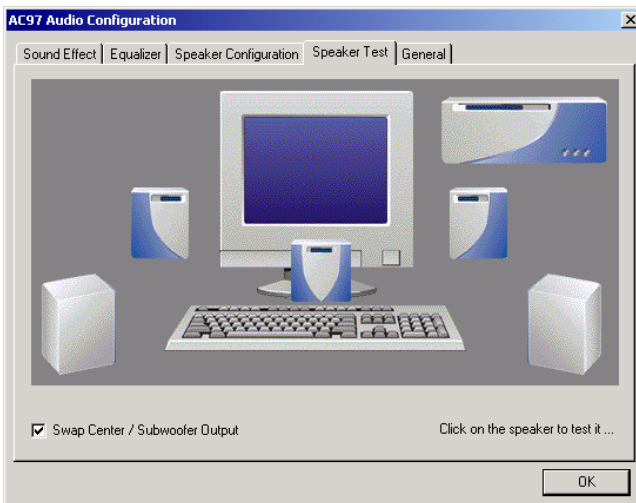
Drivers Installation



Speak Configuration:

<Figure 5>

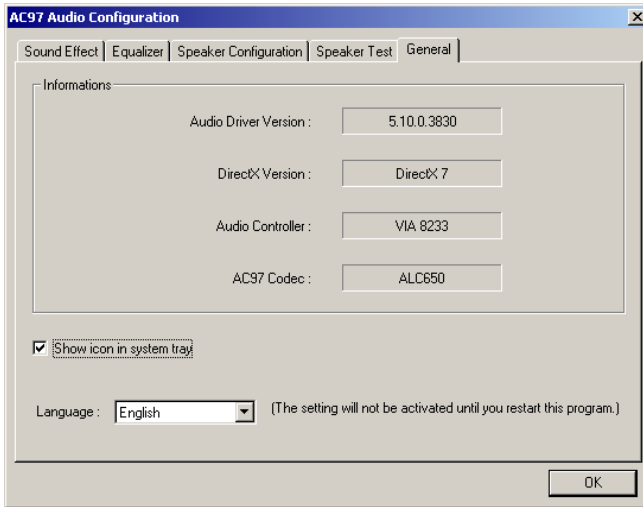
5. The selected screen appears.



Speaker Test:

<Figure 6>

6. Click **Speaker Test** button and click on the speakers directly which show on the screen to test it.

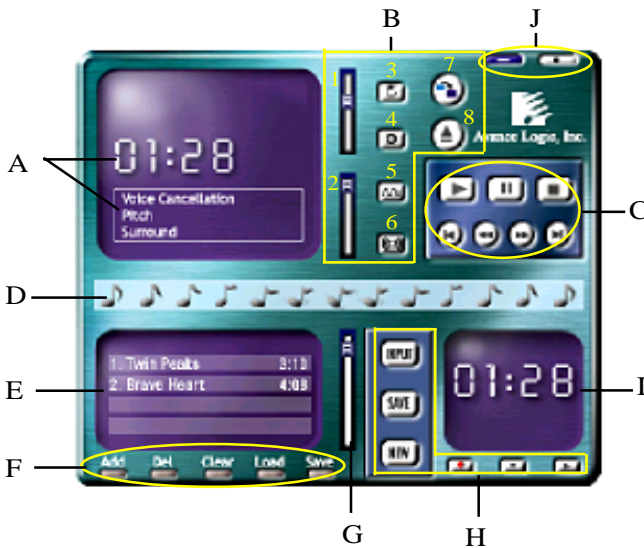


General:

<Figure 7>

7. General Information for user reference.

A-1 Avance® Media Player User's Guide

Avance® Media Player Platform**Functional Descriptions****A. Playback Windows Display**

Playback windows displays the following mode information:

1. Playback Time Display
2. Voice Cancellation Mode Display
3. Pitch Mode Display
4. Surround Sound Mode Display

B. Playback Function Controls

There are 8 selectable functions for the playback:

1. Volume control High/Low Adjustment Bar.
2. Pitch control 4-step High/Low Adjustment Bar.

Appendix

- 3. Repeat mode Choice of Repeat, All Repeat, Random or No Repeat Mode.
- 4. Mute Mute On/Off Mode select.
- 5. Voice cancellation Voice Cancellation On/Off Mode select for Karaoke.
- 6. Surround mode A total of 26 Surround Sound mode select as shown in the table below.

Surround mode	Surround mode
Generic	Stone corridor
Padded	Alley
Room	Forrest
Bathroom	City
Living room	Mountain
Stone	Quarry
Auditorium	Plain
Concert	Parking lot
Cave	Sewer pipe
Arena	Under water
Hangar	Drug
Carpet	Dizzy
Hallway	Psychological

- 7. Skin change Media Player Skin Type select.
- 8. Open Open file formats including MP3, CDA, MDI, WAV & WMA support.

C. Playback Controls

The playback controls include “Play”, “Pause”, “Stop”, “Previous”, “Backward”, “Forward”, & “Next”.

D. Seeking bar

Display Animated Playback Status

E. Title/Play List Windows

Display Currently Selected Title(s)

F. Title/Play List Edit Controls

There title/play list controls include “Add”, “Del”, “Clear”, “Load”, & “Store”.

1. Add Add to the Title/Play List.
2. Del Remove form the Title/Play List.
3. Clear Clear the Title/Play Lost.
4. Load Load Title/Play List.
5. Store Save Title/Play List.

G. Title/Play List Scroll bar

Scroll Up/Down the Title/Play List.

H. Recording Function Controls

The recording function controls include “Input”, “Save:”, “New”, “Rec”, “Stop”, & “Play”.

1. Input Input soruce select.
2. Save Save to file.
3. New Open new file & select format includes Sampling Rate, Sampling bit, Mono or Stereo.
4. Rec Start Rec.
5. Stop Stop Rec.
6. Play Playback Rec file.

I. REC/Playback Time Display

Displays REC/Playback Time.

J. Platform Display Panel Controls

The platform display panel control include “Minimize” & “Close”.

1. Minimize Minimize Platform Display Panel.
2. Close Close/Exit Platform Display Panel.

B-1 Update Your System BIOS

Download the xxxxx.EXE file corresponding to your model form the our website to an empty directory on your hard disk or floppy. Run the downloaded xxxxx.EXE file and it will self extract. Copy these extracted files to a bootable DOS floppy disk.

Note: The DOS floppy disk should contain NO device drivers or other programs.

1. Type "A:\AWDFLASH and press <Enter> Key.
2. You will see the following setup on screen.
3. Please key in the xxxxx.bin BIOS file name.

```
FLASH MEMORY WRITER V7.88
(C)Award Software 2000 All Rights Reserved

For xxxx-W83627-6A69LPA9C-0  DATE: 05/11/2000
Flash Type -
File Name to Program : _____

Error Message:
```

4. If you want to save the previous BIOS data to the diskette, please key in [Y], otherwise please key in [N].

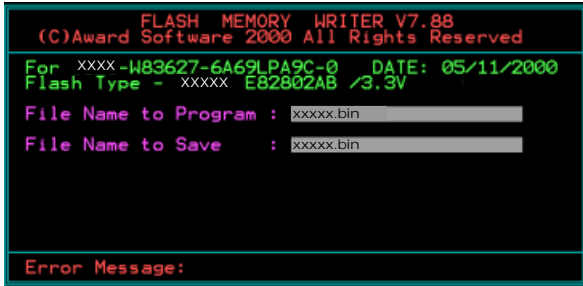
```
FLASH MEMORY WRITER V7.88
(C)Award Software 2000 All Rights Reserved

For xxxx-W83627-6A69LPA9C-0  DATE: 05/11/2000
Flash Type - xxxxx E82802AB /3.3V
File Name to Program : xxxxx.bin

Error Message: Do You Want To Save Bios (Y/N)
```

Appendix

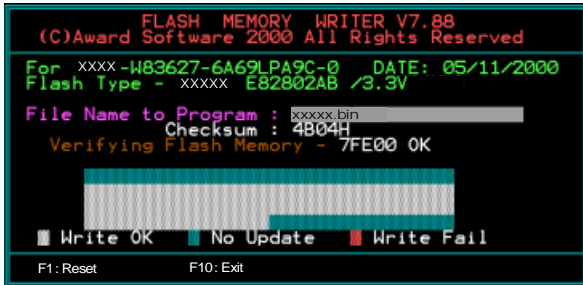
5. Key in File Name to save previous BIOS to file.



6. Are you sure to program (y/n), please key in [Y] to start the programming.

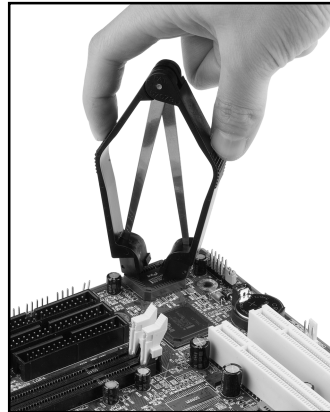
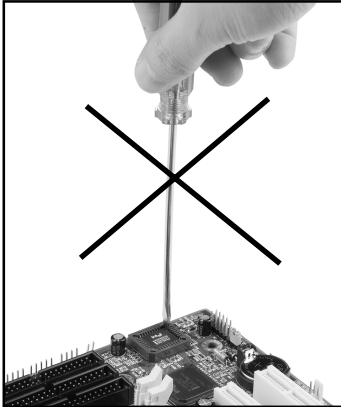


7. The programming is finished.



C-1 EEPROM BIOS Remover

Do not remove the BIOS chip, unless instructed by a technician and only with a PLCC IC extractor tool.

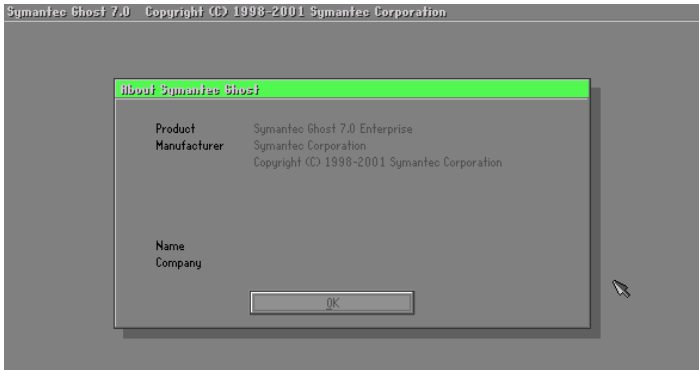


The BIOS socket may be damaged if using an improper method to replace the BIOS chip.

D-1 GHOST 7 Quick User's Guide (Optional)

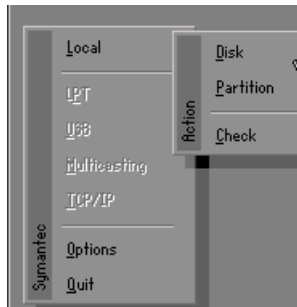
Installation is very easy. You only need to copy the **Ghost7** folder or **Ghost.exe** to your hard disk.

Main Menu



Description of Menu

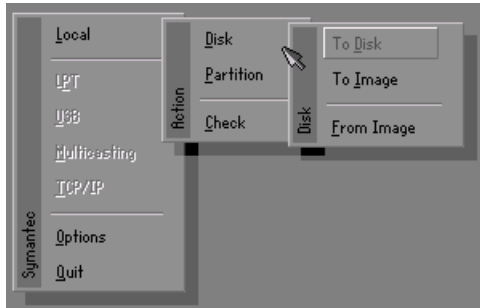
Ghost clones and backs up **Disk** and **Partition**.



In which **Disk** indicates hard disk options
Partition indicates partition options
Check indicates check options

Appendix

Disk



There are 3 hard disk functions:

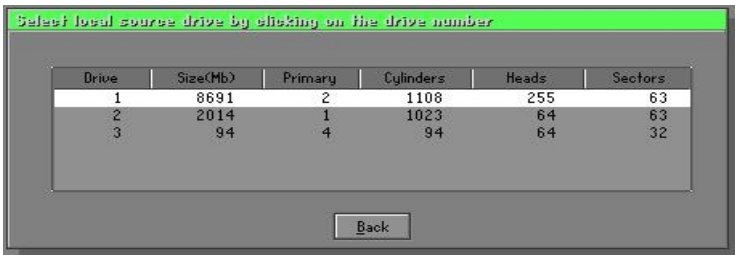
1. Disk To Disk (disk cloning)
2. Disk To Image (disk backup)
3. Disk From Image (restore backup)

Important!

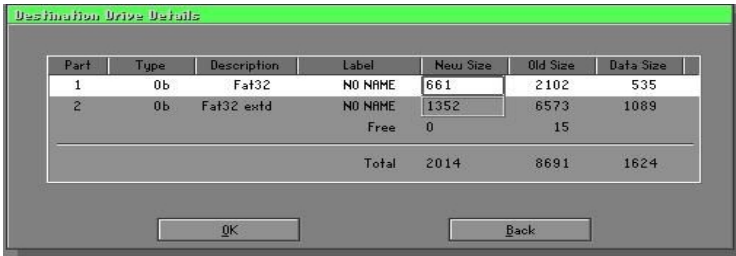
1. To use this function, the system must have at least 2 disks. Press the **Tab** key to move the cursor.
2. When restoring to a destination disk, all data in that disk will be completely destroyed.

Disk To Disk (Disk Cloning)

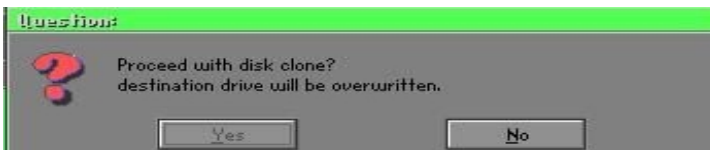
1. Select the location of the **Source drive**.
2. Select the location of the **Destination drive**.



- When cloning a disk or restoring the backup, set the required partition size as shown in the following figure.

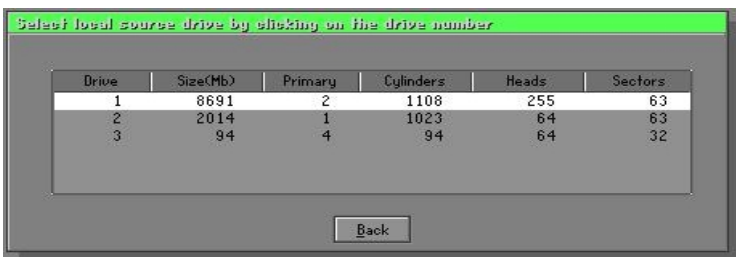


- Click OK to display the following confirmation screen. Select **Yes** to start.



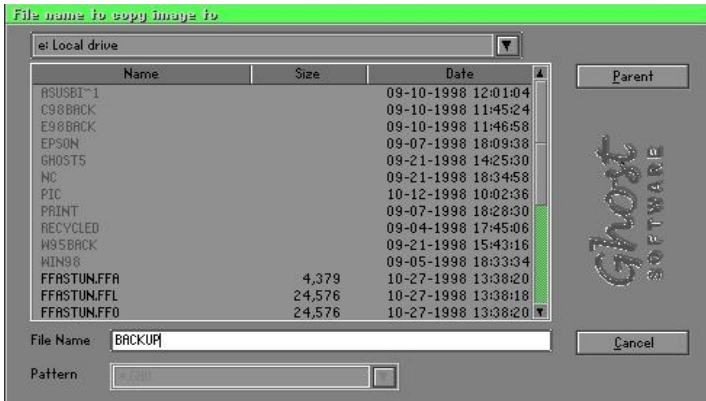
Disk To Image (Disk Backup)

- Select the location of the Source drive.



Appendix

2. Select the location for storing the backup file.

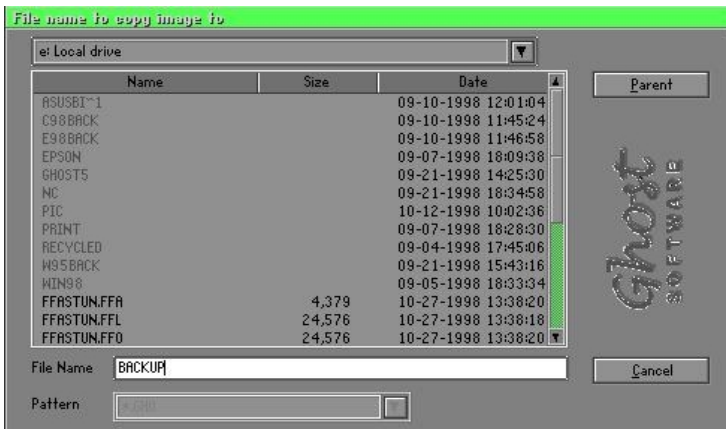


3. Click **OK** to display the following confirmation screen. Select **Yes** to start.

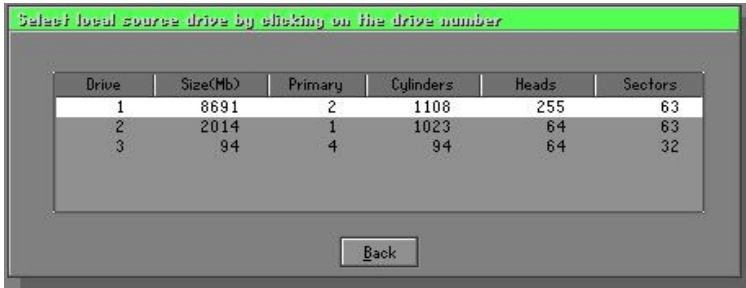


Disk From Image (Restore Backup)

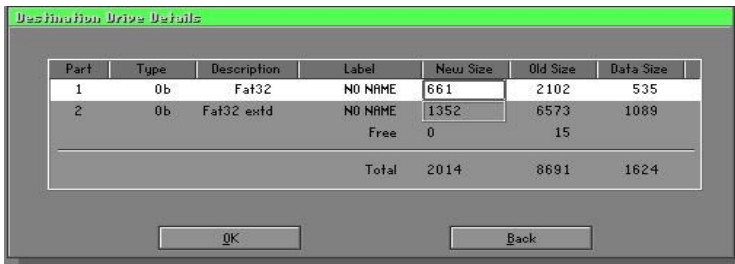
1. Select the Restoring file.



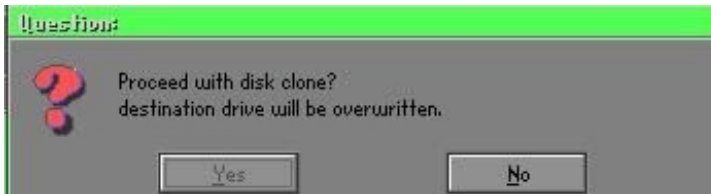
2. Select the **Destination drive** of the disk to be restored.



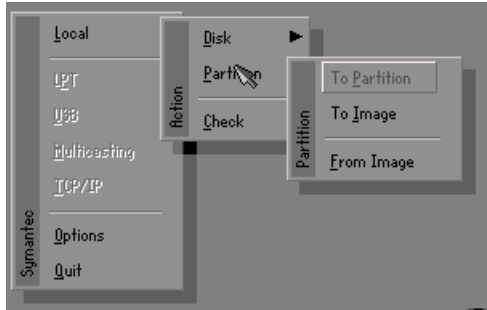
3. When restoring disk backup, set the required partition size as shown in the following figure.



4. Click **OK** to display the following confirmation screen. Select **Yes** to start.



Partition



There are 3 partition functions:

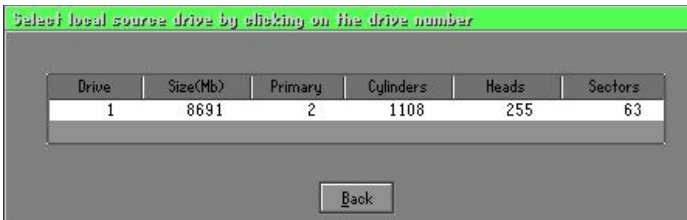
1. **Partition To Partition** (partition cloning)
2. **Partition To Image** (partition backup)
3. **Partition From Image** (restore partition)

Partition To Partition (Partition Cloning)

The basic unit for partition cloning is a “partition”. Refer to “disk cloning” for the operating method.

Partition To Image (Partition Backup)

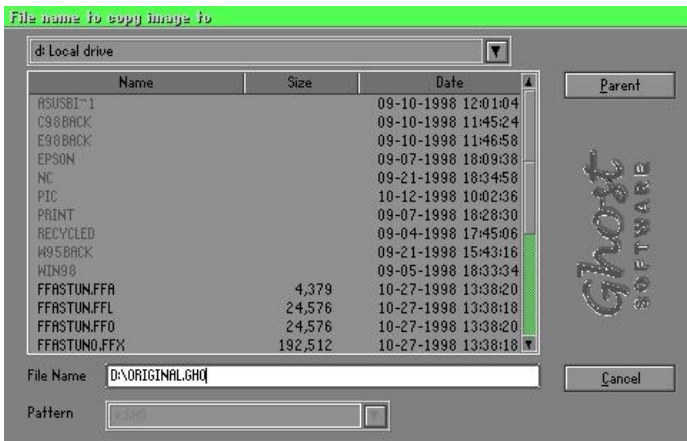
1. Select the disk to be backed up.



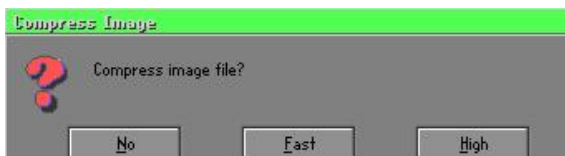
2. Select the first partition to be backed up. This is usually where the operating system and programs are stored.



3. Select the path and file name to store the backup file.



4. Is the file compressed? There are 3 options:
 - (1) No: do not compress data during backup
 - (2) Fast: Small volume compression
 - (3) High: high ratio compression. File can be compressed to its minimum, but requiring longer execution time.



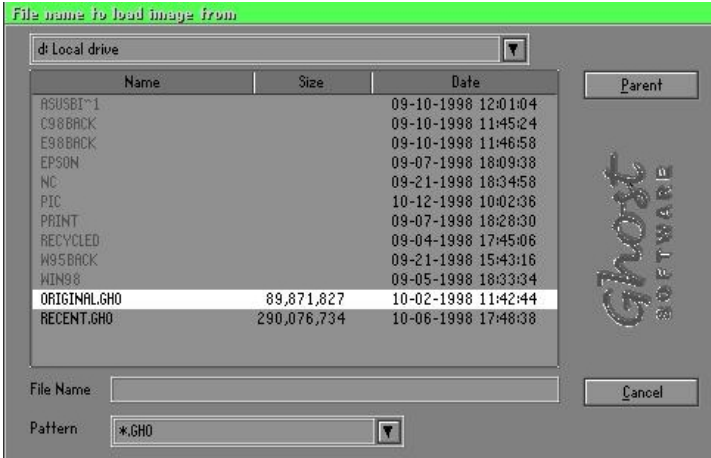
Appendix

5. Select Yes to start performing backup.



Partition From Image (Restore Partition)

1. Select the backup file to be restored.



2. Select the source partition.



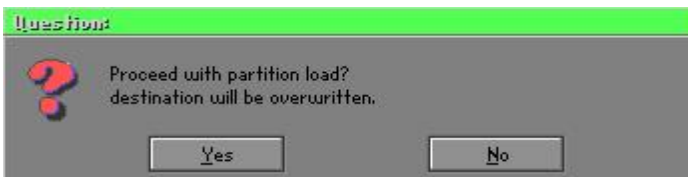
3. Select the disk to be restored.



4. Select the partition to be restored.



5. Select Yes to start restoring.



Check

This function is to check possible error caused by defective FAT or track during backup or restoring.

How to Reinstall Windows in 2 Minutes

This chapter guides you how to setup your computer properly and, if necessary, reinstall Windows in 2 minutes. Ghost provides different methods to complete this task. The following two sections explain how to create an emergency **Recover Floppy** and **Recover CD**:

Emergency Recover Floppy

Divide a hard disk into two partitions. The first partition is to store the operating system and application programs. The second partition is to back up the operating system and data. The size of the partition can be determined according to the backup requirements. For example, the **Windows** operating system needs 200MB of hard disk space, Plus complete **Office** programs require 360MB. The remaining space can be used to store data.

After installing **Windows**, use **Ghost** to create a backup area for the system and to store the file (Image file) in drive D. The file is named **Original.gho**. Then, create a recover floppy disk containing:

- ◆ Bootable files (Command.com, Io.sys, and MSDOS.SYS)
- ◆ Config.sys (configuration setup file)
- ◆ Autoexec.bat (auto-execution batch file)
- ◆ Ghost.exe (Ghost execution file)

There are two ways to create the content of the recover floppy for restoring:

- (1) To load **Windows** automatically after booting, store the **Autoexec.bat** file with a command line:

```
Ghost.exe clone, mode=pload, src=d:\original.gho:2,dst=1:1 -fx -sure -rb
```

Command Description: Runs the restore function automatically with the Image File. Stored in drive D. After execution, it will exit Ghost and boots the system.

Refer to the [Introducing Ghosts Functions] for details.

- (2) After booting, the screen displays the Menu. Select Backup or Restore: Since the user may install other applications in the future, he/she may alter **Autoexec.bat** file to back up or restore the user-defined Image file as follows:

Backup

Back up Windows and application programs as a file (Recent.gho). Command is:

```
Ghost -clone,mode=pdump,src=1:1,dst=d:\Recent.gho -fx -  
sure -rb
```

Restore

Restore types include [**General Windows**] and [**Windows and Application Programs**]. If you select [**General Windows**], the system is restored to the general **Windows** operation condition. The command is:

```
Ghost.exe -clone,mode=pload,src=d:\Original.gho,dst=1:1 -fx  
-sure -rb
```

If you select [**Windows and Application Programs**], the latest backup file (Recent.gho) is restored, skipping the installation and setup of application programs.

For description of related parameters, refer to [**Introducing Ghosts Functions**].

For more information about menu design, refer to Config.sys and Autoexec.bat under /Menu in the CD. You can also create a backup CD containing Ghost.exe and these two files.

Recover CD

The following is a simple guide to create a recover CD:

1. First, create a recover floppy disk contains the following with any copy program such as “Easy CD Create” (Note 2) :

Bootable files (Command.com and Io.sys and MSDOS.SYS)

Config.sys (Configuration setup file)

Autoexec.bat (Auto-execution batch file)

Mscdex.exe (CD-Rom execution file)

Ghost.exe (Ghost execution file)

Oakcdrom.sys (ATAPI CD-ROM compatible driver)

The content of Config.sys is:

```
DEVICE=Oakcdrom.sys /d:idecd001
```

The content of Autoexec.bat includes:

```
MSCDEX.EXE /D:IDECD001 /L:Z
```

```
Ghost.exe clone,mode=load,src=z:\original.gho,dst=1 -sure -rb
```

2. Write the backup image file (original.gho) of the entire hard disk or partition into the recover CD. Use the Recover CD to boot up the system and restore the backup files automatically.

For description of related parameters, refer to **[Introducing Ghosts Functions]**.

Note: For more details about copy the creation program and method to create a recover CD, please refer to the related software and its associated operating manual.

Ghost Command Line Switches Reference

Ghost may be executed in interactive or in batch mode. Most of the Ghost switches are used to assist in batch mode operation. To list switches, type ghost.exe -h.

-clone

The full syntax for this switch is:

```
clone,MODE={copy|load|dump|pcopy|pload|pdump},SRC=
{drive|file|drive:partition|,DST={drive|file|drive:partition},SZE{F|L|n=
{nnnnM|nnP|F|V}}
```

Clone using arguments. This is the most useful of the batch switches and has a series of arguments that define:

- a) **MODE** Defines the type of clone command, can be:
- COPY** disk to disk copy
 - LOAD** file to disk load
 - DUMP** disk to file dump
 - PCOPY** partition to partition copy
 - PLOAD** file to partition load
 - PDUMP** partition to file dump
- b) **SRC** Defines the source location of the command:
- | Mode | Meaning: |
|---------------|---|
| COPY/ | |
| DUMP | Source drive (e.g, 1 for drive one) |
| LOAD | Disk image filename or device (e.g, g:\Images\system2.img) |
| PCOPY/ | |
| PDUMP | Source partition e.g, 1:2 indicates the second partition on drive one. |
| PLOAD | Partition image filename or device and partition number. Example: g:\images\disk1.img:2 indicates the second partition in the Image file. |

Appendix

c) DST	This defines the destination location for the operation:
Mode	Meaning
COPY/	
LOAD	Destination drive (e.g, 2 for drive two)
DUMP	Disk image filename or device,(e.g, g:\images\system2.img)
PCOPY/	
PLOAD	Destination partition,(e.g, 2:2 indicates the second partition on drive two).
PDUMP	Partition image filename (e.g, g:\images\part1.img).
d) SZEy	Partition size to be transferred.

Available Options:

- F** Resizes the first partition to maximum size allowed based on file system type.
- L** Resizes the last partition to maximum size allowed based on file system type.
- n=xxxxM** - indicates that the nth destination partition is set to have a size of xxxx Mb. (e.g, SZE2=800M indicates partition two is to have 800 mb.) **n=mmP** indicates the destination partition is set to have a size of mm percent of the target disk.
- n=F** - indicates the destination partition is remaining fixed size.
- n=V** - Indicates that the partition will be resized according to the following rules:

Rule 1 - If the destination disk is larger than the original source disk, then the partition(s) will be expanded to have the maximum amount of space subject to the free space available and the partition type (e.g, FAT16 partitions will have a maximum size of 2048Mb.)

Rule 2 - If the destination disk is smaller than the original source disk, (but still large enough to accommodate the data from the source disk), the free space left over after the data space has been satisfied will be distributed between the

destination partitions in proportion to the data usage in the source partitions. Some examples follow that will help illustrate:

- fx flag Exit. Normally when Ghost has finished copying a new system to a disk, it prompts the user to reboot with a press Ctrl-Alt-Del to reboot window. However, if Ghost is being run as part of a batch file it is sometimes useful to have it just exist back to the DOS prompt after completion so that further batch commands may be processed. -fx enables this. See -rb for another option on completing a clone.
- ia Image All. The Image All switch forces Ghost to do a sector by sector copy of all partitions. When copying a partition from a disk to an image file or to another disk, Ghost examines the source partition and decides whether to copy just the files and directory structure, or to do an image (sector by sector) copy. If it understands the internal format of the partition it defaults to copying the files and directory structure. Generally this is the best option, but occasionally if a disk has been set up with special hidden security files that are in specific positions on the partition, the only way to reproduce them accurately on the target partition is via an image or sector-by-sector copy.
- span enables spanning across volumes.
- split=x splits image file into 'x' Mb? Mb spans. Use this to create a 'forced' size volume set. For example, if you would like to force smaller image files from a 1024 Megabyte drive, you could specify 200 megabyte segments. For example, ghost.exe -split=200 will divide the image into 200 Megabyte segments.
- sure use the -sure switch in conjunction with -clone to avoid being prompted with the final 'Proceed with disk clone destination drive will be overwritten?'. This command is useful in batch mode.

Appendix

Example 1:

To copy drive one to drive two on a PC, without final prompt if OK to proceed.

```
ghost.exe -clone,mode=copy,src=1,dst=2 -sure
```

Example 2:

To connect via NetBIOS to another PC running Ghost in slave mode, and dump a disk image of local drive two to the remote file c:\drive2.gho

```
ghost.exe -clone,mode=dump,src=2,dst=C:\drive2.gho -nbm
```

Note: The slave Ghost can start with ghost -nbs command

Example 3:

To copy drive one of second partition from a PC to drive two of first of the same PC,

```
ghost.exe -clone,mode=pcopy,src=1:2,dst=2:1 -sure
```

Example 4:

To dump the second partition of drive one to an image file on a mapped drive g:

```
ghost.exe -clone,mode=pdump,src=1:2,dst=g:\part2.gho
```

Example 5:

To load partition 2 from a two-partition image file on a mapped drive g: onto the second partition of the local disk

```
ghost -clone,mode=pload,src=g:\part2.gho:2,dst=1:2
```

Example 6:

To load drive 2 from an image file and resize the destination partitions into a 20:40 allocation

```
ghost.exe -clone,mode=load,src=g:\2prtdisk.gho,dst=2,size1=60P,  
size2=40P
```


E-1 POST CODES (Optional)

<u>POST (hex)</u>	<u>DESCRIPTION</u>
CFh	Test CMOS R/W functionality.
C0h	Early chipset initialization: <ul style="list-style-type: none">- Disable shadow RAM- Disable L2 cache (socket 7 or below)- Program basic chipset registers
C1h	Detect memory <ul style="list-style-type: none">- Auto-detection of DRAM size, type and ECC.- Auto-detection of L2 cache (socket 7 or below)
C3h	Expand compressed BIOS code to DRAM
C5h	Call chipset hook to copy BIOS back to E000 & F000 shadow RAM.
0h1	Expand the Xgroup codes locating in physical address 1000:0
02h	Reserved
03h	Initial Superio_Early_Init switch.
04h	Reserved
05h	<ol style="list-style-type: none">1. Blank out screen2. Clear CMOS error flag
06h	Reserved
07h	<ol style="list-style-type: none">1. Clear 8042 interface2. Initialize 8042 self-test
08h	<ol style="list-style-type: none">1. Test special keyboard controller for Winbond 977 series Super I/O chips.2. Enable keyboard interface.
09h	Reserved
0Ah	<ol style="list-style-type: none">1. Disable PS/2 mouse interface (optional).2. Auto detect ports for keyboard & mouse followed by a port & interface swap (optional).3. Reset keyboard for Winbond 977 series Super I/O chips.
0B-0Dh	Reserved

Appendix

0Eh	Test F000h segment shadow to see whether it is R/W-able or not. If test fails, keep beeping the speaker.
0Fh	Reserved
10h	Auto detect flash type to load appropriate flash R/W codes into the run time area in F000 for ESCD & DMI support.
11h	Reserved
12h	Use walking 1's algorithm to check out interface in CMOS circuitry. Also set real-time clock power status, and then check for override.
13h	Reserved
14h	Program chipset default values into chipset. Chipset default values are MODBINable by OEM customers.
15h	Reserved
16h	Initial Early_Init_Onboard_Generator switch.
17h	Reserved
18h	Detect CPU information including brand, SMI type (Cyrix or Intel) and CPU level (586 or 686).
19-1Ah	Reserved
1Bh	Initial interrupts vector table. If no special specified, all H/W interrupts are directed to SPURIOUS_INT_HDLR & S/W interrupts to SPURIOUS_soft_HDLR.
1Ch	Reserved
1Dh	Initial EARLY_PM_INIT switch.
1Eh	Reserved
1Fh	Load keyboard matrix (notebook platform)
20h	Reserved
21h	HPM initialization (notebook platform)
22h	Reserved
23h	<ol style="list-style-type: none">1. Check validity of RTC value: e.g. a value of 5Ah is an invalid value for RTC minute.2. Load CMOS settings into BIOS stack. If CMOS checksum fails, use default value instead.3. Prepare BIOS resource map for PCI & PnP use. If ESCD is valid, take into consideration of the ESCD's legacy information.

	<ol style="list-style-type: none">Onboard clock generator initialization. Disable respective clock resource to empty PCI & DIMM slots.Early PCI initialization:<ul style="list-style-type: none">-Enumerate PCI bus number-Assign memory & I/O resource-Search for a valid VGA device & VGA BIOS, and put it into C000:0.
24-26h	Reserved
27h	Initialize INT 09 buffer
28h	Reserved
29h	<ol style="list-style-type: none">Program CPU internal MTRR (P6 & PII) for 0-640K memory address.Initialize the APIC for Pentium class CPU.Program early chipset according to CMOS setup. Example: onboard IDE controller.Measure CPU speed.Invoke video BIOS.
2A-2Ch	Reserved
2Dh	<ol style="list-style-type: none">Initialize multi-languagePut information on screen display, including Award title, CPU type, CPU speed
2E-32h	Reserved
33h	Reset keyboard except Winbond 977 series Super I/O chips.
34-3Bh	Reserved
3Ch	Test 8254
3Dh	Reserved
3Eh	Test 8259 interrupt mask bits for channel 1.
3Fh	Reserved
40h	Test 8259 interrupt mask bits for channel 2.
41h	Reserved
42h	Reserved
43h	Test 8259 functionality.
44h	Reserved
45-46h	Reserved
47h	Initialize EISA slot

Appendix

48h	Reserved
49h	<ol style="list-style-type: none">1. Calculate total memory by testing the last double word of each 64K page.2. Program writes allocation for AMD K5 CPU.
4A-4Dh	Reserved
4Eh	<ol style="list-style-type: none">1. Program MTRR of M1 CPU2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable range.3. Initialize the APIC for P6 class CPU.4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical.
4Fh	Reserved
50h	Initialize USB
51h	Reserved
52h	Test all memory (clear all extended memory to 0)
53-54h	Reserved
55h	Display number of processors (multi-processor platform)
56h	Reserved
57h	<ol style="list-style-type: none">1. Display PnP logo2. Early ISA PnP initialization -Assign CSN to every ISA PnP device.
58h	Reserved
59h	Initialize the combined Trend Anti-Virus code.
5Ah	Reserved
5Bh	(Optional Feature) Show message for entering AWDFLASH. EXE from FDD (optional)
5Ch	Reserved
5Dh	<ol style="list-style-type: none">1. Initialize Init_Onboard_Super_IO switch.2. Initialize Init_Onboard_AUDIO switch.
5E-5Fh	Reserved
60h	Okay to enter Setup utility; i.e. not until this POST stage can users enter the CMOS setup utility.
61-64h	Reserved
65h	Initialize PS/2 Mouse

66h	Reserved
67h	Prepare memory size information for function call: INT 15h ax=E820h
68h	Reserved
69h	Turn on L2 cache
6Ah	Reserved
6Bh	Program chipset registers according to items described in Setup & Auto-configuration table.
6Ch	Reserved
6Dh	<ol style="list-style-type: none">1. Assign resources to all ISA PnP devices.2. Auto assign ports to onboard COM ports if the corresponding item in Setup is set to "AUTO".
6Eh	Reserved
6Fh	<ol style="list-style-type: none">1. Initialize floppy controller2. Set up floppy related fields in 40:hardware.
70-72h	Reserved
73h	(Optional Feature) Enter AWDFLASH.EXE if : -AWDFLASH is found in floppy drive. -ALT+F2 is pressed
74h	Reserved
75h	Detect & install all IDE devices: HDD, LS120, ZIP, CDROM.....
76h	Reserved
77h	Detect serial ports & parallel ports.
78h	Reserved
79h	Reserved
7Ah	Detect & install co-processor
7B-7Eh	Reserved
7Fh	<ol style="list-style-type: none">1. Switch back to text mode if full screen logo is supported. -If errors occur, report errors & wait for keys -If no errors occur or F1 key is pressed to continue:<ul style="list-style-type: none">◆ Clear EPA or customization logo.
80h	Reserved
81h	Reserved
82h	<ol style="list-style-type: none">1. Call chipset power management hook.2. Recover the text fond used by EPA logo (not for full

Appendix

	screen logo)
	3. If password is set, ask for password.
83h	Save all data in stack back to CMOS
84h	Initialize ISA PnP boot devices
85h	<ol style="list-style-type: none">1. USB final Initialization2. NET PC: Build SYSID structure3. Switch screen back to text mode4. Set up ACPI table at top of memory.5. Invoke ISA adapter ROMs6. Assign IRQs to PCI devices7. Initialize APM8. Clear noise of IRQs.
86-92h	Reserved
93h	Read HDD boot sector information for Trend Anti-Virus code
94h	<ol style="list-style-type: none">1. Enable L2 cache2. Program boot up speed3. Chipset final initialization.4. Power management final initialization5. Clear screen & display summary table6. Program K6 write allocation7. Program P6 class write combining
95h	<ol style="list-style-type: none">1. Program daylight saving2. Update keyboard LED & typematic rate
96h	<ol style="list-style-type: none">1. Build MP table2. Build & update ESCD3. Set CMOS century to 20h or 19h4. Load CMOS time into DOS timer tick5. Build MSIRQ routing table.
FFh	Boot attempt (INT 19h)