# MP042 User's Manual OPTi 486 PCI/ISA Motherboard

### WARNING

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To ensure the reliability of the Computer DO NOT reconfigure the board while the Power is ON. If you wish to reconfigure the board, make sure that the power to the system is OFF before changing any hardware setting such as Jumper setting or DIP switch.

CHECKLIST ======= Your 486 PCI/ISA Cache package contain the following: \* 486 PIC/ISA Cache Mother board. \* User's manual.

## ABOUT THIS MANUAL

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This manual is designed to provide the basic information necessary for the end user to understand and properly use of the 486 PCI/ISA Motherboard. The mother board ensure superlative performance and complete compatibility with software written for IBM PC/AT 80286 type, 80386 type and 80486 type of processor.

# TRADEMARKS

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#### MOTHERBOARD FEATURES

- \* OPTI chipset 82C895, 82C822 and 82C602
- \* Support 80486 SX/DX/DX2, P24C (DX4) SL-enhanced CPU P24T, P24D, AMD 486 DX/DX2 and Cyrix M7 Microprocessor
- \* Memory up to 128MB

* Optional 128KB or 512KB external cache * Three 16bit slots and Four PCI slots * Dimension: 19 x 22.5 cm with 4 Layers	
* Two clock source, supporting system running from 25 to 50 MHz, PCI clock running ASYNC or SYNC	
* Support System Memory Management (SMM) for Power Management * Support full SMI Interface * Power Management port for specific control during all modes of operatio	n
JUMPERS AND CONNECTORS REFERENCE	
Before installing the motherboard, make sure the jumpers setting are set your configuration. The Function of each jumpers are as follows:	for
CPU Type SelectorJP14, JP15, JP16, JP17, JP18, JP29 JP21, JP23, JP24, JP25, JP26SL-enhanced CPU SelectorJP6, JP10, JP18, JP28CPU VOLTAGE SelectorJP30CPU Clock SelectorJP31, JP32P24C (DX4) Clock selectorJP16PCI Clock SelectorJP33Cache OptionJP13, JP19, JP22Charge / Discharge CMOSJP1CRT Power DownJP38Speaker ConnectorJP40Turbo Switch ConnectorJP39Reset ConnectorJ2Keylock/Power LED ConnectorJ2Keyboard ConnectorJ1Power Supply ConnectorPL1Micellaneous JumpersJP4, JP5, JP27	
NOTE: RED Jumpers Represent Voltage Selector White Jumpers Represent CPU Type Yellow Jumpers Represent Clock Selector Blue Jumpers Represent Cache Option Black Jumpers Represent Others	

JUMPER SWITCHES

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These switches consist of two or three pins set in a plastic base. Two of the pins are covered by a cap which connect or short each other. It is

possible to change the switch setting by moving the cap between the first or last two pins.

JUMPERS OPEN & SHORT

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If you wish to change any of the jumper setting, make sure the board is disconnected from the power source. This is to protect the mother board from getting damage.

CPU Type	JP14	JP15	JP16	JP17	JP18	JP21	JP23	JP24	JP25	JP26	IP29
486SX	1-2	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	2-3	OPEN
486DX	1-2	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	1-2,3-4	OPEN
U5S	1-2	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	1-2,3-4	OPEN
M7	1-2	1-2	OPEN	OPEN	2-3	CLOSE	CLOSE	OPEN	OPEN	1-2,3-4	OPEN
P24C	1-2	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	1-2,3-4	OPEN
P24D	1-2	2-3	OPEN	1-2	OPEN	CLOSE	OPEN	1-2	CLOSE	1-2,3-4	OPEN
P24T	2-3	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	1-2,3-4	OPEN

- NOTE: If AMD DX2-66, DX2-80, DX4-100 CPU (3.0 VOLTS) is used note JP17 (1-2 for DX4-100, 2-3 for DX2-66, DX2-80) JP30 (3-5, 4-6 for 3.45 VOLTS)
- NOTE: If SL-enhanced CPU is used note JP6, JP10, JP18, JP28
- NOTE: IF CYRIX M7 CPU is used note JP29 CYRIX M7 CLOCK Selector (JP29 CLOSE is CLOCK x 2)
- NOTE: If P24C (DX4-100 MHz) CPU is used note JP16 P24C CLOCK x 3 (DX4-75 MHz CLOCK x 3) JP30 CPU VOLTAGE 3-5,4-6 3.45 VOLTS

WARNING: WROMG CPU VOLTAGE TO P24C WILL DAMAGE THE CPU.

CPU CLOCK Selector

CLK	JP31	JP32			
25 MHz	OPEN	OPEN			
33 MHz	CLOSE	CLOSE			
40 MHz	OPEN	CLOSE			
50 MHz	CLOSE	OPEN			

PCI CLOCK Selector JP33 1-2 ASYNC CLK JP33 2-3 SYNC CLK

IF CPU CLK = 50 MHz SET PCI CLK ASYNC IF CPU CLK < 50 MHz SET PCI CLK SYNC						
MICELLANEOUS JUMPER ====================================						
SL-ENHANCE CPU Selector SP6 JP10 JP28 JP18 CLOSE CLOSE CLOSE 1-2 FOR SL-ENHANCE CPU OPEN OPEN OPEN FOR OTHER CPU 2-3 FOR CYRIX CPU						
CPU VOLTAGE Selector						
JP30 1-3,2-4 5 VOLTS OTHER CPU 3-5,4-6 3.45 VOLTS P24C CPU						
P24C CLOCK Selector						
JP16 OPEN P24C x3 AND OTHER CPU 1-2 P24C x2 2-3 P24C x2.5						
P24D WRITE BACK / THROUGHT Selector						
JP17						
1-2 P24D WRITE BACK 2-3 P24D WRITE THROUGHT						
CACHE OPTION						
SRAM         TAG RAM           SIZE         BANK         U17         JP13         JP19         JP22           128KB         32K*8         8K*8         2-3         OPEN         OPEN           512KB         128K*8         32K*8         1-2         CLOSE         CLOSE						
BANK U3, U8, U9, U10						

Charge/Discharge CMOS (JP1)

CMOS Charge/Discharge CMOS is used to Charge and Discharge CMOS. If you discharge the CMOS all datas in the CMOS will be erase. To discharge CMOS, place a jumper cap in pin 1 & 2 then turn on the Power supply for 1 to 2 seconds then turn off the power supply. In this procedure the CMOS is discharge, all the datas in the CMOS is erase.Place the jumper cap in pin 2 & 3 for CMOS charge, then run setup.

- JP1 1-2 DISCHARGE CMOS
- JP1 2-3 CHARGE CMOS

Keyboard Connector J1

SMI LED JP38

Keylock/Power LED J9

Turbo Switch JP40

OPEN NORNAL SPEED CLOSE TURBO SPEED

Speaker Connector JP41

Reset Connector S1

MICELLANEOUS JUMPER JP4, JP5

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JP4 1-2,5-4	FOR EDGE TRIGGER
2-3	FOR LEVEL TRIGGER
	FOD EDGE TDICCED

JP5 1-2,3-4 FOR EDGE TRIGGER 2-3 FOR LEVEL TRIGGER

MEMORY CONFIGURATION

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The system board Memory can be expanded from 2MB to 128MB. Memory can be installed by using 512K \* 9/36, 1M \* 9/36, 2M \* 36, 4M \* 36 and 16M \* 36 SIMM RAM Module.

MEMORY SIZE 2M	SIMM1 512K * 36	SIMM2
4M	1M * 36 512K * 36	512K * 36
8M	1M * 36 2M * 36	1M * 36
12M	1M * 36	2M * 36
16M	4M * 36 2M * 36	2M * 36
20M	1M * 36	4M * 36
32M	4M * 36	4M * 36
64M	16M * 36	
128M	16M * 36	16M * 36

## AWARD BIOS SETUP

Award BIOS support plug and play function and has a built-in setup program that allows the users to modify the basic system configuration. This type of information is stored in CMOS so that it retains the setup information when the power is turned off.

- 1. Turn on or reboot the system. when the below message appears at the bottom of the screen during the POST ( Power On Self Test ) press
- DEL

key to enter setup.

- 2. Press the DEL key to enter the Award BIOS program and the main menu will appear on the screen. The main menu allows you to select from
- ten

setup functions and two exit choices.

3. For the safe, please select "LOAD SETUP DEFAULTS" for BIOS setup.

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