SUPER®

SUPER P4SGA
SUPER P4SGL
SUPER P4SGR
SUPER P4SGE

USER'S MANUAL

Revision 1.1c

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Preface

About This Manual

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the SUPER P4SGA/P4SGL/P4SGR/P4SGE motherboard. The P4SGA/P4SGL/P4SGR/P4SGE supports single Intel Pentium® 4 1.5 - 3.06 GHz processors at a system bus speed of 533/400 MHz (3.0 GHz and 400 MHz only for P4SGL).   Refer to the support section of our web site (http://www.supermicro.com/TechSupport.htm) for a listing of supported processors. Single 478-pin Pentium 4 processors are housed in a 478-pin microPGA (Plastic Grid Array) package. This product is intended to be professionally installed.

Manual Organization

Chapter 1 includes a checklist of what should be included in your mainboard box, describes the features, specifications and performance of the P4SGA/P4SGL/P4SGR/P4SGE mainboard and provides detailed information about the chipset.

Chapter 2 begins with instructions on handling static-sensitive devices. Read this chapter when installing the processor and DDR memory modules and when mounting the mainboard in the chassis. Also refer to this chapter to connect the floppy and hard disk drives, the IDE interfaces, the parallel and serial ports, the mouse and keyboard and the twisted wires for the power supply, the reset button, the keylock/power LED and the speaker.

If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for the video, the memory and the setup configuration stored in CMOS. For quick reference, a general FAQ (Frequently Asked Questions) section is provided. Instructions are also included for contacting technical support. In addition, you can visit our web site at www.supermicro.com/techsupport.htm for more detailed information.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the CMOS Setup utility.

Appendix A provides AwardBIOS POST Codes.
Appendix B lists AwardBIOS POST Messages.
Appendix C lists AwardBIOS Error Beep Codes.

Table of Contents

Preface

Chapter 1: Introduction

Chapter 2: Installation

Chapter 3: Troubleshooting

Chapter 4: AwardBIOS

Appendices:

Chapter 1
Introduction

1-1   Overview

Checklist

Congratulations on purchasing your computer motherboard from an ac-
knowledged leader in the industry. Supermicro boards are designed with
the utmost attention to detail to provide you with the highest standards in
quality and performance.

Please check that the following items have all been included with your
motherboard. If anything listed here is damaged or missing, contact your
retailer.

All Included with Retail Box Only

One (1) Supermicro Mainboard

One (1) ATA66/100 ribbon cable for IDE devices

One (1) floppy drive ribbon cable

One (1) serial port (COM) cable

One (1) CPU retention bracket

One (1) USB 4-port cable (retail only)

One (1) I/O shield

One (1) Supermicro CD or diskettes containing drivers and utilities

One (1) User's/BIOS Manual

SCSI Accessories (P4SGR only)

One (1) 68-pin LVD SCSI cable

One (1) set of SCSI driver diskettes

One (1) SCSI manual

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Notes

SUPER P4SGA/P4SGL

Figure 1-1. SUPER P4SGA/P4SGL Image*
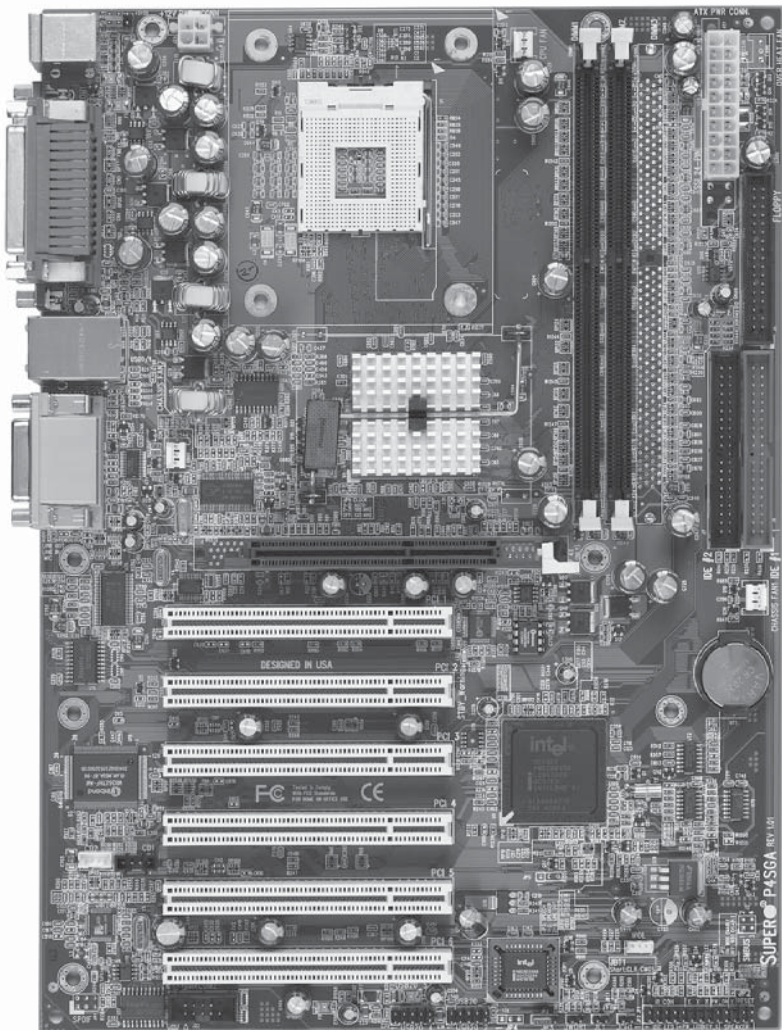
* The P4SGL has the same layout but no 4xAGP slot.

SUPER P4SGR/P4SGE

Figure 1-2. SUPER P4SGR/P4SGE Image*
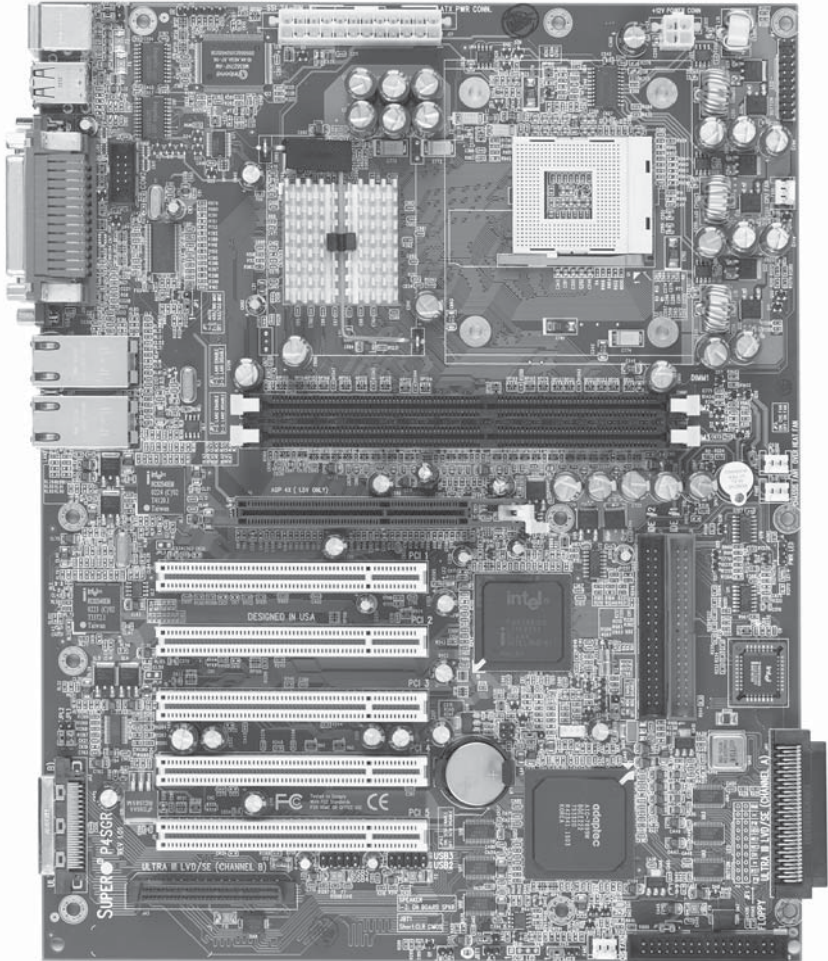
* The P4SGE has the same layout but no onboard SCSI.

Figure 1-3. Motherboard (P4SGA//P4SGL) Layout*
(not drawn to scale)
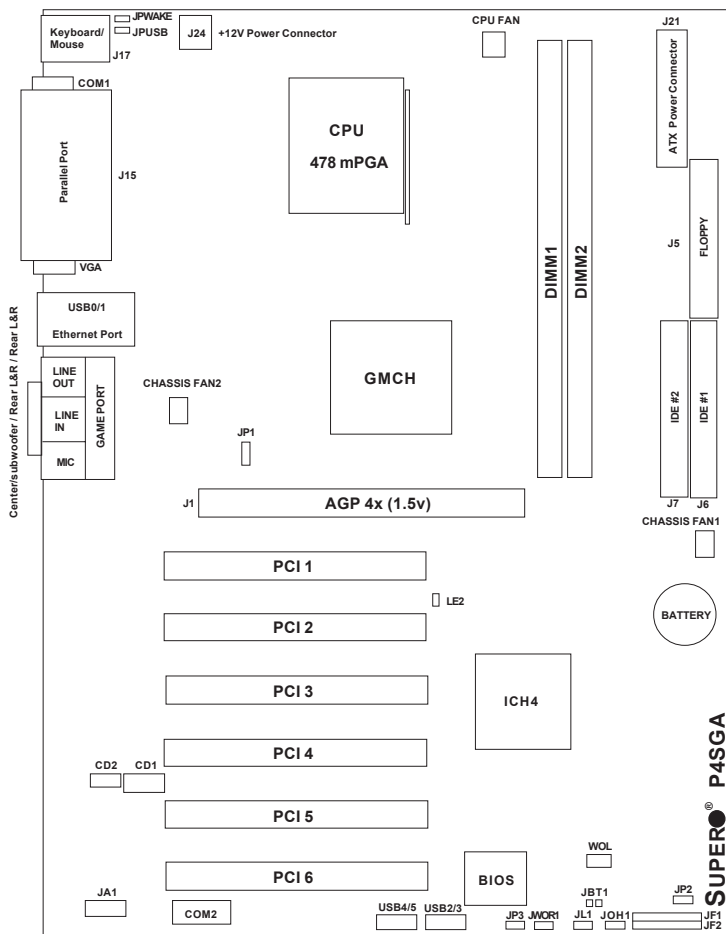
*The P4SGL has the same layout but no 4xAGP slot.

*The P4SGA+ has the same layout - see page 1-11 for differences.

See Chapter 2 for detailed information on jumpers, I/O ports and JF1/JF2 Front Panel connections.

Jumpers not indicated are for test purposes only.

P4SGA/P4SGL Quick Reference

Jumpers	Description	Default Setting
JBT1	CMOS Clear	See Section 2-7
JP1	Front Side Bus Speed	Pins 1-2 (Auto)
JP2	Watch Dog Enable	Open (Disabled)
JP3	Speech Output Select	Pins 1-2 (Speaker)
JPUSB	USB0/1 Wake Up	Pins 1-2 (Disabled)
JPWAKE	Keyboard Wake-Up	Pins 1-2 (Disabled)

Connectors	Description
CD1	Audio CD Input (large connector)
CD2	Audio CD Input (small connector)
Chassis Fan1/2	Chassis Fan Header
COM1/COM2	COM1/COM2 Serial Port Connector/Header
CPU Fan	CPU Fan Header
DIMM1/2	Memory (DIMM) Slots
Ethernet	LAN (Ethernet Port)
Game Port	Game Port
J1*	4xAGP
J5	Floppy Disk Drive Connector
J6/J7	IDE #1/#2 Hard Disk Drive Connectors
J15	Parallel Printer Port
J17	PS/2 Keyboard/Mouse
J21	ATX 12V Power Connector (20-pin)
J24**	ATX 12V Power Connector (4pin)
JA1	SPDIF Connector
JF1, JF2	Front Control Panel
JL1	Chassis Intrusion Header
JOH 1	Overheat LED
JWOR1	Wake-On-Ring Header
LE2	5v Standby Warning LED
LINE IN	Audio In Connector
LINE OUT	Audio Out (Speaker) Connector
MIC	Microphone Input
USB2/USB3	Universal Serial Bus Port 2/3
USB4/USB5	Universal Serial Bus Port 4/5
WOL	Wake-On-LAN

* P4SGA only

** The 4-pin connector at J24 must be connected to meet the safety requirements of the ATX 12V specifications.

Figure 1-4. Motherboard (P4SGR/P4SGE) Layout*
(not drawn to scale)
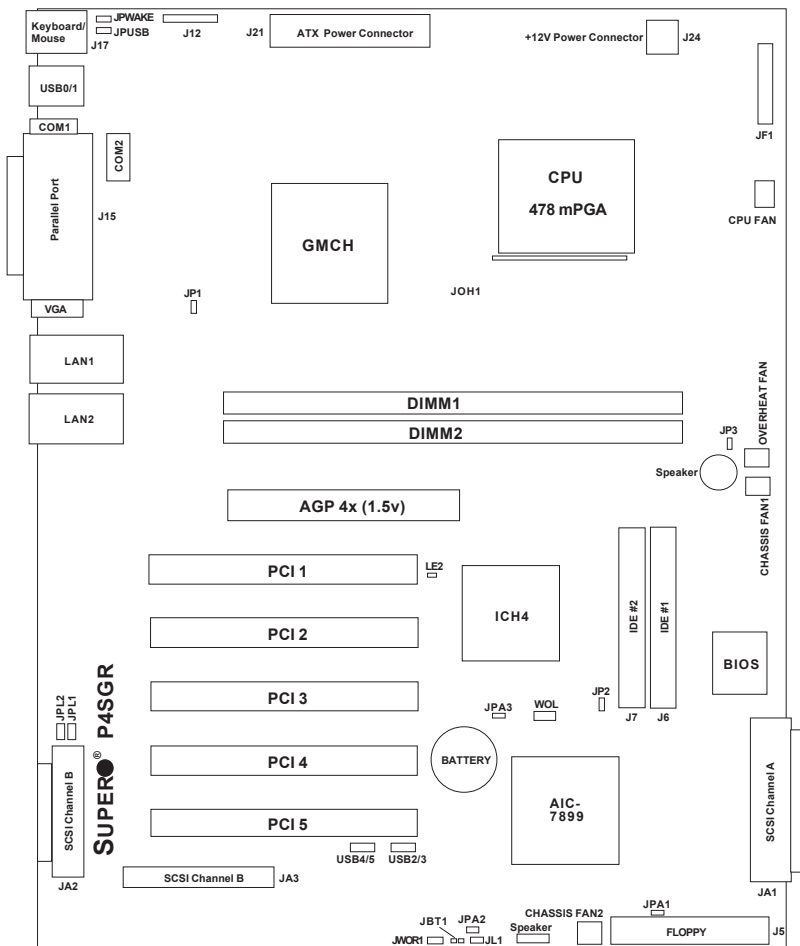
*The P4SGE has the same layout but no onboard SCSI.

See Chapter 2 for detailed information on jumpers, I/O ports and JF1 Front Panel connections.

Jumpers not indicated are for test purposes only.

P4SGR/P4SGE Quick Reference

Jumpers	Description	Default Setting
JBT1	CMOS Clear	See Section 2-7
JP1	Front Side Bus Speed	Pins 1-2 (Auto)
JP2	Watch Dog Enable	Open (Disabled)
JP3	Fan Select	Open (OH Fan)
JPA1*	SCSI CH A Termination	Open (Enabled)
JPA2*	SCSI CH B Termination	Open (Enabled)
JPA3*	SCSI Enable/Disable	Pins 1-2 (Enabled)
JPL1/JPL2	LAN1/LAN2 En/Dis	Pins 1-2 (Enabled)
JPUSB	USB0/1 Wake Up	Pins 1-2 (Disabled)
JPWAKE	Keyboard Wake-Up	Pins 1-2 (Disabled)

Connectors	Description
Chassis Fan1/2	Chassis Fan Header
COM1/COM2	COM1/COM2 Serial Port Connector/Header
CPU Fan	CPU Fan Header
DIMM1/2	Memory (DIMM) Slots
J1	4xAGP
J5	Floppy Disk Drive Connector
J6/J7	IDE #1/#2 Hard Disk Drive Connectors
J12	Infrared Header
J15	Parallel Printer Port
J17	PS/2 Keyboard/Mouse
J21	ATX 12V Power Connector (20-pin)
J24**	ATX 12V Power Connector (4pin)
JA1/JA2/JA3*	Ultra160 SCSI Channel A/B/B
JF1	Front Control Panel
JL1	Chassis Intrusion Header
JOH 1	Overheat LED
JWOR1	Wake-On-Ring Header
LAN1/LAN2	Gb Ethernet Ports
LE2	5v Standby Warning LED
Overheat Fan	Overheat (Thermal) Fan
USB0/1	Universal Serial Bus Port 0/1
USB2/3/4/5	Universal Serial Bus Header 2/3/4/5
WOL	Wake-On-LAN

* P4SGR only

** The 4-pin connector at J24 must be connected to meet the safety requirements of the ATX 12V specifications.
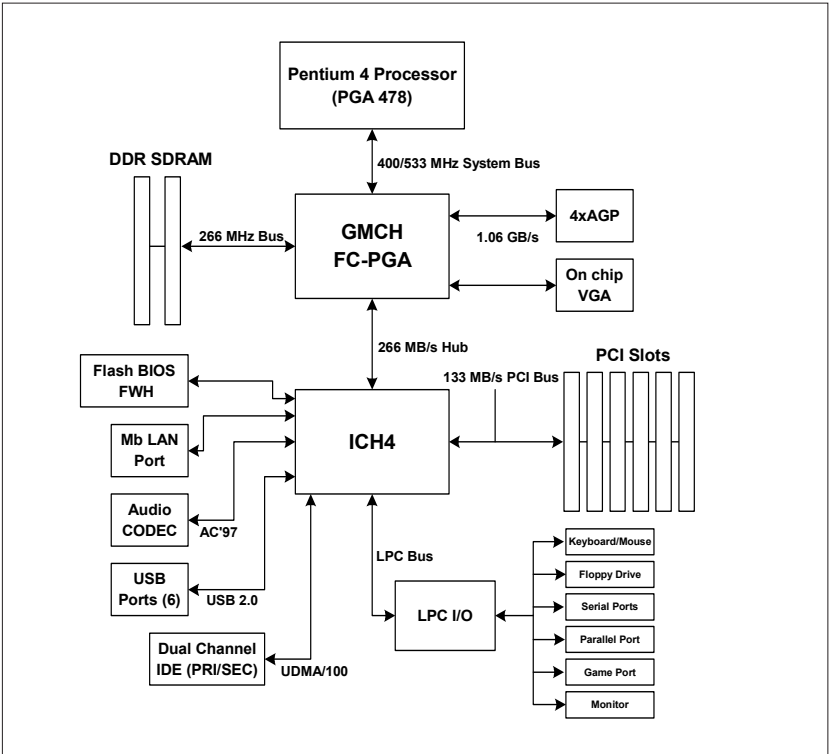
Figure 1-5. 845G* Chipset:
System Block Diagram

Note: This is a general block diagram and may not exactly represent the features on your motherboard. See the following pages for the actual specifications of each motherboard.

*The 845GL (for the P4SGL) is the same but does not include support for AGP and supports a 400 MHz System Bus only.

*The 845GE (for the P4SGR, P4SGE and P4SGA+) is the same but supports PC2100 and PC2700 (DDR-266/DDR-333) only.

Motherboard Features

CPU ◆ *Latest CPU technology!*
- Single Pentium® 4 478-pin mPGA 1.5 - 3.06 GHz processors at a 533/ 400 MHz system bus speed. P4SGL: 400 MHz system bus only. (P4SGA+: same as P4SGA but also supports hyper-threading.)

Memory ◆ *Latest memory technology!*
- Two 184-pin DIMM slots for up to 2 GB of non-ECC unbuffered PC2100/1600 (DDR-266/200) SDRAM (PC2700/2100 for P4SGR/P4SGE/ P4SGA+)

  Note: See Section 2-4 for details.

Chipset ◆ *Latest Intel chipset technology!*
- Intel 845G Chipset - P4SGA
- Intel 845GL Chipset - P4SGL
- Intel 845GE Chipset - P4SGR/P4SGE/P4SGA+

Expansion Slots
- Six 32-bit, 33 MHz PCI slots (P4SGA/P4SGL/P4SGA+)
- Five 32-bit, 33 MHz PCI slots (P4SGR/P4SGE)
- One 1.5V 4xAGP slot (P4SGA/P4SGR/P4SGE)

BIOS ◆ *With the latest voice BIOS technology for POST messaging!**
- 4 Mb Firmware Hub AwardBIOS® Flash BIOS
- APM 1.2, DMI 2.3, PCI 2.2, ACPI 1.0, Plug and Play (PnP)

PC Health Monitoring
- Nine onboard voltage monitors for CPU core, chipset voltage, +3.3V, +3.3V standby, +5V, +5V standby, Vbat (battery voltage) and ±12V
- Fan status monitor with firmware/software on/off control
- SuperDoctor II Utility
- Environmental temperature monitoring and control
- Power-up mode control for recovery from AC power loss
- System overheat LED and control
- System resource alert
- Hardware BIOS virus protection
- Auto-switching voltage regulator for the CPU core

*P4SGA/P4SGL/P4SGA+ only

ACPI Features

- Microsoft OnNow
- Slow blinking LED for suspend state indicator
- BIOS support for USB keyboard
- Real-time clock wake-up alarm
- Main switch override mechanism
- Internal/external modem ring-on

Onboard I/O

- Dual Ultra DMA 100 IDE Bus Masters
- Adaptec AIC-7899W for dual channel Ultra160 SCSI (P4SGR only)
- 1 floppy port interface (up to 2.88 MB)
- 2 Fast UART 16550A compatible serial ports
- 1 10/100 Mb LAN Ethernet port (works with integrated MAC - P4SGA/ P4SGL/P4SGA+)
- Intel 82540EM Gb Ethernet Controller for dual Gb ports (P4SGR/P4SGE)
- 1 EPP (Enhanced Parallel Port) and ECP (Extended Capabilities Port) supported parallel port
- PS/2 mouse and PS/2 keyboard ports
- Integrated high-perfomance "on-chip" Intel Extreme Graphics
- 1 game port with audio (P4SGA/P4SGL/P4SGA+) ◆ *Latest onboard 6-channel sound technology!*
- Up to 6 USB (Universal Serial Bus) ports ◆ *Latest USB 2.0 technology!*
- 1 infrared port

Other

- Wake-on-LAN (WOL)
- Multiple CPU clock frequency ratio selections (set in BIOS)
- AC97' CODEC
- Suspend-to-RAM
- Onboard +5vsb warning LED ("LE2")

CD Utilities

- BIOS flash upgrade utility
- Drivers and software for 845GE/845G/845GL chipset utilities

Dimensions

- P4SGA/P4SGL/P4SGA+: ATX form factor, 12" x 8.7" (305 x 221 mm)
- P4SGR/P4SGE: ATX form factor, 12" x 9.5" (305 x 241 mm)

1-2   Chipset Overview

Intel's 845G chipset (P4SGA) is made up of two primary components: the Graphics Memory Controller Hub (GMCH) and the I/O Controller Hub (ICH4). The 845GL (P4SGL) and 845GE (P4SGR/P4SGE) chipsets also include these components, however the 845GL provides no AGP support and only a 400 MHz system bus speed and the 845GE supports faster memory.

Graphics Memory Controller Hub (GMCH)

The GMCH includes the host (CPU) interface, memory interface, ICH4 interface and 4xAGP interface for the 845G/GL/GE chipset. It contains advanced power management logic and supports a single channel of DDR SDRAM. The AGP 2.0 interface supports 4x data transfers and operates at a peak bandwidth of 1.6 GB/s.

An external graphics accelerator card is not required due to the integrated graphics in the 845G/GL/GE chipset. (If the system BIOS detects an external AGP device, it will disable the integrated graphics.) The integrated graphics controller delivers 3D, 2D and video capabilities, including video conferencing applications. The controller does not utlilize local memory, but accesses graphics data located in system memory at speeds matching that of the SDRAM installed. It also includes a cache controller to avoid frequent memory fetches of recently accessed texture data.

I/O Controller Hub (ICH4)

The ICH4 is a fourth-generation I/O Controller Hub subsystem that integrates many of the input/output functions of the chipset, including a two-channel ATA100 Bus Master IDE controller. The ICH4 also interfaces with the PCI cards, the AC'97 Audio CODEC and the various communications ports. Nearly all communications between the GMCH and the ICH4 takes place over the hub Interface, which is a 66 MHz/266 MB/s bus.

Recovery from AC Power Loss

BIOS provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must hit the power switch to turn it back on) or for it to automatically return to a power on state. See the Power Lost Control setting in the BIOS chapter of this manual to change this setting. The default setting is Always OFF.

1-3   PC Health Monitoring

This section describes the PC health monitoring features of the SUPER P4SGA/P4SGL/P4SGR/P4SGE. The motherboard has an onboard System Hardware Monitor chip that supports PC health monitoring.

Nine Onboard Voltage Monitors for the CPU Core, Chipset Voltage, +3.3V, +3.3V standby, +5V, +5V standby, Vbat and ±12V

The onboard voltage monitor will scan these voltages continuously. Once a voltage becomes unstable, it will give a warning or send an error message to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware/Software On/Off Control

The PC health monitor can check the RPM status of the cooling fans. The onboard 3-pin fans are controlled by the power management functions.

Environmental Temperature Control

The P4SGA/P4SGL/P4SGR/P4SGE has a CPU "thermal trip" feature. A thermal control sensor monitors the CPU temperature in real time and will send a signal to shut down the system whenever the CPU temperature exceeds a certain threshold. This works to protect the CPU from being damaged by overheating.

CPU Overheat LED and Control

This feature is available when the user enables the CPU overheat warning function in the BIOS. This allows the user to define an overheat temperature. When this temperature is exceeded, the CPU thermal trip feature will activate and shut down the system.

Hardware BIOS Virus Protection

The system BIOS is protected by hardware so that no virus can infect the BIOS area. The user can only change the BIOS content through the flash utility provided by Supermicro. This feature can prevent viruses from infecting the BIOS area and destroying valuable data.

Auto-Switching Voltage Regulator for the CPU Core

The 3-phase-switching voltage regulator for the CPU core can support up to 60A current and auto-sense voltage IDs ranging from 1.1V to 1.85V. This will allow the regulator to run cooler and thus make the system more stable.

1-4   Power Configuration Settings

This section describes features of your motherboard that deal with power and power settings.

Microsoft OnNow

The OnNow design initiative is a comprehensive, system-wide approach to system and device power control. OnNow is a term for a PC that is always on but appears to be off and responds immediately to user or other requests.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will wake-up and the LED will automatically stop blinking and remain on.

BIOS Support for USB Keyboard

If the USB keyboard is the only keyboard in the system, it keyboard will function like a normal keyboard during system boot-up.

Real Time Clock Wake-Up Alarm

Although the PC may be perceived to be off when not in use, it is still capable of responding to preset wake-up events. In the BIOS, the user can set a timer to wake-up the system at a predetermined time.

Main Switch Override Mechanism

When an ATX power supply is used, the power button can function as a system suspend button. When the user depresses the power button, the system will enter a SoftOff state. The monitor will be suspended and the

hard drive will spin down. Depressing the power button again will cause the whole system to wake-up. During the SoftOff state, the ATX power supply provides power to keep the required circuitry in the system alive. In case the system malfunctions and you want to turn off the power, just depress and hold the power button for 4 seconds. The power will turn off and no power will be provided to the motherboard.

Wake-On-Ring Header

Wake-up events can be triggered by a device such as the external modem ringing when the system is in the SoftOff state. Note that external modem ring-on can only be used with an ATX 2.01 (or above) compliant power supply.

Wake-On-LAN (WOL)

Wake-On-LAN is defined as the ability of a management application to re-motely power up a computer that is powered off. Remote PC setup, up-dates and asset tracking can occur after hours and on weekends so that daily LAN traffic is kept to a minimum and users are not interrupted. The motherboards have a 3-pin header (WOL) to connect to the 3-pin header on a Network Interface Card (NIC) that has WOL capability. Wake-On-LAN must be enabled in BIOS. Note that Wake-On-Lan can only be used with an ATX 2.01 (or above) compliant power supply.

1-5   Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates of 1 GHz and faster.

The SUPER P4SGA/P4SGL/P4SGR/P4SGE accommodates 12V ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. You power supply must supply 1 amp of 5V standby voltage.

It is strongly recommended that you use a high quality power supply that meets 12V ATX power supply Specification 1.1 or above. Additionally, in areas where noisy power transmission is present, you may choose to in-stall a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

It also provides two high-speed, 16550 compatible serial communication ports (UARTs), one of which supports serial infrared communication. Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt sys-tem. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Con-figuration and Power Interface), which includes support of legacy and ACPI power management through a SMI or SCI function pin. It also features auto power management to reduce power consumption.

The IRQs, DMAs and I/O space resources of the Super I/O can be flexibly adjusted to meet ISA PnP requirements, which suppport ACPI and APM (Ad-vanced Power Management).

1-6   Super I/O

The disk drive adapter functions of the Super I/O chip include a floppy disk drive controller that is compatible with industry standard 82077/765, a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic.   The wide range of functions integrated onto the Super I/O greatly reduces the number of components required for interfacing with floppy disk drives.   The Super I/O supports four 360 K, 720 K, 1.2 M, 1.44 M or 2.88 M disk drives and data transfer rates of 250 Kb/s, 500 Kb/s or 1 Mb/s.

Chapter 2
Installation

2-1 Static-Sensitive Devices

Electric Static Discharge (ESD) can damage electronic components. To prevent damage to your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the anti-static bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

Installation Procedures

Follow the procedures as listed below for full installation of the motherboard into a chassis:

1. Install the processor and the heatsink to the motherboard.

2. Install the motherboard in the chassis.

3. Install the memory and add-on cards.

4. Finally, connect the cables and install the drivers.

2-2   Processor and Heatsink Fan Installation

When handling the processor package, avoid placing direct pressure on the label area of the fan.

IMPORTANT: Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.

Installation of the Processor and Heatsink

1. Locate the following components, which are included in the shipping package.

Clips (2)

Bracket (1)

Note: The CPU heatsink fan with clips is included only with the retail versions of the P4SGA/P4SGL/P4SGR/P4SGE. If you buy a boxed Intel Pentium 4 478/Northwood processor, it should include a heatsink, fan and retention mechanism. If you buy a processor separately, use only a Supermicro or Intel certified heatsink and fan.

2. Insert the white pegs into the black anchors. Do not force the white pegs all the way in - about 1/3 of the white pegs should be inside the black anchors. (These are for chassis that do not have four CPU retention holes.)

Bracket in position

3. Place a retention bracket in the proper position and secure it by pressing two pegs into the retention holes until you hear a *click*. The clicking sound indicates that the peg is locked and secured.
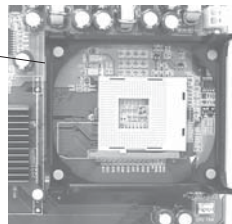
4. Secure the other retention bracket into position by repeating Step 3.

5. Lift the lever on the CPU socket.
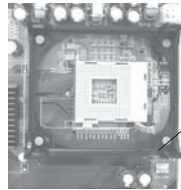
Socket Lever
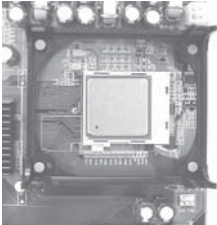
6. Install the CPU in the socket. Make sure that Pin 1 of the CPU is seated on Pin 1 of the socket (both corners are marked with a triangle).

7. Press the lever down until you hear it *click* into the locked position.
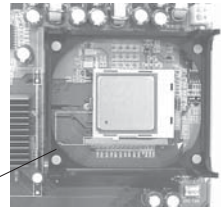
Socket lever in locked position

8. Apply the proper amount of thermal compound to the CPU die.

9. Place the heatsink on top of the CPU and press firmly downward - do not twist or slide the heatsink to seat thermal compound.

10. Secure the heat sink by locking the retention clips into their proper position.
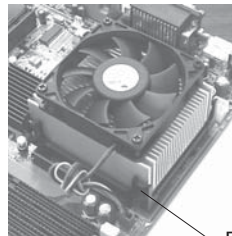
Retainer clip attachment point

11. Connect the CPU fan cable to the CPU Fan header on the motherboard.
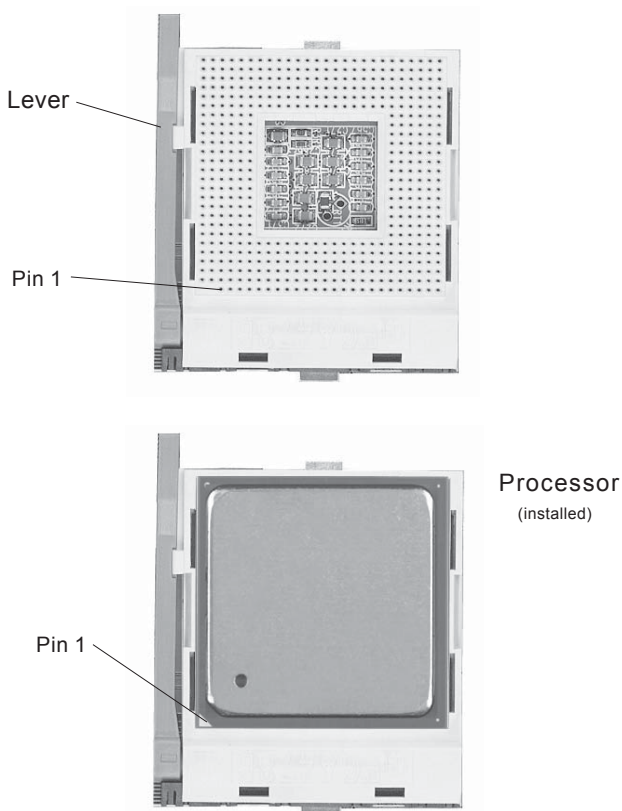
Figure 2-1. 478-pin mPGA Socket: Empty and with Processor Installed

2-3   Mounting the Motherboard in the Chassis

All motherboards have standard mounting holes to fit different types of chassis. Make sure the location of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

2-4   Installing DDR Memory

CAUTION
Exercise extreme care when installing or removing memory modules to prevent any possible damage.

Memory Module Installation (See Figure 2-2)

1.   Insert each DDR memory module vertically into its slot. Pay attention to the notch along the bottom of the module to prevent inserting the module incorrectly.   (See support information below.)
2.   Gently press down on the memory module until it snaps into place.
3.   Two 184-pin memory sockets support up to 2 GB unbuffered non-ECC DDR type SDRAM (see support information below).

Support
The P4SGA/P4SGL supports both double-sided and single-sided non-registered, unbuffered PC2100 (DDR-266) or PC1600 (DDR-200) SDRAM. The P4SGR/P4SGE/P4SGA+ supports both double-sided and single-sided non-registered, unbuffered PC2700 (DDR-333) or PC2100 (DDR-266) SDRAM.   Always populate DIMM1 first.

Figure 2-2.   Side and Top Views of DDR Module Installation into Slot

To Install:
Insert module vertically and press down until it snaps into place. Pay attention to the notch.

Notch   DDR   Notch

Release Tab   Note: Notch should align with the receptive point on the slot   Release Tab
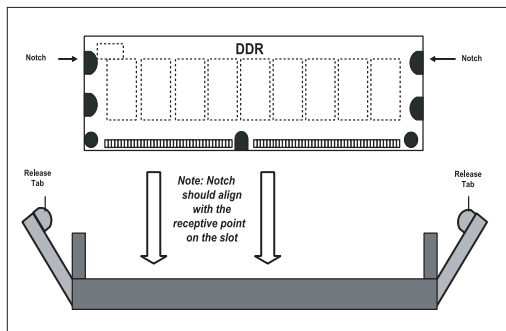
To Remove:
Use your thumbs gently to push each release tab outward to release the DIMM from the slot.

Top View of DDR Slot

Release Tab   Release Tab
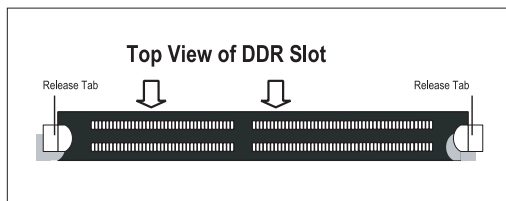
2-5   I/O Port/Control Panel Connector Locations

The I/O ports are color coded in conformance with the PC99 specification to make setting up your system easier. See Figure 2-3 below for the colors and locations of the various IO ports.

Figure 2-3.   I/O Port Locations and Definitions

P4SGA/P4SGL

Mouse (Green)   Parallel Port (Burgundy)   Game Port (Gold)

LAN Port

Keyboard   COM1 Port (Turquoise)   VGA Port (Blue) USB 0/1 Ports   Line Out / Line In / Mic
(Purple)   (Lime) / (Light Blue) / (Pink)

P4SGR/P4SGE

Mouse (Green)   Parallel Port (Burgundy)
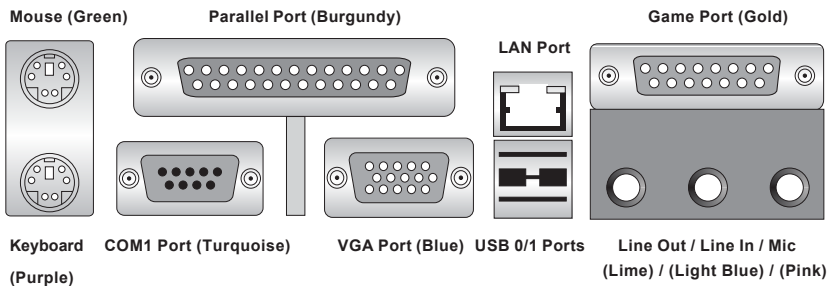
Keyboard   USB 0/1 Ports COM1 Port (Turquoise)   VGA Port (Blue)   LAN1   LAN2
(Purple)
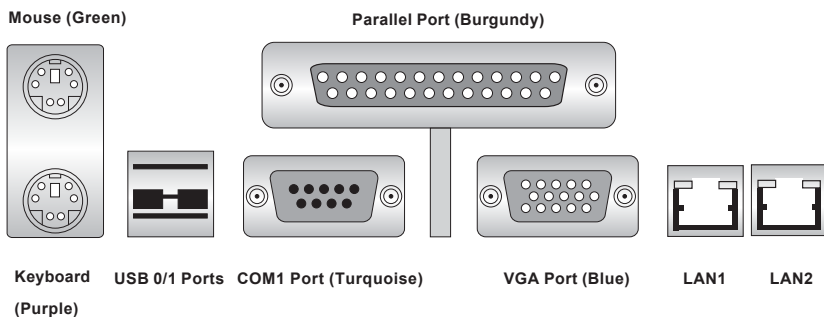
Front Control Panel

JF1/JF2 (P4SGA/P4SGL) and JF1 (P4SGR/P4SGE) contain header pins for various front control panel connectors. See Figure 2-4 for the pin definitions of the various connectors included speaker. Refer to Section 2-6 for details.

Figure 2-4. Front Control Panel Connectors

P4SGA/P4SGL

JF1 JF2

P4SGR/P4SGE

JF1
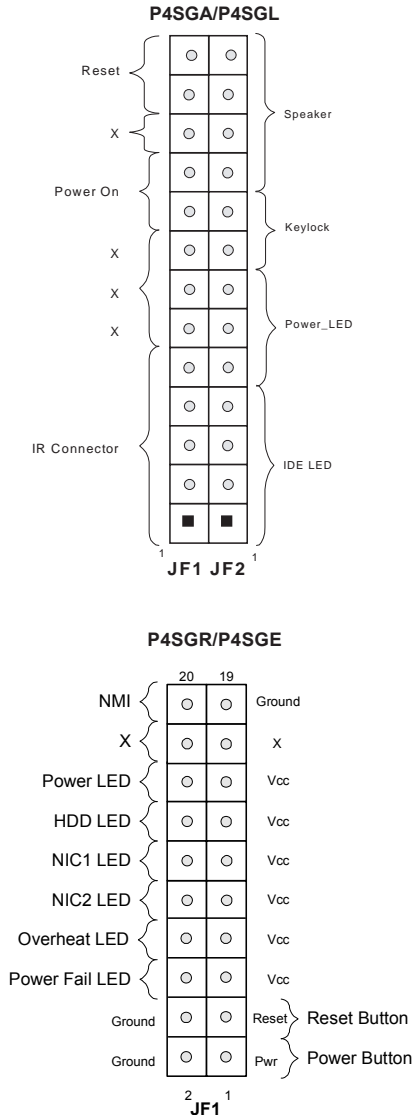
2-6   Connecting Cables

Power Supply Connectors

The primary power supply connec-
tor on the P4SGA/P4SGL/P4SGR/
P4SGE meets the SSI (Superset
ATX) 20-pin specification. Refer
to the table on the right for the pin
definitions of the ATX 20-pin
power connector. You must also
connect the 4-pin J24 power con-
nector to your power supply. Re-
fer to the table below right for the
J24 (12V) connector.

ATX 20-pin Power Connector
Pin Definitions (J21)

Pin #	Definition	Pin #	Definition
11	+3.3V	1	+3.3V
12	-12V	2	+3.3V
13	COM	3	COM
14	PS_ON	4	+5V
15	COM	5	COM
16	COM	6	+5V
17	COM	7	COM
18	-5V	8	PW-OK
19	+5V	9	5VSB
20	+5V	10	+12V

+12V 4-pin PWR
Connector
(J24)

Pins #	Definition
1 & 2	Ground
3 & 4	+12 V

Required

Connection

PWR Supply
Color Definition

Color	Definition
Orange	+3.3V
Black	Com
Red	5V
White	Power OK
Yellow	+12V
Purple	5V standby
Brown	-5V
(For reference only)	

IR Connector

The infrared connector is located
on pins 1-5 of JF1 on the P4SGA/
P4SGL and on J12 on the P4SGR/
P4SGE. See the tables on the right
for pin definitions. Refer to the
Technical Support section of our
web page for information on the
infrared devices you can connect
to the system.

Infrared
Pin Definitions
(JF1)

Pin Number	Definition
1	+5V
2	CIR
3	IRRX
4	Ground
5	IRTX

Infrared
Pin Definitions
(J12)

Pin Number	Definition
1	+5V
2	CIR
3	IRRX
4	Ground
5	IRTX
6	NC

PW_ON Connector

The PW_ON connector is located on pins 9 and 10 of JF1 on the P4SGA/P4SGL (1 and 2 on the P4SGR/P4SGE). This header should be connected to the chassis power button, which you may also configure to put the system into suspend mode (see the Power Button Mode setting in BIOS). To turn off the power when the suspend mode is enabled, depress the power button for at least 4 seconds. See the table on the right for pin definitions.

PW_ON Pin Definitions (JF1)

Pin Number	Definition
9	PW_ON
10	Ground

Reset Connector

The reset connector is located on pins 12 and 13 of JF1 on the P4SGA/P4SGL (3 and 4 on the P4SGR/P4SGE). This connector attaches to the reset switch on the computer chassis. See the table on the right for pin definitions.

Reset Pin Definitions (JF1)

Pin Number	Definition
12	Ground
13	Reset

IDE LED

The IDE LED is located on pins 1 to 4 of JF2 on the P4SGA/P4SGL (13 and 14 of JF1 on the P4SGR/P4SGE). This connects to the hard drive LED to display hard drive activity. See the table on the right for pin definitions.

IDE LED Pin Definitions (JF2)

Pin Number	Definition
1	+5V
2	HD Active
3	HD Active
4	+5V

Power_LED Connector

The Power LED connector is located on pins 5-7 of JF2 on the P4SGA/P4SGL (15 and 16 of JF1 on the P4SGR/P4SGE). This connection is used to provide LED indication of power being supplied to the system. See the table on the right for pin definitions.

Power_LED Pin Definitions (JF2)

Pin Number	Definition
5	+5V
6	Key
7	Control

Keylock Connector

The keylock connector is located on pins 8 and 9 of JF2 on the P4SGA/P4SGL. See the table on the right for pin definitions.

Keylock Pin Definitions (JF2)

Pin Number	Definition
8	+5V
9	Control

Speaker Connector

The speaker connector is located on pins 10 to 13 of JF2 on the P4SGA/P4SGL. See the table on the right for pin definitions.

Speaker Pin Definitions (JF2)

Pin Number	Function	Definition
10	+	Red wire, Speaker data
11	Key	No connection
12	Key	No connection
13	-	Speaker data

Serial Ports

Two serial ports are included on the motherboard: COM1 is a port located beside the mouse/keyboard ports and COM2 is a header located on the motherboard near PCI slot 6. See the table on the right for pin definitions.

Serial Port Pin Definitions (COM1, COM2)

Pin Number	Definition	Pin Number	Definition
1	CD	6	DSR
2	RD	7	RTS
3	TD	8	CTS
4	DTR	9	RI
5	Ground	10	NC

Note: Pin 10 is included on the header but not on the port.

*NC indicates no connection.

CD Header (P4SGA/SGL)

There are two 4-pin CD headers of different sizes on the motherboard. These allow you to use the onboard sound for audio CD playback. Connect the audio cable from your CD drive to the header that fits your cable's connector. Only one CD header can be use at any one time. See the tables on the right for pin definitions.

Audio CD Header Pin Definitions (CD1)

Pin Number	Definition
1	Left Stereo Signal
2	Ground
3	Ground
4	Right Stereo Signal

Audio CD Header Pin Definitions (CD2)

Pin Number	Definition
1	Right Stereo Signal
2	Ground
3	Left Stereo Signal
4	Ground

Fan Headers

There are three fan headers on the P4SGA/P4SGL, which are designated CPU Fan, Chassis Fan 1 and Chassis Fan 2. The P4SGR/P4SGE also has an Overheat Fan. Connect the fan on your CPU heatsink to the CPU Fan header. See the table on the right for pin definitions.

Fan Header Pin Definitions (CPU, Chassis and Overheat)

Pin Number	Definition
1	Ground (black)
2	+12V (red)
3	Tachometer

Caution: These fan headers are DC power.

Chassis Intrusion

The Chassis Intrusion header is designated JL1. See the board layout in Chapter 1 for the location of JL1 and the table on the right for pin definitions.

Chassis Intrusion Pin Definitions (JL1)

Pin Number	Definition
1	Intrusion Input
2	Ground

Overheat LED (JOH1)

Connect an LED to the JOH1 header to provide warning of chassis overheating. See the table on the right for pin definitions.

Overheat LED Pin Definitions (JOH1)

Pin Number	Definition
1	+5V
2	OH Active

ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and the PS/2 mouse are located on J17. See the table on the right for pin definitions. (The mouse port is above the keyboard port. See the table on the right for pin definitions.

PS/2 Keyboard and Mouse Port Pin Definitions (J17)

Pin Number	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

Universal Serial Bus (USB)

Two Universal Serial Bus 2.0 connectors (USB0 and USB1) are provided for backpanel access. See the table on the right (above) for pin definitions. The P4SGA/P4SGL/P4SGR/P4SGE also provides four additional USB 2.0 headers on the motherboard, which may be used to provide front side chassis access (cables not included). These additional ports are labeled USB2, USB3, USB4 and USB5. See the tables on the right (below) for pin definitions.

USB0/USB1 Port Pin Definitions (J18/J19)

Pin#	Definition
1	+5V
2	P0-
3	P0+
4	Ground

USB2/4

Pin Number	Definition
1	+5V
2	PO-
3	PO+
4	Ground
5	Key

USB3/5

Pin Number	Definition
1	+5V
2	PO-
3	PO+
4	Ground
5	Ground

Wake-On-LAN

The Wake-On-LAN header is designated WOL on the motherboard. See the table on the right for pin definitions. You must enable the LAN Wake-Up setting in BIOS to use this function. (You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.)

Wake-On-LAN Pin Definitions (WOL)

Pin Number	Definition
1	+5V Standby
2	Ground
3	Wake-up

Wake-On-Ring

The Wake-On-Ring header is desig-
nated JWOR. This function allows
your computer to receive and be
"awakened" by an incoming call when
in the suspend state. See the table on
the right for pin definitions. You must
also have a WOR card and cable to
use WOR.

Wake-On-Ring Pin Definitions (JWOR1)

Pin Number	Definition
1	Ground
2	Wake-up

AC'97 Output (P4SGA/P4SGL)

AC'97 provides high quality onboard
audio. The P4SGA/P4SGL features 6-
channel sound for front L&R, rear
L&R, center and subwoofer speak-
ers. This feature is activated with
the Avanc software (on the CDROM
included with your motherboard).
Sound is then output through the Line
In, Line Out and MIC jacks (see at
right). Activate AC 97 with the "AC
97 Audio" setting in the Advanced
Chipset Features section of BIOS.

Front L&R / Rear L&R / Center & Subwoofer
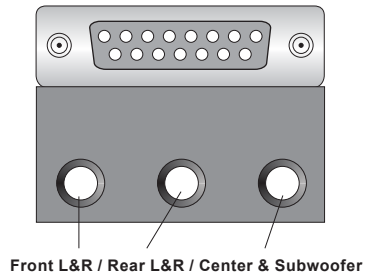
2-7   Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional set-tings. Jumpers create shorts be-tween two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

Note: On a two-pin jumper, "Closed" means the jumper is on both pins and "Open" means the jumper is either on only one pin or completely removed.

Connector Pins

Jumper Cap

Setting

```
3   2   1
●   ●   ■
```

```
3   2   1
●   ●   ■
```
Pin 1-2 short
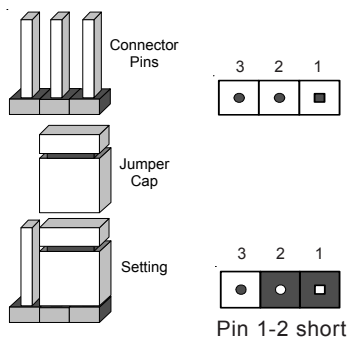
CMOS Clear

JBT1 is used to clear CMOS. Instead of pins, this jumper consists of con-tact pads to prevent accidentally clearing the contents of CMOS.
To clear CMOS,
1) First unplug the power cord(s) .
2) With the power disconnected, short the CMOS pads with a metal object such as a small screwdriver.
3) Remove the screwdriver (or short-ing device).
4) Reconnect the power cord(s) and power on the system.

Note: *Do not use the PW_ON con-nector to clear CMOS*.

USB Wake-Up

Use JPUSB to enable or disable USB Wake-Up, which allows you to wakeup the system by de-pressing a key on the keyboard or by clicking the mouse when either is connected to the USB0 or USB1 port. This jumper is used together with the USB Wake-Up function in BIOS. Enable both the jumper and the BIOS setting to allow the system to be woken up. See the table on the right for jumper settings.

USB Wake-Up Jumper Settings (JPUSB)

Jumper Position	Definition
1-2	Disabled
2-3	Enabled

Watch Dog Enable/Disable

JP2 enables and disables the Watch Dog function. Watch Dog is a system monitor that will re-start the system if a software ap-plication freezes operations. This jumper is used together with the Watch Dog enable function in BIOS. Enable both the jumper and the BIOS setting to use the Watch Dog feature. See the table on the right for jumper settings.

Watch Dog Enable Jumper Settings (JP2)

Jumper Position	Definition
Open	Disabled
Closed	Enabled

Front Side Bus Speed

JP1 is used to set the system (front side) bus speed for the pro-cessors. It is best to keep this jumper set to Auto. Note that the P4SGL only supports 400 MHz FSB speed. This jumper is used together with the CPU Clock set-ting in BIOS. See the table on the right for jumper settings.

Front Side Bus Speed Jumper Settings (JP1)

Jumper Position	Definition
Pins 1-2	Auto
Pins 2-3	400 MHz
Open	533 Mhz

Keyboard Wake-Up

The JPWAKE jumper is used to-gether with the Keyboard Wake-Up function in BIOS. Enable both the jumper and the BIOS setting to allow the system to be woken up by depressing a key on the key-board. See the table on the right for jumper settings. Your power supply must meet ATX specifica-tion 2.01 or higher and supply 720 mA of standby power to use this feature.

Keyboard Wake-Up
Jumper Settings
(JPWAKE)

Jumper Position	Definition
1-2	Disabled
2-3	Enabled

Speech Output Select (P4SGA/P4SGL)

The system BIOS has a voice fea-ture that it can use to inform you of the POST (Power-On-Self-Test) routine details. You can also pro-gram your voice to give the BIOS certain commands with the soft-ware included on the Supermicro CD that was included with your motherboard. JP3 is used to direct the BIOS voice output to either the Line Out port or via the Speaker header on JP2. The "Voice Warn-ing Output" setting (in the Ad-vanced BIOS Features section of BIOS) must be enabled to use this feature. See the table on the right for jumper setting pin definitions.

Speech Output Select
Jumper Settings (JP3)

Jumper Position	Definition
1-2	Speaker
2-3	Line Out

LAN1 Enable/Disable (P4SGR/P4SGE)

Change the setting of jumper JPL1 to enable or disable the onboard LAN1 or NIC (Network Interface Card) on the motherboard. See the table on the right for jumper settings. The default setting is Enabled.

**LAN1 (NIC)
Enable/Disable
Jumper Settings
(JPL1)**

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

LAN2 Enable/Disable (P4SGR/P4SGE)

Change the setting of jumper JPL2 to enable or disable the onboard LAN2 or NIC (Network Interface Card) on the motherboard. See the table on the right for jumper settings. The default setting is Enabled.

**LAN2 (NIC)
Enable/Disable
Jumper Settings
(JPL2)**

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

SCSI Enable/Disable (P4SGR)

The SCSI Termination jumper at JPA3 allows you to enable or disable the onboard SCSI controller. The normal (default) position is on to enable SCSI termination. See the table on the right for jumper settings.

**SCSI Enable/Disable
Jumper Settings
(JPA3)**

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

SCSI Channel A/B Termination Enable/Disable (P4SGR)

Jumpers JPA1 and JPA2 allow you to enable or disable termination for the individual SCSI channels. Jumper JPA1 controls SCSI channel A and JPA2 controls SCSI channel B. The normal (default) setting is open to enable (teminate) both SCSI channels. If you wish to connect external SCSI devices, you should disable termination for the channnel(s) you will be connecting them to. See the table on the right for jumper settings.

SCSI Channel Termination Enable/Disable Jumper Settings (JPA1, JPA2)

Jumper Position	Definition
Open	Enabled
Closed	Disabled

Chassis/Overheat Fan Select (P4SGR/P4SGE)

JP3 allows you to select between use of the Chassis fan and the Overheat fan. The default position is open to select the Overheat fan. The CPU Chassis fan is intended for use with Supermicro chassis. A closed jumper setting forces the chassis fan to always be on. See the table on the right for jumper settings.

Chassis/Overheat Fan Select Jumper Settings (JP3)

Jumper Position	Definition
Closed	Chassis Fan
Open	Overheat Fan

2-8   Game Port, Parallel Port, Floppy Drive, Hard Drive and AGP Connections

Use the following information to connect the floppy and hard disk drive cables.

- The floppy disk drive cable has seven twisted wires.

- A red mark on a wire typically designates the location of pin 1.

- A single floppy disk drive ribbon cable has 34 wires and two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.

- The 80-wire ATA100/66 IDE hard disk drive cable that came with your system has two connectors to support two drives. This special cable should be used to take advantage of the speed this new technology offers. The blue connector connects to the onboard IDE connector inter-face and the other connector(s) to your hard drive(s). Consult the documentation that came with your disk drive for details on actual jumper locations and settings for the hard disk drive.

Game Port Connector (P4SGA/P4SGL)

The game port is the gold port on the I/O backplane. See the table on the right for pin definitions.

Game Port Pin Definitions (GAME PORT)

Pin Number	Function	Pin Number	Function
1	+5V fused	2	GP4 (JSBUTO)
3	GP0 (JSX1R)	4	Ground
5	Ground	6	GP1 (JSY1R)
7	GP5 (JSBUT1)	8	+5V fused
9	+5V fused	10	GP6 (JSBUT2)
11	GP2 (JSX2R)	12	MIDI-OUTR
13	GP3 (JSY2R)	14	GP7 (JSBUT3)
15	MIDI-INR		

Parallel Port Connector

The parallel port is located on J15. See the table on the right for pin definitions.

Parallel (Printer) Port Pin Definitions (J15)

Pin Number	Function	Pin Number	Function
1	Strobe-	2	Auto Feed-
3	Data Bit 0	4	Error-
5	Data Bit 1	6	Init-
7	Data Bit 2	8	SLCT IN-
9	Data Bit 3	10	GND
11	Data Bit 4	12	GND
13	Data Bit 5	14	GND
15	Data Bit 6	16	GND
17	Data Bit 7	18	GND
19	ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SLCT	26	NC

Ultra160 SCSI Connectors (P4SGR)

Refer to the table on the right for the pin definitions of the Ultra160 SCSI connectors located at JA1, JA2 and JA3.

68-pin Ultra160 SCSI Connectors (JA1, JA2, JA3)

Connector Contact Number	Signal Names	Connector Contact Number	Signal Names
1	+DB(12)	35	-DB(12)
2	+DB(13)	36	-DB(13)
3	+DB(14)	37	-DB(14)
4	+DB(15)	38	-DB(15)
5	+DB(P1)	39	-DB(P1)
6	+DB(0)	40	-DB(0)
7	+DB(1)	41	-DB(1)
8	+DB(2)	42	-DB(2)
9	+DB(3)	43	-DB(3)
10	+DB(4)	44	-DB(4)
11	+DB(5)	45	-DB(5)
12	+DB(6)	46	-DB(6)
13	+DB(7)	47	-DB(7)
14	+DB(P)	48	-DB(P)
15	GROUND	49	GROUND
16	DIFFSENS	50	GROUND
17	TERMPWR	51	TERMPWR
18	TERMPWR	52	TERMPWR
19	RESERVED	53	RESERVED
20	GROUND	54	GROUND
21	+ATN	55	-ATN
22	GROUND	56	GROUND
23	+BSY	57	-BSY
24	+ACK	58	-ACK
25	+RST	59	-RST
26	+MSG	60	-MSG
27	+SEL	61	-SEL
28	+C/D	62	-C/D
29	+REQ	63	-REQ
30	+I/O	64	-I/O
31	+DB(8)	65	-DB(8)
32	+DB(9)	66	-DB(9)
33	+DB(10)	67	-DB(10)
34	+DB(11)	68	-DB(11)

Floppy Connector

The floppy connector is located on JP5. See the table on the right for pin definitions.

Floppy Connector Pin Definitions (JP5)

Pin Number	Function	Pin Number	Function
1	GND	2	FDHDIN
3	GND	4	Reserved
5	Key	6	FDEDIN
7	GND	8	Index-
9	GND	10	Motor Enable
11	GND	12	Drive Select B-
13	GND	14	Drive Select A-
15	GND	16	Motor Enable
17	GND	18	DIR-
19	GND	20	STEP-
21	GND	22	Write Data-
23	GND	24	Write Gate-
25	GND	26	Track 00-
27	GND	28	Write Protect-
29	GND	30	Read Data-
31	GND	32	Side 1 Select-
33	GND	34	Diskette

IDE Connector Pin Definitions
(J6, J7)

Pin Number	Function	Pin Number	Function
1	Reset IDE	2	GND
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	GND	20	Key
21	DRQ3	22	GND
23	I/O Write-	24	GND
25	I/O Read-	26	GND
27	IOCHRDY	28	BALE
29	DACK3-	30	GND
31	IRQ14	32	IOCS16-
33	Addr 1	34	GND
35	Addr 0	36	Addr 2
37	Chip Select 0	38	Chip Select 1-
39	Activity	40	GND

IDE Connectors

There are no jumpers to config-
ure the onboard IDE interfaces
J6 and J7. See the table on the
left for pin definitions. You
must use the ATA100/66 cable
included with your system to
benefit from the ATA100/66
technology.

1.5V 4xAGP Slot (P4SGA/P4SGR/P4SGE)

The 4xAGP slot is not included on the P4SGL. You should first disable
the integrated video in BIOS before adding a VGA card to this slot. (See
the "On-Chip VGA" setting in the Advanced Chipset Features section in
BIOS.) The 4xAGP slot is backward compatible with 2xAGP graphics
cards.

To install a VGA card, power down the system and remove the I/O shield
corresponding to the AGP slot. Then, pull back the locking tab on the slot
before inserting your VGA card. After the card is inserted, close the
locking tab if possible (some VGA cards have a notch in the corner of
the board to lock it into place, others do not.) Replace all chassis covers
before restoring power to the system.

Locking Tab
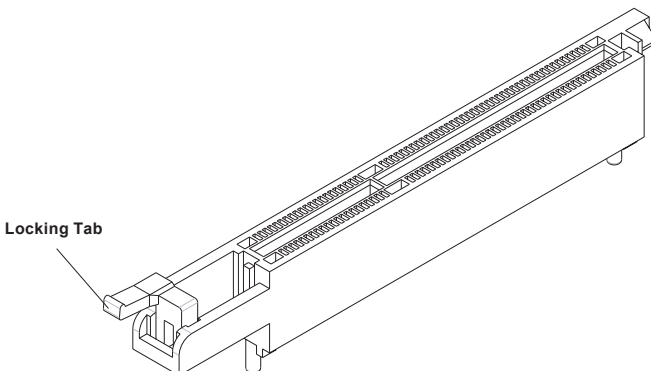
2-9   Installing Software

After all the hardware has been installed, you must install, first of all, the operating system, and then, the software drivers. The necessary drivers are all included on the Supermicro CD that came packaged with your motherboard. After inserting this CD into your CDROM drive, the display shown in Figure 2-5 should appear. (If this display does not appear, click on the My Computer icon and then on the icon representing your CDROM drive. Finally, double click on the S "Setup" icon.)
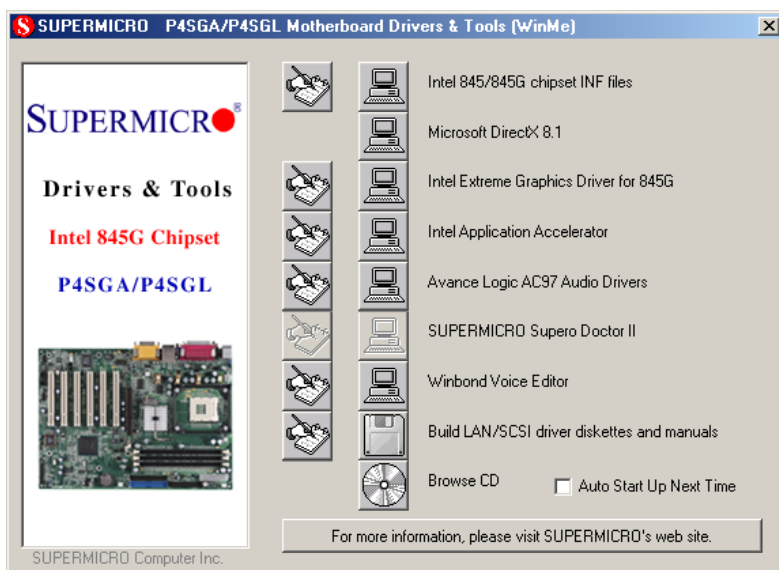
Figure 2-5. Driver/Tool Installation Display Screen

Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these *in order from top to bottom* to install each item one at a time. **After installing each item, you must reboot the system before moving on to the next item on the list.** You should install everything here except for the SUPER Doctor utility and the LAN/SCSI driver diskettes, which are optional.   The Security and Graphics Drivers support multiple languages. Click the arrows to pull down a menu of choices. The bottom icon with a CD on it allows you to view the entire contents of the CD. **Note:** The memory size reported in the device manager may be less than expected because some is used by the onboard graphics. Higher screen resolutions will take up more of this memory.

SuperO Doctor II

The Super Doctor II program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. Super Doctor II displays crucial system information such as CPU temperature, system voltages and fan status. See Figure 2-6 below for a display of the SuperDoctor interface.
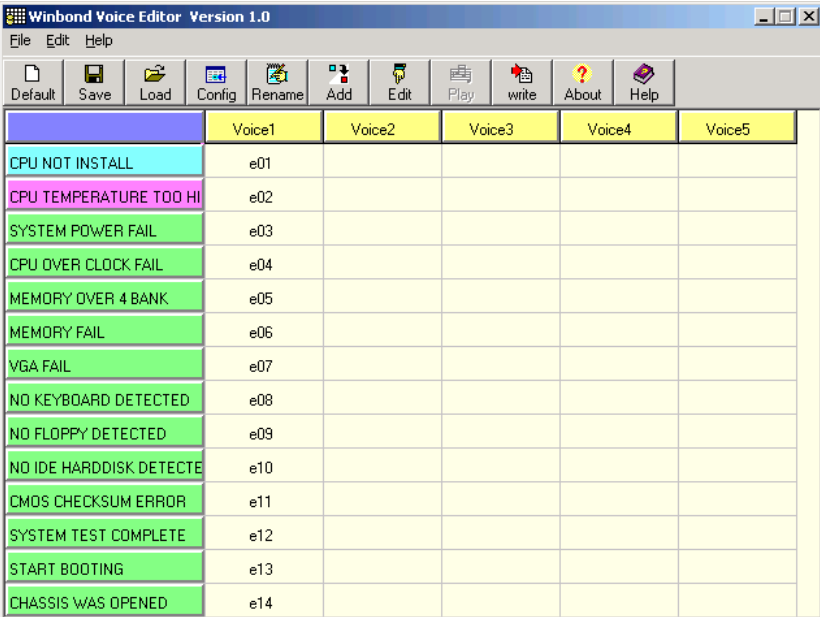
Figure 2-6. SuperDoctor II Interface Display Screen

Voice BIOS Editor

The Winbond Voice BIOS featured on your motherboard includes an editor that alllows you to choose from multiple languages and to make your own recordings for POST routine details. The software is included on the CDROM that came with your motherboard. See Figure 2-7 below for a display of the Winbond Voice Editor interface.

Figure 2-7. Voice BIOS Editor Display Screen

	Voice1	Voice2	Voice3	Voice4	Voice5
CPU NOT INSTALL	e01				
CPU TEMPERATURE TOO HI	e02				
SYSTEM POWER FAIL	e03				
CPU OVER CLOCK FAIL	e04				
MEMORY OVER 4 BANK	e05				
MEMORY FAIL	e06				
VGA FAIL	e07				
NO KEYBOARD DETECTED	e08				
NO FLOPPY DETECTED	e09				
NO IDE HARDDISK DETECTE	e10				
CMOS CHECKSUM ERROR	e11				
SYSTEM TEST COMPLETE	e12				
START BOOTING	e13				
CHASSIS WAS OPENED	e14				

Chapter 3
Troubleshooting

3-1   Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any hardware components.

Before Power On

1. Check that the +5v standby LED is not lit (LE2 on motherboard).
2. Make sure the 4-pin 12v power connector at J24 is connected to your power supply.
3. Make sure no short circuits exist between the motherboard and chassis.
4. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
5. Remove all add-on cards.
6. Install a CPU and heatsink (making sure it is fully seated) and connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.

No Power

1. Make sure no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

NOTE

If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to App. B.

Memory Errors

1. Make sure the DIMM modules are properly and fully installed.
2. You should be using unbuffered DDR memory (see next page). Also, it is recommended that you use the same memory speed for all DIMMs in the system. See Section 2-4 for memory limitations.
3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.
4. Check the power supply voltage 115V/230V switch.

Losing the System's Setup Configuration

1. Ensure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2   Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, Supermicro does not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our web site   (http://www.supermicro.com/techsupport.htm) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our web site at

http://www.supermicro.com/techsupport/download.htm.

Note: Not all BIOS can be flashed depending on the modifications to the boot block code.

3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:
   •Motherboard model and PCB revision number
   •BIOS release date/version (this can be seen on the initial display when your system first boots up)
   •System configuration
   An example of a Technical Support form is posted on our web site at http://www.supermicro.com/techsupport/contact_support.htm.
4. Distributors: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail at support@supermicro.com or by fax at (408) 503-8019.

3-3 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The P4SGA/P4SGL supports up to 2 GB of non-ECC **unbuffered** DDR-200 (PC1600) or DDR-266 (PC2100) type SDRAM. The P4SGR/P4SGE supports up to 2 GB of non-ECC **unbuffered** DDR-266 (PC2100) or DDR-333 (PC2700) type SDRAM. **Important:** See Section 2-4 on rules to observe when installing DDR memory modules.

Question: How do I update my BIOS?

Answer: It is recommended that you **not** upgrade your BIOS if you are not experiencing problems with your system. Updated BIOS files are located on our web site at http://www.supermicro.com/techsupport/BIOS/bios.htm. Please check our BIOS warning message and the information on how to update your BIOS on our web site. Also, check the current BIOS revision and make sure it is newer than your current BIOS before downloading. Select your mainboard model and download the BIOS file to your computer. Unzip the BIOS update file and you will find the readme.txt (flash instructions), the awdflash.exe (BIOS flash utility) and the BIOS image (xxxx.bin) files. Copy these files onto a bootable floppy and reboot your system. It is not necessary to set the BIOS boot block protection jumpers on the mainboard. At the DOS prompt upon rebooting, enter the command "awdflash." Then type in the BIOS file that you want to update (xxxx.bin).

Question: After flashing the BIOS my system does not have video. How can I correct this?

Answer: If the system does not have video after flashing your new BIOS, it indicates that the flashing procedure failed. To fix this, first clear CMOS per the instructions in this manual and retry the BIOS flashing procedure. If you still do not have video, please use the following **BIOS Recovery Procedure**. First, make sure the JPWAKE jumper is disabled. Then, turn your system off and place the floppy disk with the saved BIOS image file (see above FAQ) in drive A. Press and hold <Alt> and <F2> at the same time, then turn on the power keeping these keys pressed until your floppy drive starts reading. Your screen will remain blank until the BIOS program is done. If the system reboots correctly, then the recovery was successful. The BIOS Recovery Procedure will not update the boot block in your BIOS.

Question: What's on the CD that came with my motherboard?

Answer: The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the applications you need. Applications on the CD include chipset drivers for Windows and security and audio drivers.

Question: Why can't I turn off the power using the momentary power on/off switch?

Answer: The instant power off function is controlled in BIOS by the Power Button Mode setting. When the On/Off feature is enabled, the motherboard will have instant off capabilities as long as the BIOS has control of the system. When the Standby or Suspend feature is enabled or when the BIOS is not in control such as during memory count (the first screen that appears when the system is turned on), the momentary on/off switch must be held for more than four seconds to shut down the system. This feature is required to implement the ACPI features on the motherboard.

Question: How do I utilize the six-channel sound?

Answer: Six-channel sound is only available on the P4SGA/P4SGL. Enable it through the audio driver software that was included in your motherboard package. When activated, sound will be routed through the jacks under the Game Port as follows: Line Out = front L&R speakers, Line In = rear L&R speakers, MIC = center and subwoofer speakers. You must also enable the "AC97 Audio" setting in the Advanced Chipset section of the BIOS setup.

Question: I see some of my PCI devices sharing IRQs, but the system seems to be fine. Is this correct or not?

Answer: Some PCI Bus Mastering devices can share IRQs without performance penalties. These devices are designed to work correctly while sharing IRQs. See Table 3-1, below, for details on shared IRQs.

Table 3-1. P4SGA/P4SGL/P4SGR/P4SGE IRQs

PCI 1 shares an IRQ with AC97 and the System Management bus
PCI 2 shares an IRQ with a USB 1.1 controller (USB4 & USB5)*
PCI 3 shares an IRQ with a USB 1.1 controller (USB2 & USB3)*
PCI 4 has a dedicated IRQ (does not share)
PCI 5 has a dedicated IRQ (does not share)
PCI 6 shares an IRQ with the USB 2.0 controller (USB0 - USB5)**

* Assumes USB 2.0 drivers have not been installed.
** Since the P4SGR/P4SGE only have five PCI slots, the USB 2.0 controller has its own dedicated IRQ.

Question: I installed my microphone correctly but I can't record any sound (P4SGA/P4SGL). What should I do?

Answer: Go to <Start>, <Programs>, <Accessories>, <Entertainment> and then <Volume Control>. Under the Properties tab, scroll down the list of devices in the menu and check the box beside "Microphone".

Question: How do I connect the ATA100/66 cable to my IDE device(s)?

Answer: The 80-wire/40-pin high-density ATA100/66 IDE cable that came with your system has two connectors to support two drives. This special cable must be used to take advantage of the speed the ATA100/66 technology offers. **Connect the blue connector to the onboard IDE header and the other connector(s) to your hard drive(s).** Consult the documentation that came with your disk drive for details on actual jumper locations and settings.

3-4   Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4
AwardBIOS

4-1   Introduction

This chapter describes the AwardBIOS for the P4SGA/P4SGL/P4SGR/P4SGE. The Award ROM BIOS is stored in a Flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Refer to the Manual Download area of our web site for any changes to BIOS that are not reflected in this manual.

System BIOS

The BIOS is the Basic Input Output System used in all IBM® PC, XT™, AT®, and PS/2® compatible computers. The AwardBIOS Flash chip stores the system parameters, such as amount of memory, type of disk drives and video displays, etc. CMOS requires very little power. When the computer is turned off, a back-up battery provides power to the BIOS Flash chip, enabling it to retain the system parameters. Each time the computer is powered-on, the computer is then configured with the values stored in the BIOS ROM by the system BIOS, which gains control when the computer is powered on.

How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing <Del> at the appropriate time during system boot.

Starting the Setup Utility

Normally, the only visible POST (Power On Self Test) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens, such as the Chipset and Power menus. Section 4-3 gives detailed descriptions of each parameter setting in the Setup utility.

4-2   Running Setup

Optimal default settings are in bold text unless otherwise noted.

The BIOS setup options described in this section are selected by choosing the appropriate text from the Main BIOS Setup screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options (see on next page).

When you first power on the computer, the AwardBIOS™ is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1.   By pressing <Del> immediately after turning the system on, or
2.   When the following message appears briefly at the bottom of the screen during the POST (Power On Self-Test), press the <Del> key to activate the Main Setup Menu.

Press DEL to enter SETUP

4-3   Main BIOS Setup

All Main Setup options are described in this section. The Main BIOS Setup screeen is displayed below.

Use the <Up> <Down> arrow keys or the <Tab> key to move among the different settings in the above menu.

Press the <Esc> key to exit theCMOS Setup Menu and use the <Left> <Right> arrow keys to enter the the other categories of BIOS settings. The next section is described in detail to illustrate how to navigate through the menus.

Main BIOS Setup Menu

```
BIOSVIEW                                                    _ □ ✕

Auto   ▼   ▫ ▣ ▦ ⊞ ⬚⬚ A

              Phoenix - Award WorkstationBIOS CMOS Setup Utility
   Main  Advanced  PnP/PCI  Power  Boot  Security  Exit

   Date (mm:dd:yy)            Mon, Sep 23 2002        Item Help
   Time (hh:mm:ss)            11 : 24 : 13
   Swap Floppy Drive          [Disabled]         Menu Level  ▶

 ▶ IDE Primary Master        [ None]             Change the day, month,
 ▶ IDE Primary Slave         [ None]             year and century
 ▶ IDE Secondary Master
 ▶ IDE Secondary Slave

   Drive A                    [1.44M, 3.5 in.]
   Drive B                    [None]

   Video                      [EGA/VGA]
   Halt On                    [All , But Keyboard]

   Base Memory                    640K
   Extended Memory              65535K
   Total Memory                  1024K

↑↓→←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
   F5:Previous Values    F6:Fail-Safe Defaults   F7:Optimized Defaults
```

Date/Time

Set the system date and time. Key in the correct information in the "mm", "dd" and "yy" fields. Press the "Enter" key to save the data.

Swap Floppy Drive

This setting allows the user to swap the designation (A and B) of the floppy disk drives installed in the system (if there are two floppy disk drives installed on the mainboard). The options are "**Disabled**" and "Enabled".

IDE Primary Master/IDE Primary Slave/IDE Secondary Master/IDE Secondary Slave

These options allow the user to set the parameters of the IDE Primary Master/Slave and IDE Secondary Master/Slave slots. Press "Enter" to activate the following sub-menu screen for detailed options of these items. Set the correct configurations accordingly. The items included in the sub-menu are listed below:

IDE HDD Auto-Detection

Press the <Enter> key to activate the "IDE HDD Auto-Detection" function, which will allow BIOS to automatically detect the status of the IDE HDD installed in the system, such as the size and number of cylinders.

```
BIOSVIEW                                                    _ □ ×
 Auto         ▔ □ ▤ ▦ ⊞ ☞ 🗗 A
                 Phoenix - AwardBIOS CMOS Setup Utility
▐ Main ▐

            IDE Primary Master                      Item Help

  IDE HDD Auto-Detection      [Press Enter]    Menu Level   ▶▶

  IDE Primary Master          [Auto]           To auto-detect the
  Access Mode                 [Auto]           HDD's size, head... on
                                               this channel
  Capacity                    0 MB

  Cylinder                    0
  Head                        0
  Precomp                     0
  Landing Zone                0
  Sector                      0

  IDE Primary Master PIO      [Auto]
  IDE Primary Master UDMA     [Auto]


↑↓→←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
   F5:Previous Values     F6:Fail-Safe Defaults   F7:Optimized Defaults
```

IDE Primary Master

This option allows the user to determine the manner in which the AwardBIOS sets the settings for the IDE Primary Master Device. The options are "None", "**Auto**" and "Manual."

Access Mode

This item determines the location through which the AwardBIOS accesses the IDE Primary Master Device. The settings are "CHS", "LBA", "Large", and "**Auto**".

IDE Primary Slave PIO

See the previous setting for description. The options for this setting are "**Auto**", "Mode 0", "Mode 1", "Mode 2", "Mode 3" and "Mode 4".

IDE Primary Master UDMA

This is available only when your IDE drive supports UDMA and the operating environment also includes a UDMA drive. If your IDE hard drive and your system software both support UDMA, select Auto to enabled BIOS support. The options for this setting are "**Auto**" and "Disabled".

Drive A/Drive B

These settings allow the user to set the type of floppy disk drive installed in the system. The options are "None", "360K, 5.25 in", "1.2M, 5.25 in", "720K, 3.5 in", "1.44M, 3.5 in" and "2.88M, 3.5 in". Default settings are "1.44, 3.5" in for Drive A and "None" for Drive B.

Video

Use this setting to specify the type of display you are using with the system. Options are "**EGA/VGA**", "CGA 40", "CGA 80" and "MONO".

Halt On

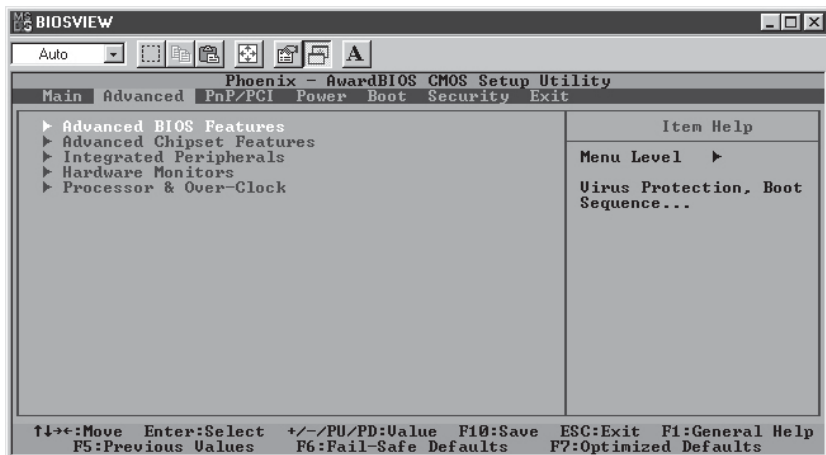If the system encounters a non-specified error during boot-up, it will come to a halt as directed by these settings. You can tell the system to halt on "All Errors", "No Errors", "**All, But Keyboard**", "All, But Diskette" or "All, But Disk/Key".

Base Memory/Extended Memory/Total Memory

These are displays that inform you how much of each type of memory is recognized as being present in the system.

4-4   Advanced BIOS Setup

 Choose Advanced BIOS Setup from the Award BIOS main menu with the Left/
Right arrow keys.   You should see the following display. Select one of the
items in the left frame of the screen to go to the sub screen for that item.
Advanced BIOS Setup options are displayed by highlighting the option using the
arrow keys. All Advanced BIOS Setup options are described in this section.

```
BIOSVIEW                                                        _ □ ×
 Auto      ▼  □ 🗐🗐  ⊞  🖻🖨  A
                   Phoenix - AwardBIOS CMOS Setup Utility
 Main  Advanced  PnP/PCI  Power  Boot  Security  Exit
  ► Advanced BIOS Features                          Item Help
  ► Advanced Chipset Features
  ► Integrated Peripherals              Menu Level    ►
  ► Hardware Monitors
  ► Processor & Over-Clock              Virus Protection, Boot
                                        Sequence...




 ↑↓←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
     F5:Previous Values      F6:Fail-Safe Defaults   F7:Optimized Defaults
```

4-4.1   Advanced BIOS Features

When the item of Advanced BIOS Features is highlighted, press the <Enter>
key to activate the screen below:

```
BIOSVIEW                                                        _ □ ×
 Auto      ▼  □ 🗐🗐  ⊞  🖻🖨  A
                   Phoenix - Award WorkstationBIOS CMOS Setup Utility
      Advanced
           Advanced BIOS Features                    Item Help

   Init Display First        [PCI Slot]        Menu Level   ►►
   CPU Hyper-Threading       [Enabled]
   Quick Power On Self Test  [Enabled]

   Boot Up NumLock Status    [On]
   Gate A20 Option           [Fast]
   Typematic Rate Setting    [Disabled]
 × Typematic Rate (Chars/Sec) 6
 × Typematic Delay (Msec)    250
   APIC Mode                 [Enabled]
   OS Select For DRAM > 64MB [Non-OS2]
   Console Redirection       [Disabled]
 × Baud Rate                 19200
   Agent after boot          [Disabled]
   PWRON After PWR-Fail      [Off]

   Full Screen LOGO Show     [Enabled]

 ↑↓←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
     F5:Previous Values      F6:Fail-Safe Defaults   F7:Optimized Defaults
```

Init Display First

This setting allows the user to determine which device will be first displayed when the sytem boots up - a device installed in the PCI slot or a device intalled in the AGP slot. The options are "PCI slot" and "**Onboard/AGP**".

Quick Power-On Self Test

If enabled, this feature will speed up the POST (Power On Self Test) routine after the computer is switched on. The settings are "**Enabled**" and "Disabled". If "Disabled", the POST will run at normal speed.

Boot Up NumLock Status

This option enables the system to check the status of the NumLock key during boot-up. The settings are "**On**" and "Off".

Gate A20 Option

This option allows the user to determine if the chipset or the keyboard controller should have the control over Gate A20. The settings are "Normal" or "**Fast**". If set to "Normal", a pin in the keyboard controller controls Gate A20. If "Fast" is selected, the chipset will have the control over Gate A20.

Typematic Rate Setting

If enabled, the option allows the user to set the number of times a key stroke repeats itself in a second when the key is held down. If disabled, the keyboard controller sets the rate.

Typematic Rate (Chars/Sec)

You may change this setting only if the Typmatic Rate Setting is enabled. This setting allows the user to set the number of times a key stroke repeats itself in a second when the key is held down. The options are: "**6**", "8", "10", "12", "15", "20", "24" and "30."

Typematic Delay

You may change this setting only if the Typmatic Rate Setting is enabled. This setting sets the delay time after a key is held down before

it begins to repeat the keystroke. The settings are: "**250**", "500", "750" and "1000."

APIC Mode

This setting allows you to **Enable** or Disable APIC. APIC is used to assign interrupt signals to a specific processor on multi-processor system and provides IRQs beyond the conventional 16 under Windows 2000 or XP. It has no effect on single processor systems.

OS Select For DRAM > 64MB

This setting should be changed only if using OS2 and your system has more than 64 MB of RAM. The options are "OS2" and "**Non-OS2**".

Console Redirection

When Enabled, the console (display) can be redirected via the COM port. When **Disabled**, console redirection is attempted only if keyboard is absent.

Agent After Boot

When Enabled, the agent will continue running after boot up. The default setting is **Disabled**.

PWRON After PWR-Fail

This setting allows the user to specify how the system will respond when power is reapplied after the system has gone down due to a power failure. The options are "**Off**", "On" and "Former-Sts".

Full Screen Logo Show

The options for this setting are "Enabled" and "**Disabled**". This refers to the logo that appears when your computer boots up.
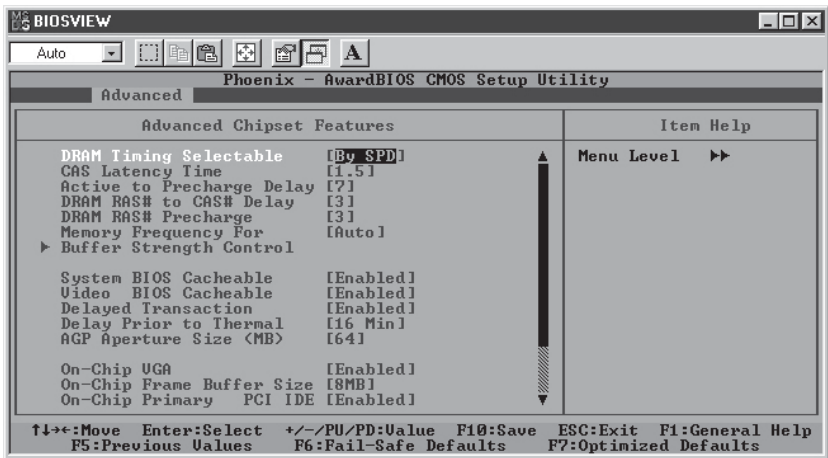
Voice Warning Output (P4SGA/P4SGL only)

This setting allows you to turn the Voice BIOS on or off. Voice BIOS is a new feature that gives you verbal (spoken) details of the POST (Power On

Self Test) routine that runs during system boot. This output is available in several languages - use the Voice BIOS software (included on the CD that came with the motherboard) to choose a different language. Remember, you will need sound output enabled and set up to use this feature. See Section 2-9 for more details. The options for this setting are "**Enabled**" and "Disabled".

4-4.2   Advanced Chipset Features

This section covers the functions used for configuring the system based upon the special features offered by the Plumas chipset. The chipset manages the operations of major components of the board. Normally, the default settings for the Advanced Chipset Features listed in the section are pre-configured by the manufacturer for the optimal performance of the system. It is recommended that the user does not alter the default settings. This section is provided as an emergency measure for the user to restore the functions of the system when the critical data stored in the BIOS is lost.

```
BIOSVIEW                                                      _ □ ×
 Auto  ▼    ⬚ 🗐 🗎 ⊞ 🖻 🖫  A
                   Phoenix - AwardBIOS CMOS Setup Utility
        Advanced
          Advanced Chipset Features                    Item Help

    DRAM Timing Selectable     [By SPD]          ▲    Menu Level   ▶▶
    CAS Latency Time           [1.5]
    Active to Precharge Delay  [7]
    DRAM RAS# to CAS# Delay    [3]
    DRAM RAS# Precharge        [3]
    Memory Frequency For       [Auto]
  ▶ Buffer Strength Control

    System BIOS Cacheable      [Enabled]
    Video  BIOS Cacheable      [Enabled]
    Delayed Transaction        [Enabled]
    Delay Prior to Thermal     [16 Min]
    AGP Aperture Size (MB)     [64]

    On-Chip VGA                [Enabled]
    On-Chip Frame Buffer Size  [8MB]
    On-Chip Primary   PCI IDE  [Enabled]  ▼

 ↑↓→←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
    F5:Previous Values     F6:Fail-Safe Defaults    F7:Optimized Defaults
```

DRAM Timing Selectable

This item regulates dynamic random access memory (DRAM) timing. The options are "Manual" and "**By SPD**".

CAS Latency TIme

This item regulates memory column address strobe (CAS) timing. The settings are "**1.5**", "2", "2.5" and "3".

Active to Precharge Delay

This item regulates the number of memory clock cycles allowed for memory refresh charging. The options are "**7**", "6" and "5". Shorter timings increase system memory throughput at the risk of lacking sufficient refresh charge.

DRAM RAS# to CAS# Delay

This item regulates the number of memory closk cycles between strobing a row address (RAS) and a column address (CAS). Shorter numbers of clock cycles improve system memory performance at the risk of missing data. The options are "**3**" and "2".

DRAM RAS# Precharge

This item regulates the number of system memory clock cycles for RAS precharging. The options are "**3**" and "2".

Memory Frequency For

This item regulates system memory frequency. The options are "PC100", "PC133" and "**Auto**".

Buffer Strength Control

Highlighting this item and pressing <Enter> will display a sub menu that allows you to control various buffer strengths.

System BIOS Cacheable

If enabled, the system BIOS information stored in the BIOS ROM (Read Only Memory) chip will be written and temporarily stored in the "cacheable" memory section of the CPU, giving the CPU faster access to the information. The options are "Disabled" and "**Enabled**".

Video BIOS Cacheable

If enabled, the information regarding the Video BIOS stored in the BIOS ROM (Read Only Memory) chip will be written and temporarily stored in the "cacheable" memory section of the CPU, giving the CPU faster access to the information. The options are "**Disabled**" and "Enabled".

Delayed Transaction

This setting compensates for the slower speed of ISA cards on a PCI interface and so is only relevant if ISA cards are present on the motherboard. The options are "**Enabled**" and "Disabled".

Delay Prior to Thermal

The options for this setting are "4 Min", "8 Min", "**16 Min**" and "32 Min".

AGP Aperture size (MB)

This setting allows the user to set the aperture size for the Accelerated Graphics Port (AGP). The options are "4", "8", "16", "32", "**64**", "128" and "256" (MB).

On-Chip VGA

The 845G chipset features high-performance on-chip graphics. You can use this setting to disable the integrated graphics if you wish to use a graphics add-on card in the 4xAGP Pro slot. The options for this setting are "**Enabled**" and "Disabled".

On-Chip Frame Buffer Size

This setting is for seeting the frame buffer size for the integrated graphics. The options for this setting are "**8MB**" and "1MB".

On-Chip Primary PCI IDE

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select Enabled to activate each channel separately. The options for this setting are "**Enabled**" and "Disabled".

On-Chip Secondary PCI IDE

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select Enabled to activate each channel separately. The options for this setting are "**Enabled**" and "Disabled".

USB Controller

You should Enable this setting if your system contains a Universal Serial Bus (USB) controller (it does) and you have USB peripherals. The options for this setting are "**Enabled**" and "Disabled".

USB Keyboard Support

If enabled, this setting allows the user to activate the BIOS support of the On-Chip USB Keyboard Controller. The options are "**Disabled**" and "Enabled".
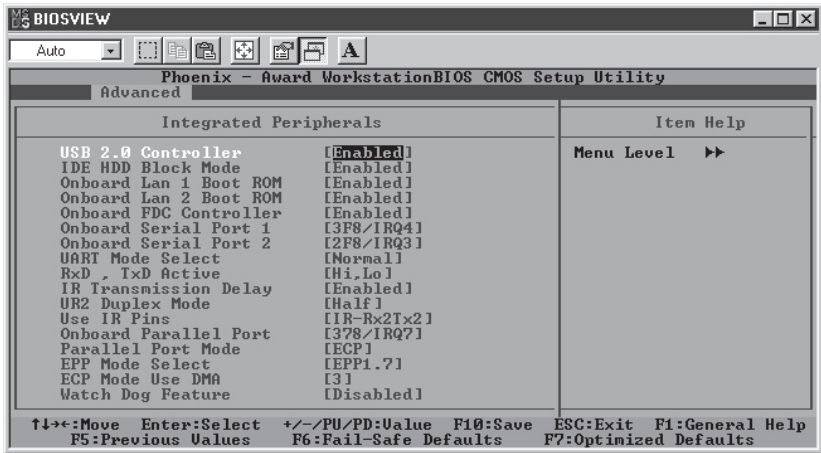
Onboard LAN Control

This setting allows the user to activate BIOS support for the onbaord LAN (Ethernet) port. The options for this setting are "**Enabled**" and "Disabled".

AC 97 Audio

This setting allows the user to activate BIOS support for AC'97 audio. The options for this setting are "**Enabled**" and "Disabled".

4-4.3 Integrated Peripherals

When the item "Integrated Peripherals" is highlighted, press the <Enter> key to activate the following sub-menu screen.

```
M5 BIOSVIEW                                                        _ □ ×

 Auto      ▼  [ ][ ][ ] [ ] [ ][ ] A

           Phoenix - Award WorkstationBIOS CMOS Setup Utility
      Advanced

          Integrated Peripherals                    Item Help

     USB 2.0 Controller        [Enabled]     Menu Level  ▶▶
     IDE HDD Block Mode         [Enabled]
     Onboard Lan 1 Boot ROM     [Enabled]
     Onboard Lan 2 Boot ROM     [Enabled]
     Onboard FDC Controller     [Enabled]
     Onboard Serial Port 1      [3F8/IRQ4]
     Onboard Serial Port 2      [2F8/IRQ3]
     UART Mode Select           [Normal]
     RxD , TxD Active           [Hi,Lo]
     IR Transmission Delay      [Enabled]
     UR2 Duplex Mode            [Half]
     Use IR Pins                [IR-Rx2Tx2]
     Onboard Parallel Port      [378/IRQ7]
     Parallel Port Mode         [ECP]
     EPP Mode Select            [EPP1.7]
     ECP Mode Use DMA           [3]
     Watch Dog Feature          [Disabled]

  ↑↓→←:Move  Enter:Select  +/-/PU/PD:Value  F10:Save  ESC:Exit  F1:General Help
       F5:Previous Values    F6:Fail-Safe Defaults   F7:Optimized Defaults
```

USB 2.0 Controller

Select "Enabled" to enable USB 2.0 on your motherboard. The settings are "**Enabled**" and "Disabled"

IDE HDD Block Mode

Select "Enabled" to allow automatic detection of the optimal number of block read/writes per sector that the IDE drive can support. The settings are "**Enabled**" and "Disabled".

Onboard LAN 1/2 Boot ROM

This setting allows you to decide whether to Enable invoking the boot ROM of the onboard chip for LAN 1/2. The settings are "**Enabled**" and "Disabled".

Onboard FDC Controller

Select "Enabled", if your system has a floppy disk controller (FDC) installed on the main board and you wish to use it. The settings are "**Enabled**" and

"Disabled".

Onboard Serial Port 1/Port 2

This setting allows the user to set the address and the corresponding IRQ for the Serial Port1 and Serial Port 2.   The options are "Disabled" , "3F8/ IRQ4", "2F8/IRQ3", "3E8/IRQ4", "2E8/IRQ3", and "Auto". The default setting for Serial Port1 is "**3F8/IRQ4**" and the default for Port 2 is is "**2F8/IRQ3**".

UART Mode Select

This setting allows the user to select the UART mode for BIOS. The options are "IrDA", "ASKIR" and "**Normal**".

RxD, TxD Active

This allows the user to change the settings for the "RxD, TxD Active" function. The options are "Hi, Hi", "**Hi, Lo**", "Lo, Hi", and "Lo, Lo".

IR Transmission Delay

If "Enabled", the transmssion of IR (infrared) signals will be delayed. The options are "**Enabled**" and "Disabled".

UR2 Duplex Mode

This setting set the mode for the UR2 Duplex Mode. The options are "Full" and "**Half**".

Use IR Pins

This item sets the usage of the IR pins. The options are "RxD2, TxD2" and "**IR-Rx2Tx2**".

Onboard Parallel Port

This setting allows the user to set the address and the corresponding IRQ for the onboard parallel port. The options are "Disabled", "**378/IRQ7**", "278/ IRQ5" and "3BC/IRQ7".

Parallel Port Mode

This setting sets the mode for the onboard Parallel port. The options are "SPP," "EPP", "**ECP**" "ECP+EPP" and "Normal".

EPP Mode Select

This setting allows the user to select the EPP port type. The options are "EPP 1.9" and "**EPP 1.7**".

ECP Mode Use DMA

This setting allows the user to select the DMA channel for the ECP mode (port) to use. The options are "1" and "**3**".

Game Port Address (P4SGA/P4SGL only)

This setting allows the user to set the Game Port address. The options are "Disabled", "**201**" and "209."

Midi Port Address (P4SGA/P4SGL only)

This setting allows the user to set the Midi Port address. The options are "Disabled", "**330**", "300" and "290".

Midi Port IRQ (P4SGA/P4SGL only)

This setting allows the user to set the Midi Port IRQ. The options are "5" and "**10**".

Watch Dog Feature

This setting allows you to Enable or Disable the Watch Dog feature. You must also change the setting of the Watch Dog jumper to enable this function (see jumper settings in Chapter 2). Options are "**Enabled**" and "Disabled".

4-4.4   Hardware Monitors

When the item "Hardware Monitors" is highlighted, press the <Enter> key to activate the following sub-menu screen.
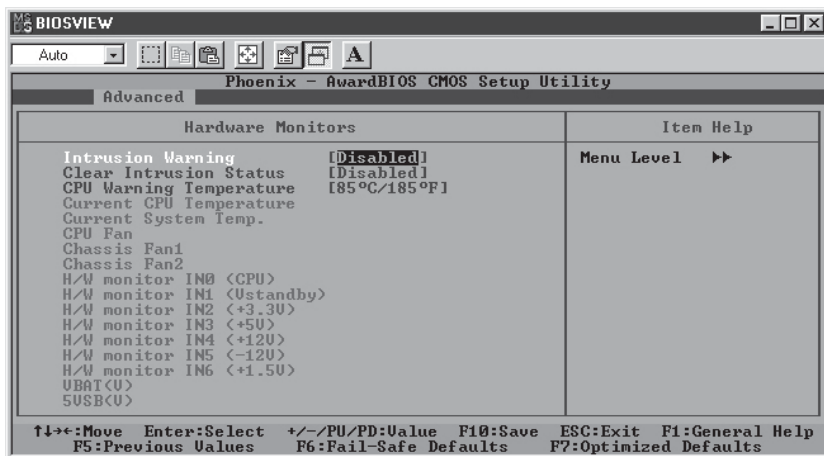
There are only three settings on this menu (below). The rest of this menu shows various temperatures and voltage levels as indicated.

Intrusion Warning

This setting enables or disables the chassis intrusion feature, which you may want to use to protect your system. The options are "Enabled" and "**Disabled**".

Clear Intrusion Status

After being notified of a chassis intrusion, enable this setting to clear the condition. If you don't, the Voice BIOS will notify you of a chassis intrusion everytime the system boots. The options are "Enabled" and "**Disabled**".

CPU Warning Temperature

This allows you to set the CPU warning temperature. If the CPU temperature reaches this threshold, an alarm will activate and a warning message will be displayed onscreen. The options are "Disabled", "60^0C/140^0F", "65^0C/149^0F", "70^0C/158^0F", "75^0C/167^0F", "80^0C/176^0F" and "**85^0C/185^0F**".

4-4.5   Processor & Over-Clock

When the item "Processor Features" is highlighted, hit the <Enter> key to activate the sub-menu shown below:
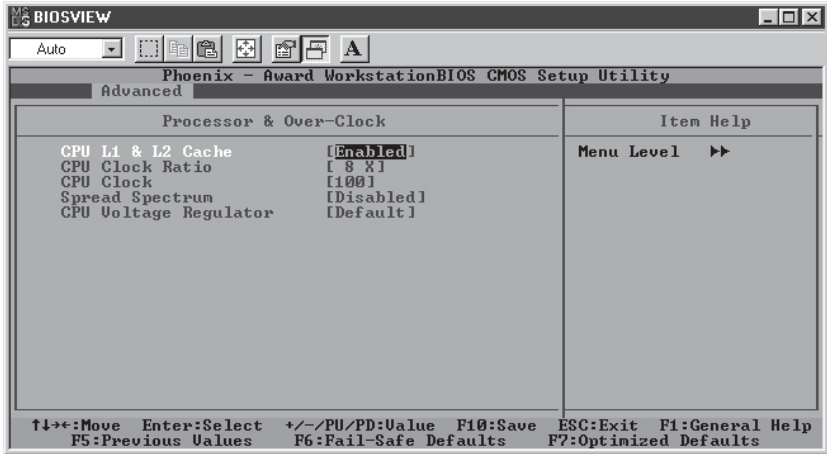
CPU L1 & L2 Cache

Set this option to "Enable" to activate the function of CPU L1 and L2 cache. The settings are "Disabled" and "**Enabled**".

CPU Clock Ratio

This item allows the user to change the CPU/Clock ratio. Key in any whole number between (and including) 8 and 50. The default setting is **"8x**".

CPU Clock

Key in a number between 100 and 165 to set the CPU clock (MHz). Supermicro does not recommend or make any guarantees with CPU overclocking.

Spread Spectrum

Spread Spectrum is a technique used to stabilize operation when a system is being affected by electromagnetic interference. The options for this setting are +/- 0.25%", "- 0.5%", "+/- 0.5%", "+/- 0.38%" and "**Disabled**".

CPU Voltage Regulator

If you overclock your CPU, it may be necessary to increase the voltage supplied to the CPU. This setting allows you to do this. The options are "-0.050V", "-0.025V", "**Default**", "+0.025V", "+0.050V" "+0.100V", "+0.150V" and "0.200V". Supermicro does not recommend or make any guarantees with CPU overclocking.

4-5   PCI/PnP Configurations

Choose PCI/PnP Configurations from the Award BIOS main menu with the Left/Right arrow keys. You should see the following display:
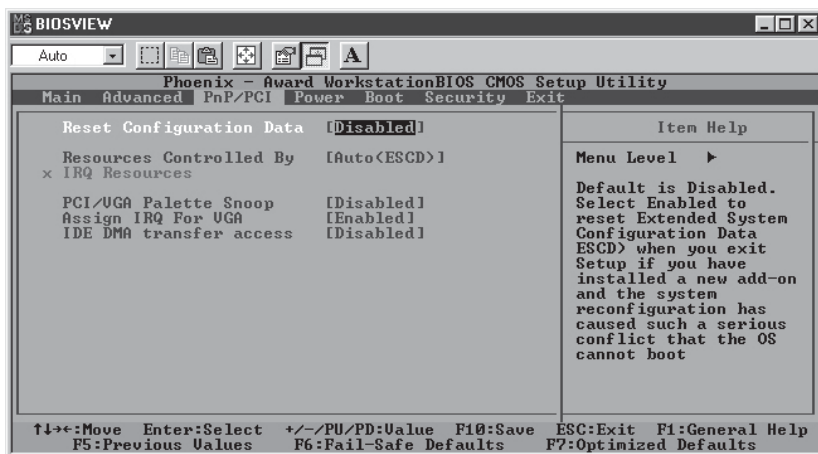
Reset Configuration Data

Enabling this setting resets the extended system configuration data when you exit setup. Do this when you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the OS cannot reboot the system. The options are "Enabled" and "**Disabled**".

Resources Controlled By

This setting allows BIOS to automatically configure all boot and Plug and Play compatible devices. If you choose Auto, you cannot select the IRQ, DMA and memory base address fields, because BIOS automatically assigns them. The options are "**Auto <ESCD>**" and "Manual".

PCI/VGA Palette Snoop

For best system performance, this item has been pre-set to "Disabled" by the manufacturer. The settings are "Enabled" and "**Disabled**".

Assign IRQ for VGA

This setting allows you to assign an IRQ (Interrupt Request) for the VGA. The settings are **"Enabled"** and "Disabled".

IDE DMA Transfer Access

This setting is to allow access to UDMA when running in DOS mode. The settings are "Enabled" and "**Disabled**".

4-6   Power

Choose Power from the Award BIOS main menu with the Left/Right arrow keys. You should see the following display:
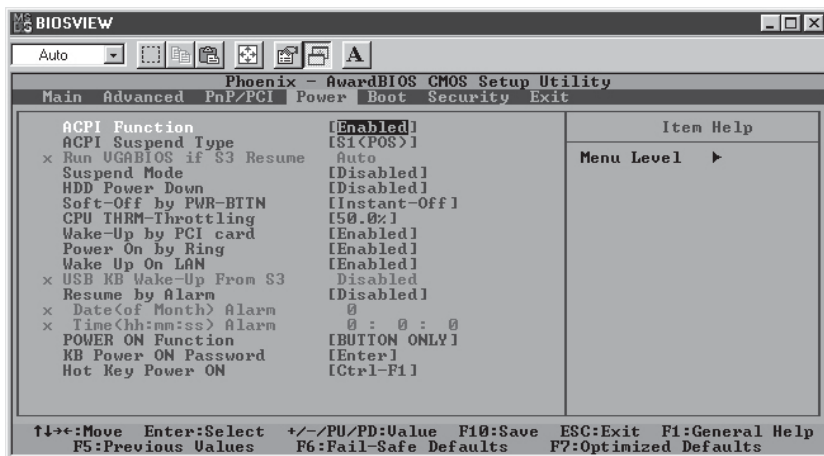
ACPI Function

This item allows you to enable and disable the ACPI (Advanced Configuration and Power Management) program. The options are "**Enabled**" and "Disabled".

ACPI Suspend Type

This item allows the user to determine the ACPI Suspend type. The options are "**S1 (POS)**", "S3 (STR)" and "S1&S3". The "S1&S3" setting should be used only with Windows XP operating systems.

Run VGABIOS if S3 Resume

This item will only be changeable if the ACPI Suspend Type (above) is set to S3. Enabling this will initiate the VGA BIOS for the S3 mode. Options are "**Auto**", "Yes" and "No".

Suspend Mode

This item sets the period of time that passes before the system goes into suspend mode. The options are "**Disabled**", "1min", "2min", "4min", "8min", "12min", "20min", "30min", "40min" and "1hr".

HDD Power Down

This item sets the period of time that passes before the hard drive(s) is powered down. The options are "**Disabled**", "1min", "2min", "3min", "4min", "5min", "6min", "7min", "8min", "9min", "10min", "11min", "12min" "13min", "14min" and "15min".

Soft-Off by PWR-BTTN

This item determines the system's "Soft-off" mode when the user presses the power-button. The options for this setting are "**Instant-Off**" and "Delay 4 sec".

CPU THRM-Throttling

THRM throttling is used to lower power consumption and reduce the heat gererated by the CPU. The options for this setting are "87.5%", "75%", "62.5%", "**50%**", "37.5%", "25%' and "12.5%'.

Wake-Up by PCI Card

If Enabled, the user is able to "wake up" the system via a PCI card. The settings are "**Enabled**" and "Disabled".

Power On by Ring

If Enabled, the user is able to "wake up" the system via the modem. The options for this setting are "**Enabled**" and "Disabled".

Wake Up on LAN

If Enabled, the user is able to "wake up" the system via the LAN. The options for this setting are "**Enabled**" and "Disabled".

USB KB Wake Up From S3

This item is only changeable if the ACPI Suspend Type (above) is set to S3. Enabling this will allow you to wake-up the system by depressing a button on a USB-connected keyboard. Options are "Enabled" and "**Disabled**".

Resume by Alarm

If Enabled, this setting will allow the AwardBIOS to turn on the system via the real-time clock (RTC). The options are "Enabled" and "**Disabled**".

Date (of Month) Alarm

Set the date for RTC alarm wake up in this field.

Time (hh:mm:s) Alarm

Set the time for RTC alarm wake up in this field.

POWER ON Function

This setting allows the user to determine the method by which the system power is activated. The options are "Password", "Hot Key", "Mouse Left", "Mouse Right", "Any Key", "**Button Only**" and "Keyboard 98".

KB Power On Password

This setting allows the user to set the password to activate the Power On function through the keyboard. Press <Enter> to enter the password.

Hot Key Power On

This option allows the user to set the hot key to activate the power on function. The settings are "**Ctrl F1**", "Ctrl F2", "Ctrl F3", "Ctrl F4", "Ctrl F5", "Ctrl F6", "Ctrl F7", "Ctrl F8", "Ctrl F9", "Ctrl F10", "Ctrl F11" and "Ctrl F12".

4-7   Boot

Choose Boot from the Award BIOS main menu with the Left/Right arrow keys. You should see the following display:
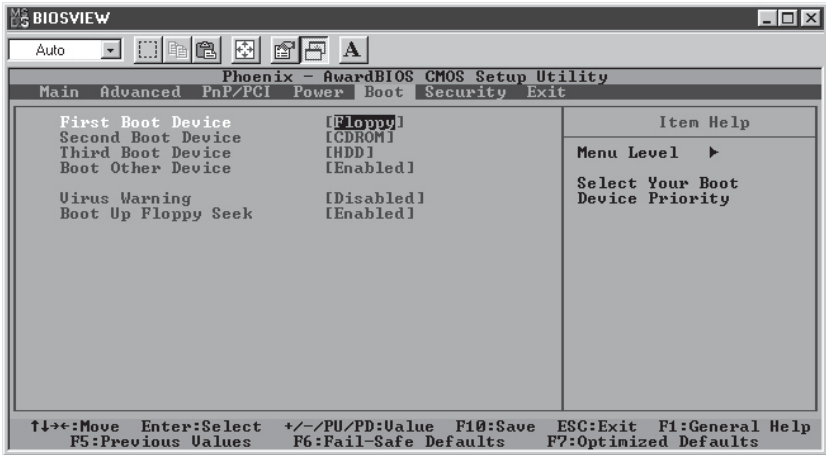
Award BIOS attempts to load the operating system from devices specified by the users in a user-specified sequence.

First Boot Device

This item allows the user to set the first boot-up device. The options are "**Floppy**", "LS120", "HDD", "SCSI", "CDROM", "ZIP100", "USB-FDD", "USB-ZIP", "USB-CDROM", "USB-HDD", "LAN" and "Disabled".

Second Boot Device

This item allows the user to set the second boot-up device. The options are "Floppy", "LS120", "HDD", "SCSI", "**CDROM**", "ZIP100", "USB-FDD", "USB-ZIP", "USB-CDROM", "USB-HDD", "LAN" and "Disabled".

Third Boot Device

This item allows the user to set the third boot-up device. The options are "Floppy", "LS120", "**HDD**", "SCSI", "CDROM", "ZIP100", "USB-FDD", "USB-ZIP", "USB-CDROM", "USB-HDD", "LAN" and "Disabled".

Boot Other Device

If enabled, this option enables the BIOS to load the OS from another device rather than the ones that have been specified as the first, second and third boot up devices. The settings are "**Enabled**" and "Disabled".

Virus Warning

This item allows the user to choose the VIRUS Warning feature for IDE Hard Drive Disk boot sector protection. If this function is enabled and someone attempts to write data into this area, the BIOS will display a warning message and an alarm will sound. The settings are "Enabled", and "**Disabled**".

Boot Up Floppy Seek

Set this option to "Enabled" to allow the BIOS to test floppy drives to determine whether they have 40 tracks or 80 tracks. The settings are "**Enabled**" or "Disabled".

4-8 Security

Choose Security from the Award BIOS main menu with the Left/Right arrow keys. You should see the following display:
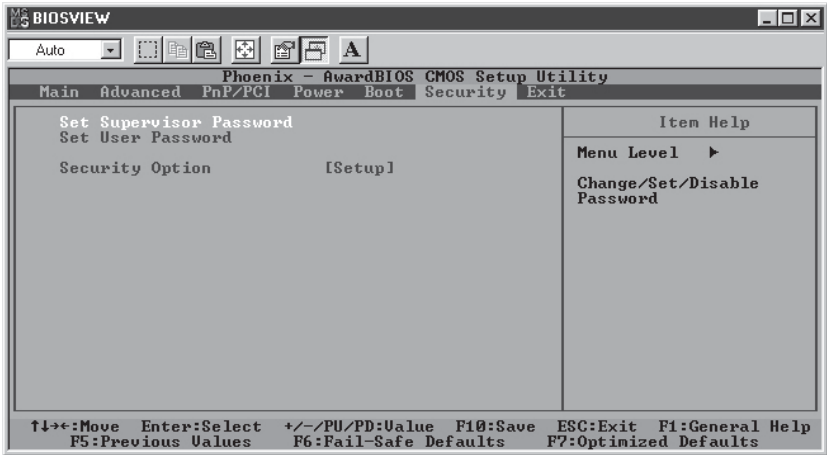
Set Supervisor Password

When the item "Set Supervisor Password" is highlighted on the above screen, press the <Enter> key. When prompted, type the Supervisor Password in the dialogue box to set or to change the Supervisor Password.

Set User Password

When the item "Set User Password" is highlighted on the above screen, press the <Enter> key. When prompted, type the User Password in the dialogue box to set or to change the User Password.

Security Option

This setting allows the user to determine if the password is required every time when the system boots up or if the password is required only when you enter the CMOS setup. The options are "System" and "**Setup**".

4-9   Exit

Choose Exit from the Award BIOS main menu with the Left/Right arrow keys. You should see the following display:
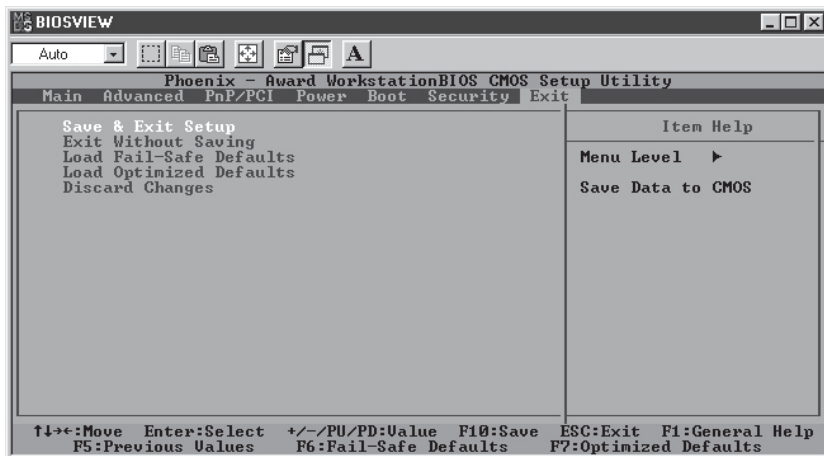
Save & Exit Setup

When the item "Save & Exit Setup" is highlighted, press <Enter> to save the changes you've made in the BIOS program (CMOS) and exit. Your system should, then, continue with the boot-up procedure.

Exit without Saving

When the item "Exit without Saving" is highlighted, press <Enter> to exit the Setup routine without saving any changes you may have made. Your system should then continue with the boot-up procedure.

Load Fail-Safe Defaults

Highlight this item and hit <Enter> to load the default settings for all items in the BIOS Setup. These are the safest settings to use.

Load Optimized Defaults

Highlight this item and hit <Enter> to load the optimized settings for all items in the BIOS Setup. These settings provide you with optimal system performance.

Discard Changes

When the item "Discard Changes" is highlighted, press <Enter> to discard any changes you made to the BIOS settings and to stay in BIOS Setup. Your system should then continue with the boot-up procedure.

Appendix A
AwardBIOS POST Messages

During the Power-On Self-Test (POST), the BIOS will check for errors. If an error is found and a correction is needed, the BIOS will activate an alarm or display a message.

If a message is displayed, it will be accompanied by the following:

PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP

POST Beep Codes

Currently, there are two kinds of beep codes used in AwardBIOS. One code indicates that a video error has occurred and that the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps. The other code indicates that a Rambus error has occurred. This beep code consists of a single long beep that sounds repeatedly.

Error Messages

One or more of the following messages may be displayed if the BIOS detects an error during the POST. This list includes messages for both the ISA and the EISA BIOS.

CMOS BATTERY HAS FAILED

The CMOS battery is no longer functional. It should be replaced.

CMOS CHECKSUM ERROR

The CMOS hecksum is incorrect. This can indicate that CMOS has been corrupted. This error may have been caused by a weak battery. Check the battery and replace if necessary.

DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. This could mean that either a boot drive was not detected or the drive does not contain the proper system boot files. Insert a system disk into Drive A: and press <Enter>. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also make sure the disk has been formatted as a boot device. Then reboot the system.

DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP

The type of diskette drive installed in the system is different from the CMOS definition. Run Setup to reconfigure the drive type correctly.

DISPLAY SWITCH IS SET INCORRECTLY

The display switch on the motherboard can be set to either monochrome or color. This indicates that the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper or enter Setup and change the VIDEO selection.

DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

ERROR ENCOUNTERED INITIALIZING HARD DRIVE

The hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize the controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup. Also check to see if any jumper needs to be set correctly on the hard drive.

FLOPPY DISK CNTRLR ERROR OR NO CNTRLR PRESENT

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during boot up.

If you are intentionally configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

Memory Address Error at ...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

Memory parity Error at ...

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

Memory Verify Error at ...

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

OFFENDING SEGMENT:

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

PRESS A KEY TO REBOOT

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

PRESS F1 TO DISABLE NMI, F2 TO REBOOT

When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot, or you can reboot the system with the NMI enabled.

RAM PARITY ERROR - CHECKING FOR SEGMENT ...

Indicates a parity error in Random Access Memory.

SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT ...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

FLOPPY DISK(S) fail (80) → **Unable to reset floppy subsystem.**

FLOPPY DISK(S) fail (40) → **Floppy Type dismatch.**

Hard Disk(s) fail (80) → **HDD reset failed**

Hard Disk(s) fail (40) → **HDD controller diagnostics failed.**

Hard Disk(s) fail (20) → **HDD initialization error.**

Hard Disk(s) fail (10) → **Unable to recalibrate fixed disk.**

Hard Disk(s) fail (08) → **Sector Verify failed.**

Keyboard is locked out - Unlock the key.

BIOS detect the keyboard is locked. P17 of the keyboard controller is pulled low.

Keyboard error or no keyboard present.

Cannot initialize the keyboard. Make sure that the keyboard is attached correctly and no keys are being pressed during the boot.

Manufacturing POST loop.

System will repeat POST procedure infinitely while the P15 of keyboard controller is pulled low. This is also used for M/B burn in testing.

BIOS ROM checksum error - System halted.

The checksum of ROM address F0000H-FFFFFH is bad.

Memory test fail..

BIOS reports the a memory test fail if the onboard memory has an error.

Appendix B
AwardBIOS POST Codes

This section lists the POST (Power On Self Testing) Codes for the Award BIOS.

POST (hex)	Description
CFh	Test CMOS R/W functionality.
C0h	Early chipset initialization: -Disable shadow RAM -Disable L2 cache (socket 7 or below) -Program basic chipset registers
C1h	Detect memory -Auto-detection of DRAM size, type and ECC. -Auto-detection of L2 cache (socket 7 or below)
C3h	Expand compressed BIOS code to DRAM
C5h	Call chipset hook to copy BIOS back to E000 & F000 shadow RAM.
0h1	Expand the Xgroup codes locating in physical address 1000:0
02h	Reserved
03h	Initial Superio_Early_Init switch.
04h	Reserved
05h	1. Blank out screen 2. Clear CMOS error flag
06h	Reserved
07h	1. Clear 8042 interface 2. Initialize 8042 self-test
08h	1. Test special keyboard controller for Winbond 977 series Super I/O chips. 2. Enable keyboard interface.
09h	Reserved
0Ah	● Disable PS/2 mouse interface (optional). ● Auto detect ports for keyboard & mouse followed by a port & interface swap (optional). ● Reset keyboard for Winbond 977 series Super I/O chips.
0Bh	Reserved
0Ch	Reserved
0Dh	Reserved
0Eh	Test F000h segment shadow to see whether it is R/W-able or not. If test fails, keep beeping the speaker.

Debuggig LED Encoding

Post Code	Encoded LED	Task
C1h	001b	Memory Detection
05h	010b	BIOS Shadowing
07h	011b	KBC Initialization
0Eh	100b	Shadow RAM test
14h	101b	Chipset defaults loaded
26h	110b	Clock generator configured
2Bh	111b	Video initialization
52h	000b	Just clear LEDs

POST (hex)   **Description**

POST (hex)	Description
0Fh	Reserved
10h	Auto detect flash type to load appropriate flash R/W codes into the run time area in F000 for ESCD & DMI support.
11h	Reserved
12h	Use walking 1's algorithm to check out interface in CMOS circuitry. Also set real-time clock power status, and then check for override.
13h	Reserved
14h	Program chipset default values into chipset. Chipset default values are MODBINable by OEM customers.
15h	Reserved
16h	Initial Early_Init_Onboard_Generator switch.
17h	Reserved
18h	Detect CPU information including brand, SMI type (Cyrix or Intel) and CPU level (586 or 686).
19h	Reserved
1Ah	Reserved
1Bh	Initial interrupts vector table. If no special specified, all H/W interrupts are directed to SPURIOUS_INT_HDLR & S/W interrupts to SPURIOUS_soft_HDLR.
1Ch	Reserved
1Dh	Initial EARLY_PM_INIT switch.
1Eh	Reserved
1Fh	Load keyboard matrix (notebook platform)
20h	Reserved
21h	HPM initialization (notebook platform)
22h	Reserved
23h	1. Check validity of RTC value: e.g. a value of 5Ah is an invalid value for RTC minute. 2. Load CMOS settings into BIOS stack. If CMOS checksum fails, use default value instead. 3. Prepare BIOS resource map for PCI & PnP use. If ESCD is valid, take into consideration of the ESCD's legacy information. 4. Onboard clock generator initialization. Disable respective clock resource to empty PCI & DIMM slots. 5. Early PCI initialization: -Enumerate PCI bus number -Assign memory & I/O resource -Search for a valid VGA device & VGA BIOS, and put it into C000:0.
24h	Reserved
25h	Reserved
26h	Reserved
27h	Initialize INT 09 buffer
28h	Reserved
29h	• Program CPU internal MTRR (P6 & PII) for 0-640K memory address. • Initialize the APIC for Pentium class CPU. • Program early chipset according to CMOS setup. Example: onboard IDE controller. • Measure CPU speed. • Invoke video BIOS.
2Ah	Reserved
2Bh	Reserved
2Ch	Reserved

POST (hex)	Description
2Dh	1. Initialize multi-language 2. Put information on screen display, including Award title, CPU type, CPU speed ….
2Eh	Reserved
2Fh	Reserved
30h	Reserved
31h	Reserved
32h	Reserved
33h	Reset keyboard except Winbond 977 series Super I/O chips.
34h	Reserved
35h	Reserved
36h	Reserved
37h	Reserved
38h	Reserved
39h	Reserved
3Ah	Reserved
3Bh	Reserved
3Ch	Test 8254
3Dh	Reserved
3Eh	Test 8259 interrupt mask bits for channel 1.
3Fh	Reserved
40h	Test 8259 interrupt mask bits for channel 2.
41h	Reserved
42h	Reserved
43h	Test 8259 functionality.
44h	Reserved
45h	Reserved
46h	Reserved
47h	Initialize EISA slot
48h	Reserved
49h	1. Calculate total memory by testing the last double word of each 64K page. 2. Program writes allocation for AMD K5 CPU.
4Ah	Reserved
4Bh	Reserved
4Ch	Reserved
4Dh	Reserved
4Eh	1. Program MTRR of M1 CPU 2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable range. 3. Initialize the APIC for P6 class CPU. 4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical.
4Fh	Reserved
50h	Initialize USB
51h	Reserved
52h	Test all memory (clear all extended memory to 0)
53h	Reserved
54h	Reserved
55h	Display number of processors (multi-processor platform)
56h	Reserved
57h	1. Display PnP logo 2. Early ISA PnP initialization -Assign CSN to every ISA PnP device.

POST (hex)   **Description**

58h	Reserved
59h	Initialize the combined Trend Anti-Virus code.
5Ah	Reserved
5Bh	(Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)
5Ch	Reserved
5Dh	1. Initialize Init_Onboard_Super_IO switch. 2. Initialize Init_Onboard_AUDIO switch.
5Eh	Reserved
5Fh	Reserved
60h	Okay to enter Setup utility; i.e. not until this POST stage can users enter the CMOS setup utility.
61h	Reserved
62h	Reserved
63h	Reserved
64h	Reserved
65h	Initialize PS/2 Mouse
66h	Reserved
67h	Prepare memory size information for function call: INT 15h ax=E820h
68h	Reserved
69h	Turn on L2 cache
6Ah	Reserved
6Bh	Program chipset registers according to items described in Setup & Auto-configuration table.
6Ch	Reserved
6Dh	1. Assign resources to all ISA PnP devices. 2. Auto assign ports to onboard COM ports if the corresponding item in Setup is set to "AUTO".
6Eh	Reserved
6Fh	1. Initialize floppy controller 2. Set up floppy related fields in 40:hardware.
70h	Reserved
71h	Reserved
72h	Reserved

73h	(Optional Feature)
	Enter AWDFLASH.EXE if :
	    -AWDFLASH is found in floppy drive.
	    -ALT+F2 is pressed

74h	Reserved
75h	Detect & install all IDE devices: HDD, LS120, ZIP, CDROM…..
76h	Reserved
77h	Detect serial ports & parallel ports.
78h	Reserved
79h	Reserved
7Ah	Detect & install co-processor
7Bh	Reserved
7Ch	Reserved
7Dh	Reserved
7Eh	Reserved

7Fh	1. Switch back to text mode if full screen logo is supported.
	    -If errors occur, report errors & wait for keys
	    -If no errors occur or F1 key is pressed to continue:
	      ◆Clear EPA or customization logo.

POST (hex)   **Description**

POST (hex)	Description
80h	Reserved
81h	Reserved
82h	1. Call chipset power management hook. 2. Recover the text font used by EPA logo (not for full screen logo) 3. If password is set, ask for password.
83h	Save all data in stack back to CMOS
84h	Initialize ISA PnP boot devices
85h	1. USB final Initialization 2. NET PC: Build SYSID structure 3. Switch screen back to text mode 4. Set up ACPI table at top of memory. 5. Invoke ISA adapter ROMs 6. Assign IRQs to PCI devices 7. Initialize APM 8. Clear noise of IRQs.
86h	Reserved
87h	Reserved
88h	Reserved
89h	Reserved
90h	Reserved
91h	Reserved
92h	Reserved
93h	Read HDD boot sector information for Trend Anti-Virus code
94h	1. Enable L2 cache 2. Program boot up speed 3. Chipset final initialization. 4. Power management final initialization 5. Clear screen & display summary table 6. Program K6 write allocation 7. Program P6 class write combining
95h	1. Program daylight saving 2. Update keyboard LED & typematic rate
96h	1. Build MP table 2. Build & update ESCD 3. Set CMOS century to 20h or 19h 4. Load CMOS time into DOS timer tick 5. Build MSIRQ routing table.
FFh	Boot attempt (INT 19h)

Notes

Appendix C
AwardBIOS Error Beep Codes

This section lists the Award BIOS Error Beep Codes.

Beep Code	Error Message Description
1 short beep	System boot.
2 short beeps	Incorrect CMOS setting.
1 long + 1 short	DRAM error.
1 long + 2 short	VGA error.
1 long + 3 short	Keyboard error.
1 long + 9 short	ROM error.
Long beeps	Memory module error.
High beeps	Power error.

Notes