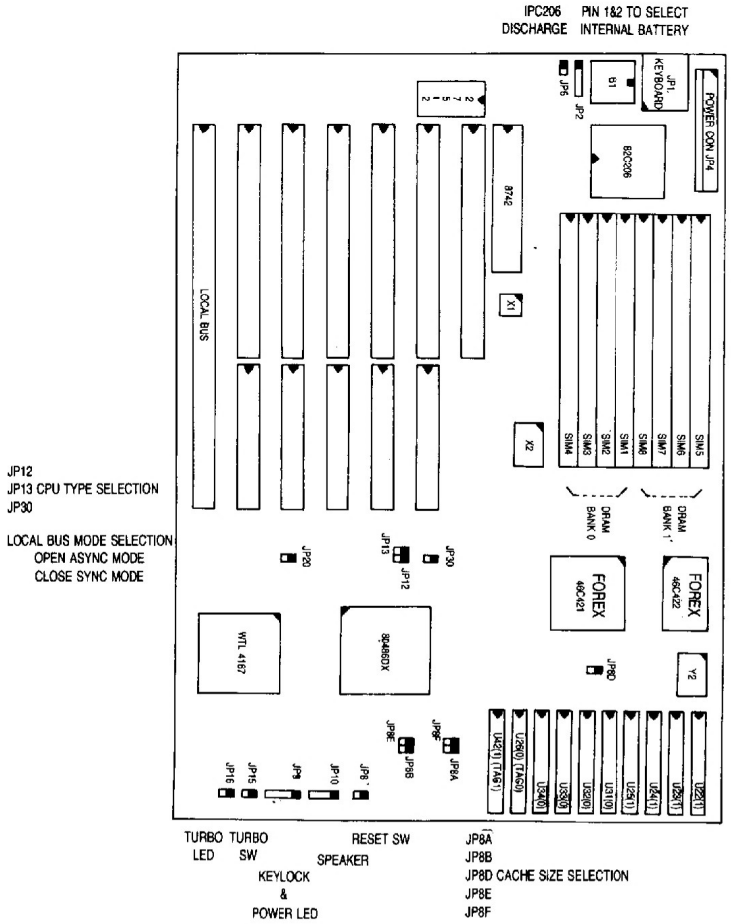

CONNECTORS

A connector is two or more pins that are used to make connections to the system standard accessories (such as power. battery, etc.) The following is a list of connectors on board, as well as descriptions of each individual connector.

(A) B1 /Rechargeable Battery

Pin #	Assignment
1	Battery Positive
2	Ground

(B) JP1 Keyboard

Pin #	Assignment
1	Keyboard Clock
2	Keyboard Data
3	No Connection
4	Ground
5	+ 5V DC

(C) JP2 3.6V External Battery

Pin #	Assignment
1	Battery Positive
2	Internal Battery Positive
3	Ground
4	Ground

Note: If EXT Battery is not installed, close pin 1 & 2 of JP2 to select internal Battery

(D) JP4 Power Connector

Pin #	Assignment
1	POWER GOOD
2	+5V DC
3	+12V DC
4	-12V DC
5	Ground
6	Ground
7	Ground
8	Ground
9	-5V DC
10	+5V DC
11	+5V DC
12	+5V DC

(E) JP16 Turbo LED

Pin #	Assignment
1	Pullup (+5V DC)
2	Turbo control
3	Pullup (+5V DC)
4	Turbo control

4

(F) JP8 Reset Switch

Pin #	Assignment	Pin 1 & 2	Function
1	Reset control	Open	No action
2	Ground	Closed	Reset

(G) JP9 Front Panel Key Lock and Power LED

Pin #	Assignment
1	Power LED
2	No Connection
3	Ground
4	Keyboard Lock
5	Ground

(H) JP10 Speaker

Pin #	Assignment
1	Speaker Data
2	No Connection
3	Ground
4	+5V DC

(i) JP15 Turbo Switch

Pin #	Assignment	Pin 1 & 2	Function
1	Pullup (+5V DC)	Open	Non-Turbo
2	Turbo Control	Closed	Turbo

JUMPERS

A jumper is two or three pins which may or may, not be covered by a plastic connector plug. A jumper is used to select different system options.

(A) JP5

(B) JP8A, 8B, 8D, 8E, 8F Cache Memory Size Select

	Pin #	Function
JP8A & D	f1-2	128K cache memory
	2-3	64K/256K cache memory
JP8B & E	Pin #	Function
	1-2	128K/256K cache memory
	2-3	64K cache memory
JP8F	Pin #	Function
	1-2	256K cache memory
	2-3	64K/128K cache memory

(C) JP12-13, JP30CPU Selection

	Pin #	Function
JP12-13	1-2	486DX/DX2 CPU or 487SX CPU
	2-3	486SX
JP30	Pin #	Function
	1-2	486DX/DX2 CPU
	2-3	486SX/487SX

(D)

JP20Local Bus Mode Selection

Pin #	Function
1-2	Closed-to select SYNC mode
1-2	Open-to select ASYNC mode

SYSTEM SETUP FOR FOREX 486SX/DX Rev.B1A MB.

	486SX-20	486SX-25 487SX-25 486DX2-50	486SX-33 486DX-33 486DX2-66	486DX-50
Y2 OSC	20MHz	25MHz	33MHz	50MHz
AT-BUS CLOCK SPEED	CLK2IN/3	CLK2IN/3	CLK2IN/4	CLK2IN/6
DRAM READ WAIT STATE	2 W/S	2 W/S	3 W/S	3 W/S
DRAM WRITE WAIT STATE	0 W/S	1 W/S	1 W/S	1 W/S
CACHE READ WAIT STATE	0 W/S	0 W/S	1 W/S	1 W/S
CACHE WRITE WAIT STATE	0 W/S	0 W/S	1 W/S	1 W/S

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