

MB-8500TEC

· **Supported CPUs: Intel Pentium (No MMX) Only!**

MB version 1 or 2 provide jumpers jp6/jp7/jp16 and only support Regular Intel Pentium 75/90/100/120/133MHz CPUs (No MMX, AMD, nor Cyrix CPUs).

MB version 3 provide jumpers jp6/jp7/jp15/jp16 and support Regular Intel Pentium 75/90/100/120/133/150/166MHz CPUs (No MMX, AMD, nor Cyrix CPUs).

JP4 CMOS Function Select

OPEN	To maintain setup and extended setup data in CMOS for normal function (default).
CLOSED	To clear CMOS setup memory (e.g. remove password).
Please follow the procedure below to CLEAR BIOS Password, if you password forgotten	
STEP 1.	POWER OFF
STEP 2.	JP4 CLOSED
STEP 3.	POWER ON
STEP 4.	AFTER MEMORY COUNT FINISHED, POWER OFF
STEP 5.	JP4 OPEN
STEP 6.	POWER ON
STEP 7.	THE PASSWORD SHOULD BE CLEARED AT THIS POINT

JP8 Secondary IDE IRQ Select

1-2 Closed	IRQ 15
2-3 Closed	IRQ thru PCI

JP9 Primary IDE IRQ Select

Closed	IRQ 14
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JP21 ISA Clock Select

JUMPER No.	For 75MHz ISA Clock= PCI Clock/3	For 90/100/120/133MHz ISA Clock = PCI Clock/4
JP21	1-2 Closed	2-3 Closed

CPU Installation/ Jumper Settings

Note: 1. JP15 jumper can only be found in PCB Ver.3 and afterward.

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CPU Clock	JP6	JP7	JP16	JP15
75MHz	OPEN	OPEN	OPEN	OPEN

90MHz	OPEN	CLOSED	OPEN	OPEN
100MHz	CLOSED	CLOSED	OPEN	OPEN
120MHz	OPEN	CLOSED	CLOSED	OPEN
133MHz	CLOSED	CLOSED	CLOSED	OPEN
150MHz	OPEN	CLOSED	CLOSED	CLOSED
166MHz	CLOSED	CLOSED	CLOSED	CLOSED

J8 Connectors

PIN No.	Assignment	Function
1	Speaker	Speaker Connector
2	No Connection	
3	Ground	
4	+5 V dc	
5	Power LED (+)	Power LED & Key lock
6	No Connection	
7	Ground	
8	Keyboard	
9	Ground	
10	Turbo LED (+)	Turbo LED
11	Turbo LED (-)	
12	Reset Control	Reset
13	Ground	
14	Turbo Control	No
15	Ground	
16	No Connection	
17	+5V dc	+5V, Ground
18	Ground	

JP17 HDD Active Indicator (LED) Connector

Pin 1	Negative (-)
Pin 2	Positive (+)

DRAM Installation

NOTE: 1. 70ns Fast Page mode module recommended!!

Total Memory Size (MB)	Bank 0 (SIMM1-SIMM2)	Bank 1 (SIMM3-SIMM4)
8 MB	4 MB x 2 PCS	-
16 MB	8 MB x 2PCS	-
32 MB	16 MB x 2PCS	-
64 MB	32MB x 2PCS	-
16 MB	4MB x 2PCS	4MB x 2PCS
24 MB	4MB x 2PCS	8MB x 2PCS
32 MB	8MB x 2PCS	8MB x 2PCS
40 MB	4MB x 2PCS	16MB x 2PCS
48 MB	8MB x 2PCS	16MB x 2PCS
64 MB	16MB x 2PCS	16MB x 2PCS
72 MB	4MB x 2PCS	32MB x 2PCS
80 MB	8MB x 2PCS	32MB x 2PCS
96 MB	16MB x 2PCS	32MB x 2PCS
128 MB	32MB x 2PCS	32MB x 2PCS

Extra official info

(by Dusko Marincic)

<https://www.vogons.org/viewtopic.php?f=46&t=85626&p=1032458#p1032458>

(Note: "Current State" means how my board is currently setup)

JP1: No Info

JUMPER No.	Pins	Current State
JP1	4	3-4 Closed

JP2: No Info

JUMPER No.	Pins	Current State
JP2	4	3-4 Closed

JP5: No Info

(might be the Bios voltage as per continuity test: 5v / 12v)

JUMPER No.	Pins	Current State
JP5	3	1-2 Closed (5V)

JP10: Tage Size

(Tage? | As per chart printed on the board)

JUMPER No.	Pins	256K 8K8/16K8/32K8	512K 16K8 or 32K8
JP10	3	2-3 Closed (Current State)	1-2 Closed

JP11: No Info

JUMPER No.	Pins	Current State
JP11	2	Open

JP12 / JP13: L2 Cache Size

(As per chart printed on the board)

L2 Size	JP12	JP13
256KB	1-2 (Current State)	2-3 (Current State)
512KB	2-3	1-2
No Cache	1-2	1-2

JP14: Pipeline, as per chart printed on the board

(JP14 not found)

JUMPER No.	Pins	Disabled	Enabled
JP14	2	Open	Closed

JP19: No Info

JUMPER No.	Pins	Current State
JP19	6	Open

JP20: No Info

(might be CPU Voltage)

JUMPER No.	Pins	Current State
JP20	4	Hard Wired

JP22: No Info

JUMPER No.	Pins	Current State
JP22	3	1-2

JP27: No Info

JUMPER No.	Pins	Current State
JP27	3	1-2

