# M6TLG

## 1.5.2 CPU Clock Ratio Select (JP3)
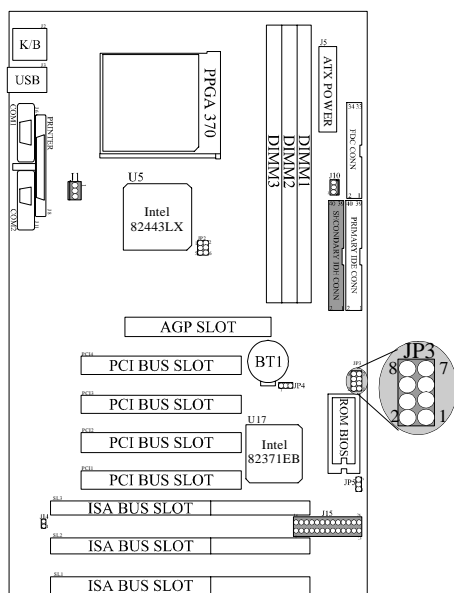


| JP3 | 1-2 | 3-4 | 5-6 | 7-8 |
|------|-------|-------|-------|-------|
| X 2.5 | OPEN | CLOSE | CLOSE | CLOSE |
| X 3.0 | CLOSE | OPEN | CLOSE | CLOSE |
| X 3.5 | OPEN | OPEN | CLOSE | CLOSE |
| X 4.0 | CLOSE | CLOSE | OPEN | CLOSE |
| X 4.5 | OPEN | CLOSE | OPEN | CLOSE |
| X 5.0 | CLOSE | OPEN | OPEN | CLOSE |
| X 5.5 | OPEN | OPEN | OPEN | CLOSE |
| X 6.0 | CLOSE | CLOSE | CLOSE | OPEN |
| X 6.5 | OPEN | CLOSE | CLOSE | OPEN |
| X 7.0 | CLOSE | OPEN | CLOSE | OPEN |
| X 7.5 | OPEN | OPEN | CLOSE | OPEN |
| X 8.0 | CLOSE | CLOSE | OPEN | OPEN |

# M6TLG

## Parallel Interface Ports

Unlike the serial port, parallel interface ports have been standardized and should not present any difficulty interfacing peripherals to your system. Sometimes called a Centronics port, the parallel port is almost exclusively used with printers. The parallel port on you system has a 25-pin, DB5 connector (see picture below). The pinouts for the parallel port are shown in the table below.
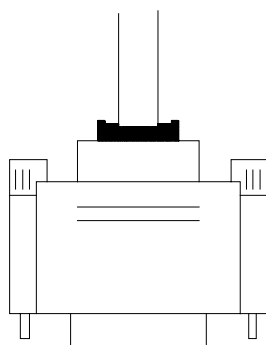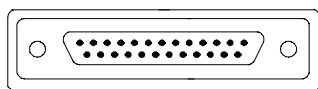
| Signal | Pin |
|---|---|
| -Strobe | 1 |
| Data 0 | 2 |
| Data 1 | 3 |
| Data 2 | 4 |
| Data 3 | 5 |
| Data 4 | 6 |
| Data 5 | 7 |
| Data 6 | 8 |
| Data 7 | 9 |
| -Ack | 10 |
| Busy | 11 |
| Paper Empty | 12 |
| +Select | 13 |
| -Auto FDXT | 14 |
| -Error | 15 |
| -Init | 16 |
| -SLCTN | 17 |
| Ground | 18 |
| Ground | 19 |
| Ground | 20 |
| Ground | 21 |
| Ground | 22 |
| Ground | 23 |
| Ground | 24 |
| Ground | 25 |

### DRAM Data Integrity Mode

Select Non-ECC or ECC(error-correcting code), according to the type of installed DRAM. The settings are Non-ECC or ECC.

**Non-ECC**

### Video RAM Cacheable

Same as system BIOS Cacheable.

### Memory Hole At 15M-16M

Enabling this features reserves 15MB to 16MB memory address space to ISA expansion cards that specifically require this setting. This makes the memory form 15MB and up unavailable to the system. Expansion cards can only access memory up to 16MB. The default is Disabled.

## AGP Aperture Size (MB)

Select the size of Accelerated Graphics Port(AGP) aperture. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation.
The Choice : 4M, 8M, 16M, 32M, 64M, 128M, 256M.

## SDRAM RAS-to-CAS Delay

This field lets you insert a timing delay between the CAS and RAS strobe signals, used when DRAM is written to, read form, or refreshed. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system.

## SDRAM RAS Precharge Time

If an insufficient number of cycles is allowed for the RAS accumulate its charge before DRAM refresh, the refresh may be incomplete and the DRAM may fail to retain data. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system.

## SDRAM CAS latency Time

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not restet this field form the default value specified by the system designer.

## CPU Host Clock

The setting are Hardware, 66MHz, 68MHz, 75MHz.

**Hardware (default)**

# 2.5 Power Management Setup

■ **Figure 5. Power Management Setup Menu**

ROM PCI/ISA BIOS (xxxxxxxx)
POWER MANAGEMENT SETUP
AWARD SOFTWARE, INC.

```
Power Management     : Disabled    ** Reload Glodal Timer Events **
PM Control by APM    : Yes         IRW[3-7,9-15],NMI    : Enabled
Video Off Method     : DPMS        Primary IDE0         : Enabled
Video Off After      : Standby     Primary IDE1         : Enabled
MODEM Use IRQ        : 3           Secondary IDE0       : Disabled
Doze Mode            : Disabled    Secondary IDE1       : Disabled
Standby Mode         : Disabled    Floppy Disk          : Enabled
Suspend Mode         : Disabled    Serial Port          : Enabled
HDD Power Down       : Disabled    Parallel Port        : Enabled
Throttle Duty Cycle  : 62.5%       Resume by Alarm      : Disabled
PCI/VGA Act-Monitor  : Disabled
Soft-Off by PW-BTTN  : Instant-off
CPUFAN off In Suspend : Enabled
PowerOn on PCI Card  : Disabled
PowerOn by Ring      : Disabled
WakeUP On LAN        : Disabled
IRQ 8 Break Suspend  : Disabled

                                   ESC : Quit          ↑↓→←Select Item
                                   F1    : Help        PU/PD/+/- : Modify
                                   F5    : Old Values  <Shift> F2 : Color
                                   F7    : Load Setup Defaults
```

## Wake Up On LAN

To use function, you need a LAN add-on card which support power on functions. It should also support the wake-up LAN jumper

        **Disabled** (default)      Wake Up On LAN not supported.

## IRQ 8 Break Suspend

| | |
|---|---|
| **IRQ[3-7,9-15], NMI** | : Enabled |
| **Primary IDE 0** | : Enabled |
| **Primary IDE 1** | : Disabled |
| **Secondary IDE 0** | : Disabled |
| **Secondary IDE 1** | : Disabled |
| **Floppy Disk** | : Disabled |
| **Serial Port** | : Disabled |
| **Parallel Port** | : Disabled |

## Resusme by Alarm

This function is for setting date and time for your computer to boot up. During Disabled, you cannot use this function.

### Onboard Parallel Port

This item allows you to determine access onboard parallel port controller
with which I/O address.
The choice : 378/IRQ7, 278/IRQ5, 3BC/IRQ7, Disabled.

### Parallel Port Mode

Select an operating mode for the onboard parallel (printer) port. Normal
EPP (Extended Parallel Port) ECP (Extended Capabilities Port)
CEP+EPP PC AT parallel port Bi-directional port Fast, buffered port Fast,
buffered, bi-directional port.

Select Normal unless you are certain your hardware and software both
support EPP or ECP mode.
The choice : SPP, EPP1.9+SPP, ECP, ECP+EPP1.9, Normal,
              EPP1.7+SPP, ECP+EPP1.7.

02/08/1999
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