

**MSSIB**
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# Manuals

[\(click here for Diagram\)](#)

JP2	Assignment
1-2 Closed	Normal Operation
2-3 Closed	Clear CMOS Data
1-2-3 Open	Onboard Battery Disabled

**JPI CPU Voltage Selection** [\(click here for Diagram\)](#)

CPU TYPE	CPU Voltage		JP1	JP6
	CORE	I / O		
<b>Single Voltage</b>  INTEL P54C/QS/CT Cyrrix 6x86 AMD K5 IDT Win Chip	3.5V	3.5V	V0(1-2) closed V1(3-4) closed V2(5-6) closed V3(7-8) closed	open
<b>Dual Voltage</b>  INTEL P55C/MMX Cyrrix 6x86L / 6x86MX/MI AMD K6-2 / K6	2.1V	3.3V	V0(1-2) closed V1(3-4) open V2(5-6) open V3(7-8) open	open
	2.2V	3.3V	V0(1-2) open V1(3-4) closed V2(5-6) open V3(7-8) open	open
	2.3V	3.3V	V0(1-2) closed V1(3-4) closed V2(5-6) open V3(7-8) open	open
	2.8V	3.3V	V0(1-2) open V1(3-4) open V2(5-6) open V3(7-8) closed	open
	2.9V	3.3V	V0(1-2) closed V1(3-4) open V2(5-6) open V3(7-8) closed	open
	3.2V	3.3V	V0(1-2) open V1(3-4) open V2(5-6) closed V3(7-8) closed	open
	3.3V	3.3V	V0(1-2) closed V1(3-4) open V2(5-6) closed V3(7-8) closed	open
	2.2V	3.45V	V0(1-2) open V1(3-4) closed V2(5-6) open V3(7-8) open	closed

**SW1 CPU Clock Selection** [\(click here for Diagram\)](#)

<b>CPU Bus Clock</b>	* SW1(1)closed & SW1(2)open & SW1(3)open :Bus Clock = 60MHz * SW1(1)open & SW1(2)open & SW1(3)open :Bus Clock = 66MHz
<b>Multiplier</b>	* SW1(4) open & SW1(5) open & SW1(6) open : Multiplier = 1.5 * SW1(4) closed & SW1(5) open & SW1(6) open : Multiplier = 2 * SW1(4) closed & SW1(5) closed & SW1(6) open : Multiplier = 2.5 * SW1(4) open & SW1(5) closed & SW1(6) open : Multiplier = 3 * SW1(4) open & SW1(5) open & SW1(6) open : Multiplier = 3.5

**INTEL CPU**

CPU Speed	Bus Clock & Multiplier	SW1	SW1	SW1	SW1	SW1	SW1
		(1)	(2)	(3)	(4)	(5)	(6)
90MHz	60MHz x 1.5	closed	open	open	open	open	open
100MHz	66MHz x 1.5	open	open	open	open	open	open
120MHz	60MHz x 2	closed	open	open	closed	open	open
133MHz	66MHz x 2	open	open	open	closed	open	open
150MHz	60MHz x 2.5	closed	open	open	closed	closed	open
166MHz	66MHz x 2.5	open	open	open	closed	closed	open
200MHz	66MHz x 3	open	open	open	open	closed	open
233MHz	66MHz x 3.5	open	open	open	open	open	open

**Cyrrix 6x86 / 6x86L CPU**

CPU Speed	Bus Clock & Multiplier	SW1	SW1	SW1	SW1	SW1	SW1
		(1)	(2)	(3)	(4)	(5)	(6)
PR-150+120MHz	60MHz x 2	closed	open	open	closed	open	open
PR-166+133MHz	66MHz x 2	open	open	open	closed	open	open
PR-200+150MHz	* 75MHz x 2	open	closed	closed	closed	open	open

**Cyrrix 6x86MX / MII CPU**

CPU Speed	Bus Clock & Multiplier	SW1	SW1	SW1	SW1	SW1	SW1
		(1)	(2)	(3)	(4)	(5)	(6)
PR-150+120MHz	60MHz x 2	closed	open	open	closed	open	open
PR-166+133MHz	66MHz x 2	open	open	open	closed	open	open
PR-166+150MHz	60MHz x 2.5	closed	open	open	closed	closed	open
PR-200+150MHz	* 75MHz x 2	open	closed	closed	closed	open	open
PR-200+166MHz	66MHz x 2.5	open	open	open	closed	closed	open
PR-200+180MHz	60MHz x 3	closed	open	open	open	closed	open

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**BONUS**

PR-233+188MHz	* 75MHz x 2.5	open	closed	closed	closed	open	open
PR-233+200MHz	66MHz x 3	open	open	open	open	closed	open
PR-300+233MHz	66MHz x 3.5	open	open	open	open	open	open
PR-300+225MHz	* 75MHz x 3	open	closed	closed	open	closed	open
PR-333+263MHz	75MHz x 3.5	open	closed	closed	open	open	open

### AMD-K5 CPU

CPU Speed	SW1	SW1	SW1	SW1	SW1	SW1
	( 1 )	( 2 )	( 3 )	( 4 )	( 5 )	( 6 )
PR-90	closed	open	open	open	open	open
PR-100	open	open	open	open	open	open
PR-120	closed	open	open	closed	open	open
PR-133	open	open	open	closed	open	open
PR-166	open	open	open	closed	closed	open
PR-200	open	open	open	open	closed	open

### AMD-K6 CPU / AMD-K6-2

CPU Speed	Bus clock & Multiplier	SW1	SW1	SW1	SW1	SW1	SW1
		( 1 )	( 2 )	( 3 )	( 4 )	( 5 )	( 6 )
166MHz	66MHz x 2.5	open	open	open	closed	closed	open
200MHz	66MHz x 3	open	open	open	open	closed	open
233MHz	66MHz x 3.5	open	open	open	open	open	open
266MHz	66MHz x 4	open	open	open	closed	open	closed
300MHz	66MHz x 4.5	open	open	open	closed	closed	closed
333MHz	66MHz x 5	open	open	open	open	closed	closed

### IDT-Win Chip CPU

CPU Speed	Bus clock & Multiplier	SW1	SW1	SW1	SW1	SW1	SW1
		( 1 )	( 2 )	( 3 )	( 4 )	( 5 )	( 6 )
180MHz	60MHz x 3	closed	open	open	open	closed	open
200MHz	66MHz x 3	open	open	open	open	closed	open

## Connectors

### J30 Panel Connector (for PCB Ver:1.0)

Pin No.	Assignment	Function	Pin No.	Assignment	Function
1	Speaker	Speaker Connector	14	+5V	VCC Ground
2	NC		15	Ground	
3	Ground		16	NC	NC
4	+5V		17	Green Control	Green Switch
5	Power LED(+)	Power LED	18	Ground	
6	NC		19	NC	NC
7	Power LED(-)		Green LED	20	HDD LED(-)
8	Green LED(+)	21		HDD LED(+)	
9	Green LED(-)	Power Button	22	+5V	IrDA Connector
10	Ground		23	NC	
11	Power Button		24	Ir IN	
12	Reset Control		25	Ground	
13	Ground	Reset Button	26	Ir OUT	

### J30 Panel Connector (for PCB Ver:1.1 and afterward)

Pin No.	Assignment	Function	Pin No.	Assignment	Function
1	HDD LED (+)	Hard Drive	2	Green LED (+)	Power
3	HDD LED (-)	LED	4	Yellow LED (+)	LED
5	Ground	Reset	6	Power Button	Power
7	Reset control	button	8	Ground	Button
9	+5V		10	Sleep Control	Sleep
11	Ir-IN	IrDA	12	Ground	Button
13	Ground	Connector	14	No Connection	No Connection
15	Ir-out		16	+5V	IrDA Power

### J31 PC Speaker (for PCB Ver:1.1 and afterward, Optional)

Pin No.	Assignment	Function	Pin No.	Assignment	Function
1	Speaker	PC	2	+5V	VCC
3	NC	Speaker	4	Ground	Ground
5	Ground	Connector	6	NC	No
7	+5V		8	NC	Connection

### J3 CPU Cooling Fan Power Connector ([click here for Diagram](#))

Pin No.	Assignment
1	Ground
2	+12 V
3	Ground

Pin No.	Assignment
1	Standby Voltage + 5V
2	Ground
3	Wakeup Signal Input

JP16, JP25 VGA Function Selection ([click here for Diagram](#))

JP 16	JP 25	On-board VGA Function
2 - 3	1 - 2	Enabled
1 - 2	2 - 3	Disabled

J4 USB Connector ([click here for Diagram](#))

Pin	Signal Name
1	+5 V (fused)
2	USBP0- [USBP1-]
3	USBP0+ [USBP1]+
4	Ground

J23 CD Audio-In Connector ([click here for Diagram](#))

Pin No.	Assignment
1	Left Channel In
2	Ground
3	Ground
4	Right Channel In

## DRAM Installation

### DIMM

DRAM Access Time : 3.3V Unbuffered SDRAM 12ns required.  
 DRAM Type : 8MB/16MB/32MB/64MB DIMM Module (16pin)

Total Memory Size (MB)	Bank 0	Bank 1
	DIMM 1	DIMM 2
8M	8M x 1 pc	----
16M	16M x 1 pc	----
32M	32M x 1 pc	----
64M	64M x 1 pc	----
128M	128M x 1 pc	----
16M	8M x 1 pc	8M x 1 pc
24M	16M x 1 pc	8M x 1 pc
40M	32M x 1 pc	8M x 1 pc
72M	64M x 1 pc	8M x 1 pc
24M	8M x 1 pc	16M x 1 pc
32M	16M x 1 pc	16M x 1 pc
48M	32M x 1 pc	16M x 1 pc
80M	64M x 1 pc	16M x 1 pc
40M	8M x 1 pc	32M x 1 pc
48M	16M x 1 pc	32M x 1 pc
64M	32M x 1 pc	32M x 1 pc
96M	64M x 1 pc	32M x 1 pc
72M	8M x 1 pc	64M x 1 pc
80M	16M x 1 pc	64M x 1 pc
96M	32M x 1 pc	64M x 1 pc
128M	64M x 1 pc	64M x 1 pc
256M	128M x 1 pc	128M x 1 pc

\*DRAM must first be installed to Bank 0.

\*The list shown above for DRAM configuration is only for reference.