

M54Li System board

- **Processor:**
 - Single ZIF socket for Intel P54C CPUs.
 - Supports Intel P75 – P100 processors (Intel P120 and P133 supported on Rev B boards)
 - Standard voltage supported (no VRE).
- **Expansion:**
 - Three 32-bit PCI slots; Four 16-bit ISA slots (one shared)
- **Chipset:**
 - SiS 85C550X chipset.
 - SMC 665 Super I/O chip.
- **Memory (parity and non-parity):**
 - Four 32/36-bit 72-pin SIMM sockets.
 - Supports 1M x 32/36 (4MB), 2M x 32/36 (8MB), 4M x 32/36 (16MB), and 8M x 32/36 (32MB).
 - Maximum memory is 128MB.
 - Supports EDO memory.
- **Cache:**
 - 16K CPU level 1 write back cache.
 - 0/256/512K Asynchronous level 2 cache.
- **PCI IDE:**
 - Two IDE connectors on-board
 - Both supporting Mode 3 transfer rates.
- **Floppy:**
 - Supports 360K, 1.2, 1.44, 2.88MB floppy drives.
- **Serial / Parallel ports:**
 - Two on-board 16550 compatible serial ports.
 - One on-board parallel port with ECP support.
- **BIOS:**
 - Phoenix 4.04 BIOS on 1MB Flash chip.
- **Keyboard:**
 - Standard AT style DIN keyboard connector.
 - Optional PS/2 style keyboard and mouse connectors.
- **Form factor:**
 - Baby AT footprint (8.5" x 13")

M54Li Jumper Settings

CPU speed:

CPU Speed	75 MHz	90 MHz	100 MHz	120 MHz	133 MHz
Bus Speed	50 MHz	60 MHz	66 MHz	60 MHz	66 MHz
W4	open	open	open	closed	closed
W14	closed	closed	open	closed	open
W15	closed	open	closed	open	closed
W16	open	closed	open	closed	open

NOTE: You must have a Rev B board to use the Intel 120 and 133 processors.

Cache Size Selection:

	256K	512K
W2	open	closed
W3	open	open

Cache Voltage Selection:

	5.0V (default)	3.3V
RP55	1-2, 3-4, 5-6, 7-8, 9-10	open
RP56	open	1-2, 3-4, 5-6, 7-8, 9-10

Parallel Port DMA Channel Selection:

	DMA 1	DMA 3
W11	1-2	2-3
W12	1-2	2-3

BIOS Reset Jumper:

Jumper	Function	Setting
W18	Normal operation (default) Clear CMOS RAM settings	open closed