Specification

- VXPRO [†] Pentium MMX™ chipset
- Supports 75~233 MHz Pentium CPUs with 321-pin ZIF socket
- Supports Pentium P54C and P55C, IBM/Cyrix 6x86/6x86L/6x86MX(M2), and
- AMD K5/K6, IDT C6 CPUs
- Supports 64M-bit (16Mx4, 8Mx8, 4Mx16) technology DRAM/SDRAM
- Switching power provide CPU core voltage from 2.5V to 3.5V
 Uses four 72-pin EDO/Page Mode SIMM modules auto banking in multiple
- configuration up to 256MB

 Provides two 168-pin DIMM to support SDRAM/EDO DRAM/Page Mode DRAM,
 supports "Table Free" configuration so that DIMM and SIMM can be installed in
- any combinations up to 384MB, except that SIMM 1, 2 and DIMM 2 can not be
- installed at the same time
- Supports 512KB onboard Pipelined Burst synchronous cache
- Three 16 bits ISA Bus slots and four PCI Local Bus slots, all four PCI slots support
- Master Mode
 - System BIOS supports four IDE harddisk drives without device driver for S/W application and the capacity of each harddisk can be larger than 528MB up to 8.4GB
- Onboard PCI Bus Master IDE interface with two connectors, supports four IDE devices in 2 channels and the PCI IDE Controller, supports PIO Mode 0 to Mode 4
- at maximum transfer rate of 16.67MB/s and Bus Master IDE DMA Mode 2
- Onboard super Multi-I/O chip that supports two serial ports with 16550 Fast UART compatible, one parallel port with EPP and ECP capabilities, and one floppy disk drive interface

Supports PS/2 mouse and the Universal Serial Bus (USB) System BIOS supports NCR810 SCSI BIOS firmware, Green feature function, "Plugand Play" Flash ROM



PENTIUM ® MMX TM New Concration VYppt Chipset

New Generation VXpro Chipset PCI Bus and ISA Bus

- With PCI IDE & Multi I/O
- Fully Compatible with Intel MMX Technology with VX₽₹ Chipset
- Features SDRAM Modules

The information presented in this publication has been carefully checked for reliability; however, no responsibility is assumed for inaccuracies.

Specifications are subject to change without notice

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Jumper Settings

JP6(A,B,C,D): CPU Core Voltage Jumpers

	Setting		Setting
3.5V	A B C D	2.8V	A B C D
3.2V	A B C D	2.5V	A B C D
2.9V	A B C D		

JP9: CPU Type Jumpers

CPU		Setting	Example
P55C (Dual Voltag	e)	1 A B	Intel MMX [™] , AMD K6, IBM/Cyrix 6x86L/6x86MX(M2)
P54C (Single Voltag		A B	Intel P54C, AMD K5, IBM/Cyrix 6x86, IDT C6

JP2: DIMM Module Voltage Jumpers

Description	JP2
For SDRAM DIMM Module(3.3V)	5V 1 3.3V A B
For EDO DRAM/Fast Page DRAM DIMM Module(5V)	5V

Jumper Settings

JP1: CMOS RAM Discharge Jumper

pin	Description
1	Internal Battery
2	CMOS
3	Ground

Description	JP1
Normal Mode	
Clear CMOS	1

JP3(A, B, C): CPU Speed Jumpers

speed sampers		
CPU Clock	JP3	
50MHz	A B C	
55MHz	A B C	
60MHz	A B C	
66MHz	1 A B C	
75MHz	1 A B C	

JP5: CPU Internal Clock Speed Jumpers

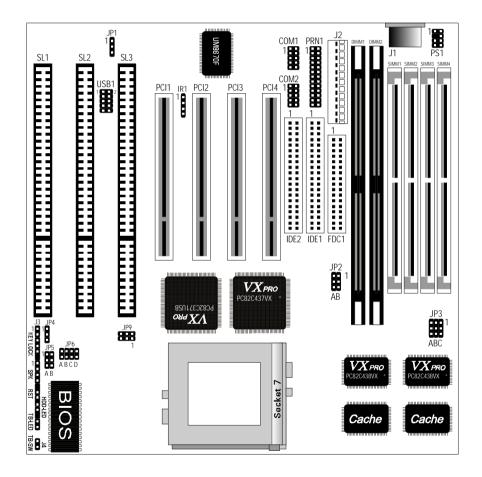
IDT	Intel	Cyrix	AMD	JP5
Reserved	1.5X / 3.5X	Reserved	K5 1.5X / K6 3.5X	1 A B
Reserved	2.0X	2.0X	Reserved	1 A B
Reserved	2.5X	M2 2.5X	2.5X	1 A B
C6 3.0X	3.0X	M2 3.0X	K6 3.0X	1 A B

JP3(D): PCI clock Jumper

	1
PCI Clock	JP3
CPU CLK/2	1 D
33 MHz	1 D

Note: If system run in 75/83 MHz, JP3(D) must be set to 33MHz position.

Component Locations



Quick Installation Guide

- 1. Set JP1 to CMOS RAM discharge jumper (pin 1-2)
- 2. Set JP3 to select CPU speed
- 3. Set JP5 to select CPU Internal Clock Speed
- 4. Set JP6(A, B, C, D) to select CPU Core Voltage
- 5. Insert CPU to CPU socket
- 6. Set JP9 to select P54C or P55C CPU
- 7. Insert 72-pin SIMM modules into SIMM1-4 and/or insert 168-pin DIMM modules into DIMM1-2, notice that DIMM2 and SIMM1, 2 can not be installed at the same time
- 8. Set JP2 to select voltage of DIMM module (if DIMM installed).
- 9. Install mainboard into system chassis
- 10. Connect keyboard to J1
- Insert the display card and other peripheral cards (if required) onto the mainboard
- 12. Connect harddisk(s) to IDE primary/secondary connector(s)
- 13. Connect floppy drive(s) to FDC1 connector
- 14. Connect serial port to COM1 and COM2 connectors
- 15. Connect parallel port to PRN1 connector
- 16. Connect J3(HDD-LED) to "Hard Disk Busy" LED on the system chassis
- 17. Connect J3(TB-LED) to Turbo LED on the system chassis
- 18. Connect J3(RST) to Reset Switch on the system chassis
- 19. Connect J3(SPK) to Speaker on the system chassis
- 20. Connect J3(KEYLOCK) to keylock and power LED on the system chassis
- 21. Connect power cord to J2 Power Supply Connector

Connectors

COM1/2: Serial Port #1/#2

PRN1: Parallel Port

FDC1: Floppy Disk Port

IDE1/IDE2: Primary/Secondary IDE Ports

J1: Keyboard Connector

J2: Power Supply Connector

J3(TB-LED): Turbo LED Connector

J3(KEYLOCK): Keylock & Power LED Connector

J3(HDD-LED): Hard Disk LED Connector

J3(RST): Reset Switch Connector

J3(SPK): Speaker Connector

PS1: PS/2 Mouse Pin Connector

JP4: Flash ROM Voltage Jumper

Description	JP4
12 Voltage Flash	1 12V
Programming	5 5V
5 Voltage Flash	1 12V
Programming	5V

IR1: Infra Red

pin	Description
1	IR In
2	Ground
3	IR Out
4	+ 5VDC

USB1: 2 sets of Universal Serial Bus Connector

pin	Description
1,2	+ 5VDC
3,4	Data-
5,6	Data+
7,8	Ground
9,10	Ground