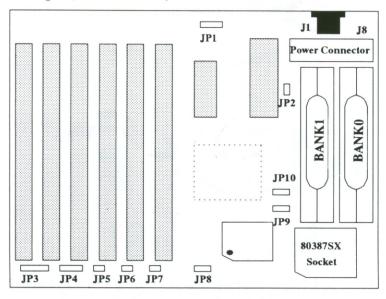
M396F

386SX / 486SLC Mainboard User's Guide

Jumper, Connector, and Socket Locations



KEY FEATURES

- Single chip for 386SX or CX486SLC 25/33/40 MHZ system
- Socket support for 80387SX coprocessor
- Page mode and block interleave DRAM management
- Memory configurations from 512KB to 16MB using combinations of 256KB,512K,1M and 4M SIMM modules
- Support hardware and software turbo switch
- Support shadow RAM for system and video BIOS
- Six 16-bit I/O slots
- External and rechargeable battery-backed CMOS RAM for real time clock and system configuration
- PCB with dimensions of 220mm x 170mm

JUMPER SETTING AND CONNECTORS

JP1. External battery connector

Pin 1: VDD(6V)

Pin 4: Gnd

Pin 2,3: Rechargeable battery pin

Note: To clear the CMOS configuration, place a jumper cap on pin3-4 and then place the cap back on pin 2-3 for normal operation.

JP2. Display type select

Close: Color

Open: Monochrome

JP3. Power LED and keylock connector

Pin 1: LED power

Pin 2: Not used

Pin 3: Gnd

Pin 4: Keyboard inhibiter

Pin 5: Gnd

JP4. Speaker connector

Pin 1: Data out

Pin 2: Not used

Pin 3: Gnd

Pin 4: +5Vdc

JP5. Turbo LED connector

Pin 1: +Anode

Pin 2: -cathode

JP6. Turbo switch connector

Close: Turbo speed

Open: Low Speed

You can also switch the clock speed using software control via keyboard commonds.

CTRL,ALT,[+]: Press these three keys simultaneously to select Turbo Mode.

CTRL,ALT,[-]: Press these three keys simultaneously to select LOW Speed Mode.

JP7. Reset connector

Close: Reset

Open: Not reset

JP9,JP10. CPU clock select

CLOCK	JP9	JP10
16 MHZ	1-2	1-2
25 MHZ	1-2	2-3
33 MHZ	2-3	1-2
40 MHZ	2-3	2-3

MEMORY ARCHITECTURE

The main board contains 2 banks of memory. A total of 8 modes are available.

DRAM TYPE		TOTAL
BANK0	BANK1	MEMORY
256K	x	512KB
256K	256K	1MB
512K	x	1MB
512K	512K	2MB
1M	x	2MB
1M	1M	4MB
4M	x	8MB
4M	4M	16MB
	256K 256K 512K 512K 1M 1M 4M	BANK0 BANK1 256K x 256K 256K 512K x 512K 512K 1M x 1M 1M 4M x

PLEASE SHORT JP1 PIN 2-3 WITH A JUMPER CAP (REMOVED FROM PIN 3-4) TO AVOID SYSTEM BOOT – UP FAILURE JP1 PIN 3-4 SHORTED IS TO PREVENT BATTERY FROM LOSING POWER WHEN SHIPPED BY SEA