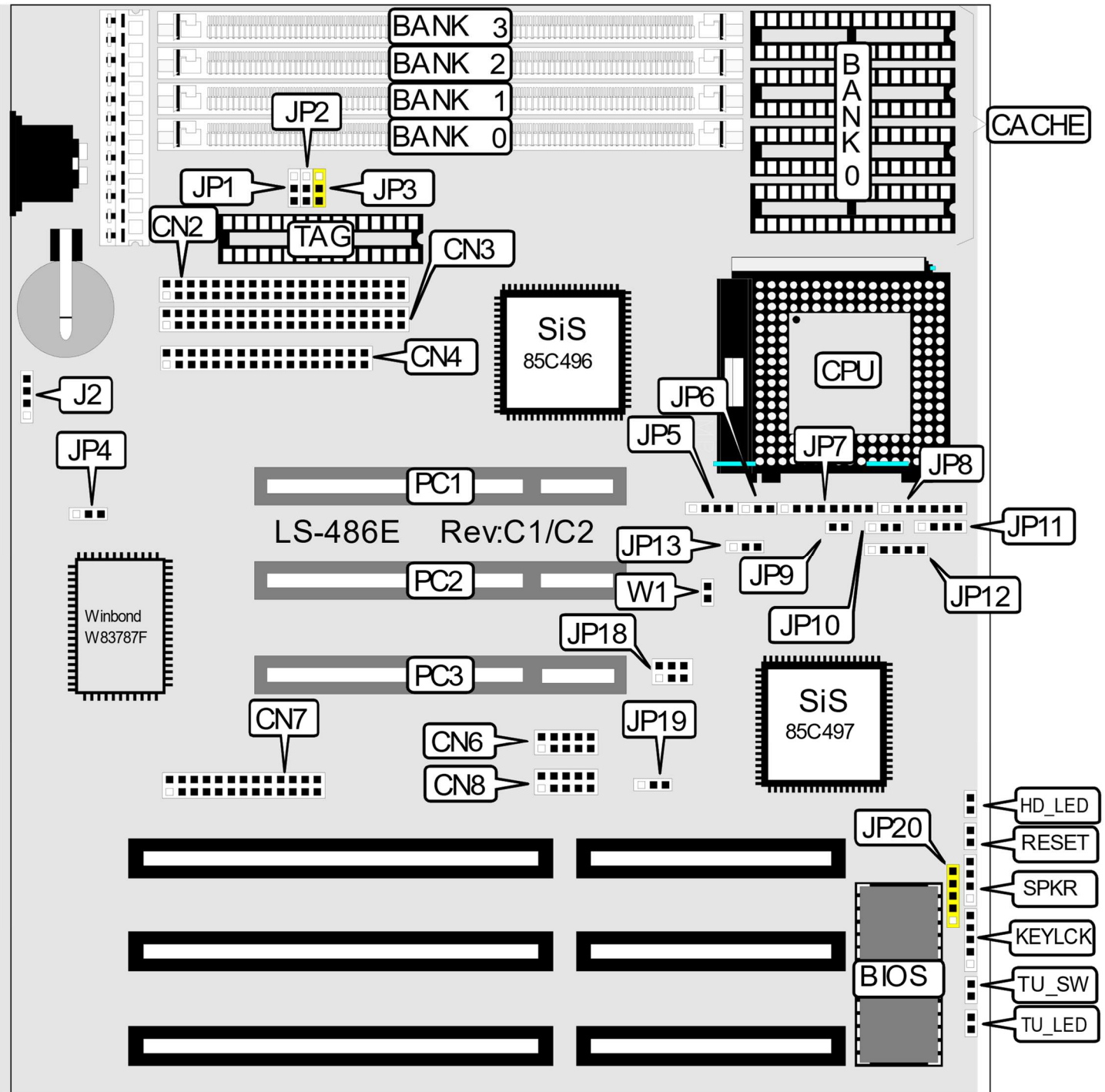


Lucky Star LS-486E rev: C1/C2 jumper settings



Note: jumpers marked with yellow might be hard wired on some boards

- Connectors list:

- | | |
|---------------------|----------------|
| CN2 – Primary IDE | CN6 – COM1 |
| CN3 – Secondary IDE | CN7 – Parallel |
| CN4 – Floppy | CN8 – COM2 |

- CPU settings

CPU voltage:	W1	JP13
3.45 volts (5x86/DX4)	Open	1-2
4.0 volts (Cyril DX2)	Open	2-3
5.0 volts (Intel DX/DX2)	Closed	Open

FSB speed:	JP18
25MHz	All Open
33MHz	5-6
40MHz	3-4
50MHz	3-4, 5-6
55MHz (*)	1-2
60MHz	1-2, 5-6
66MHz	1-2, 3-4, 5-6
75MHz (**)	1-2, 3-4

Clock chip used: Macronix MX8325MC

(*): This mode is inaccurately reported in the original manual as being 33MHz

(**): The clock chip is not guaranteed to operate at 75MHz

CPU type:	JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12
Intel DX/DX2	OFF	1-2	1-2, 6-7	4-5, 6-7	ON	2-3	1-2, 3-4	4-5
AMD 5x86	3-4	1-2	1-2, 6-7	4-5, 6-7	ON	2-3	1-2, 3-4	4-5
Cyril 5x86	OFF	1-2	1-2, 6-7	4-5, 6-7	ON	2-3	1-2, 3-4	4-5
Intel SLE DX/DX2/DX4	OFF	OFF	2-3	4-5, 6-7	ON	2-3	3-4	4-5
AMD DX/DX2/DX4	OFF	OFF	5-6 (DX4) / 6-7 (3 volt DX2)	OFF	OFF	2-3	OFF	4-5
Cyril DX2/DX4	2-3	2-3	7-8	2-3, 5-6	ON	2-3	2-3	4-5
Pentium Overdrive	1-2	1-2	3-4	1-2, 6-7	ON	1-2	3-4	1-2
IBM/ST DX4-100	OFF	OFF	2-3	4-5, 6-7	ON	2-3	1-2, 3-4	4-5

- Cache settings:

Cache bank size:	JP2
128KB (four 32K x 8 chips)	1-2
256KB (four 64K x 8 chips)	2-3

Cache TAG chip size:	JP1
8K x 8/32K x 8	1-2
16K x 8	2-3

- Other settings

CMOS operation:	JP4
Normal	1-2
Clear	2-3

Clock chip buffer input:	JP19
CPUCLK/2	1-2
CPUCLK	2-3

- Memory configs

Total memory:	SIMM 1	SIMM 2	SIMM 3	SIMM 4
4 MB	4 MB	--	--	--
8 MB	4 MB	4 MB	--	--
8 MB	8 MB	--	--	--
12 MB	4 MB	4 MB	4 MB	--
12 MB	4 MB	8 MB	--	--
16 MB	4 MB	4 MB	4 MB	4 MB
16 MB	8 MB	8 MB	--	--
16 MB	16 MB	16 MB	--	--
20 MB	4 MB	16 MB	--	--
24 MB	8 MB	8 MB	--	--
32 MB	8 MB	8 MB	8 MB	8 MB
32 MB	16 MB	16 MB	--	--
64 MB	16 MB	16 MB	16 MB	16 MB
128 MB	32 MB	32 MB	32 MB	32 MB