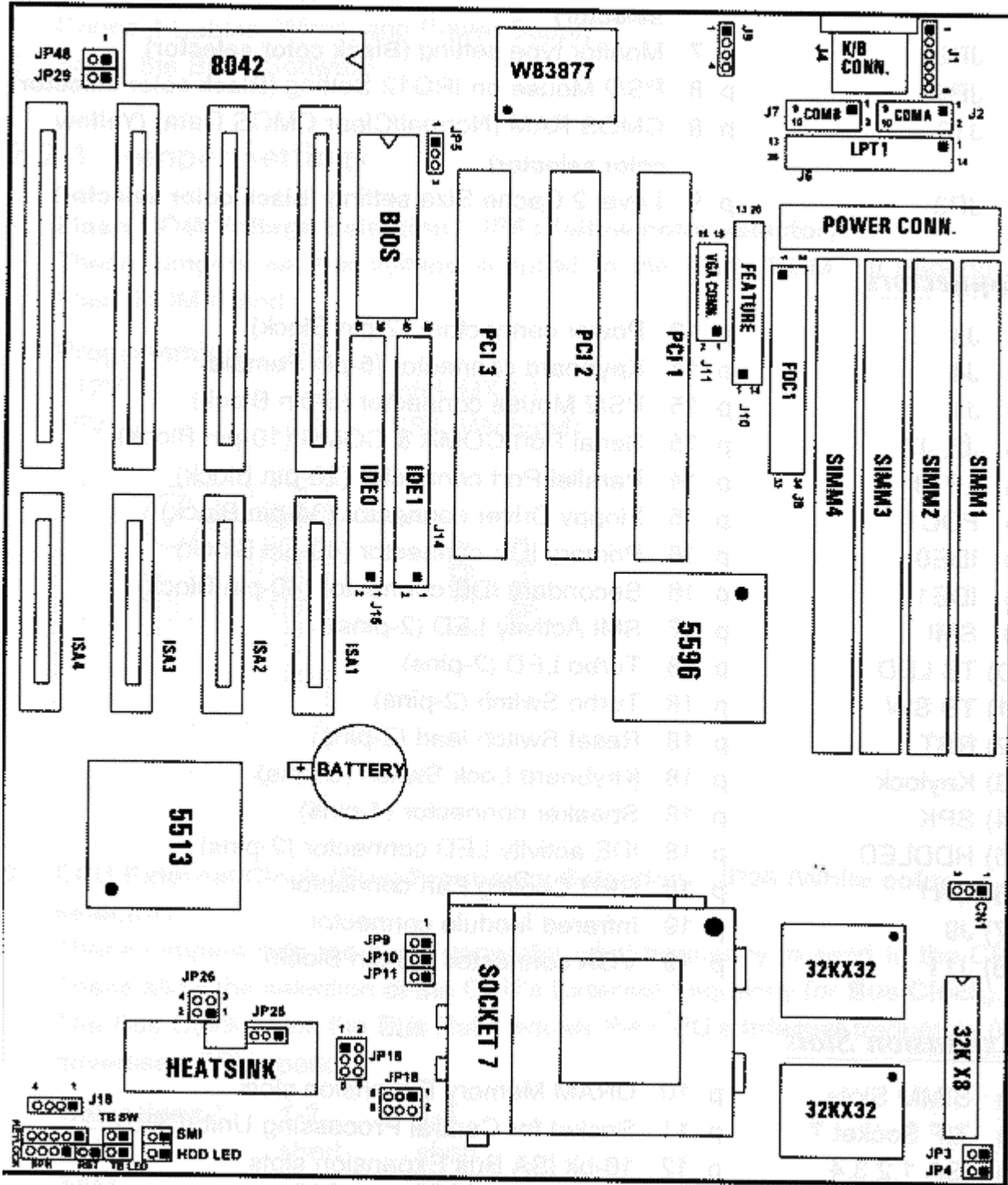


Материнская плата J-648A



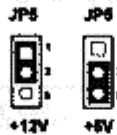
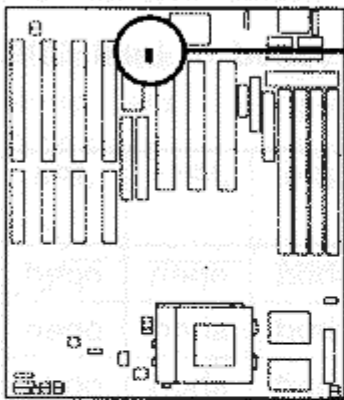
- | | |
|--------------|--|
| 1) JP5 | p. 5 Flash ROM voltage Setting (Yellow color selector) |
| 2) JP26 | p. 5 CPU External clock (Bus) Frequency Selection (White color selector) |
| 3) JP9, JP11 | p. 6 CPU to Bus/Core Ratio (Blue color selector) |
| 4) JP16 | p. 7 Voltage Regulator Output Selection (Yellow color selector) |
| 5) JP29 | p. 7 Monitor type setting (Black color selector) |
| 6) JP48 | p. 8 PS/2 Mouse on IRQ12 Setting (Black color selector) |
| 7) J18 | p. 8 CMOS RAM (Normal/Clear CMOS Data) (Yellow color selector) |
| 8) JP3 | p. 9 Level 2 Cache Size setting (Black color selector) |

Flash ROM Voltage Selection: JP5 (Yellow color selector)

These jumpers set the voltage supplied to the Flash ROM. It depend on Flash ROM Brand.

Programming JP5

+12V	1-2	(Intel, MXIC)
+5V	2-3	(SST, Winbond)



CPU External Clock (Bus) Frequency Selection: JP26 (White color selector)

These jumpers sets the clock generator what frequency to send to the CPU, These allow the selection of the CPU's External frequency (or Bus Clock).

The Bus Clock times the Bus Ratio equals the CPU's Internal frequency (the advertised CPU speed).

<u>Selections</u>	<u>1-2</u>	<u>3-4</u>
50M	short	short
55M	open	open
60M	short	open
66M	open	short



