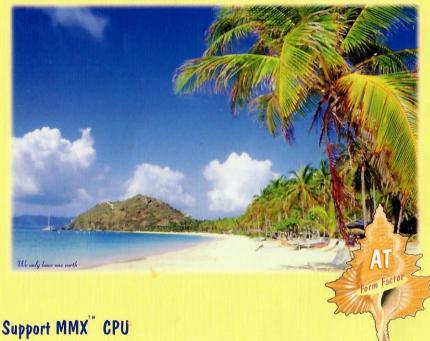
## Twill Ultra SCST Motherboard

## P55XUB"/XUWB"



Adaptec® Ultra SCSI embedded

Synchronous DRAM/DIMM

















I will USA 9004 Research Dr. Irvine, CA 92618



#### **EC Declaration of Conformity**

We

Iwill Corp.

No. 10, Wu-Chuan 3 Rd., Hsin-Chung City,

Taipei, Taiwan

declare under sole responsibility that the

P55XUB/XUWB motherboard

meets the intent of Directive 89/336/ECC for Electromagnetic Compatibility. Compliance was demonstrated to the following specifications as listed in the official Journal of the European Communities:

EN 50081-1 Emissions:

EN 55022 Radiated, Class B

EN 55022 Conducted, Class B

EN 60555-2 Power Harmonics

EN 50082-1 Immunity:

IEC 801-2 Electrostatic Discharge

IEC 801-3 RF Radiant

IEC 801-4 Fast Transient



#### **About This Manual**

This manual will guide the user how this SCSI motherboard was consisted. All useful information will be described in later chapters. Keep this manual for your future upgrade or system configuration changed.

The chapter Quick Installation ---

This chapter's description is suitable for most user, Just follow step by step in installing the system. .

The chapter Overview ---

Describe the feature and specificaton of this SCSI motherboard.

The chapter Manufacture Default ---

This SCSI motherboard has pre-setting before ship out and this pre-setting default is suitable for most user.

The Hardware Installation ---

Describes the hardware detail specification and connection.

The ATX Form-Factor Overview ---

The ATX is the new generation form-factor.

The Award BIOS Setup ---

Detail description of this SCSI motherboard, no need to change the manufacture default unless you have conflict in your system.

The SCSI BIOS Setup ---

The Adaptec SCSI BIOS setup.

This manual is subject to change without notice.

This manual contains information protected by copyright law. All rights are reserved. No any part of this document may be used or reproduced in any forms or by any means, or stored in a database or retrieval system, without prior written permission from IWILL Corporation.

#### **Trademark**

Intel / Pentium is trade mark of Intel Corporation and all other product names are trademarks and registered trademarks of their respective owners.

#### **Contents**

Chapter 0- Quick Installation	0-0
Chapter 1- Overview	
1.1 Features	1-1
1.2 Specifications	1-3
Chapter 2- Hardware Installation	
2.1 Preparation and Inspection	2-1
2.2 Placement	2-2
2.3 CPU group	2-5
2.4 Cache Memory	2-9
2.5 D-RAM Configuration	2-9
2.6 SCSI Interface	2-11
2.7 RAIDBUS	2-16
2.8 IDE Interface	2-17
2.9 USB	2-18
2.10 Enhanced Multi-IO	2-19
2.11 Other	2-21
Chapter 3- Award BIOS Setup	
Chapter 3- Award BIOS Setup  3.1 Entering Setup	3-4
The state of the s	3-4 3-7
3.1 Entering Setup	
3.1 Entering Setup 3.2 Standard CMOS Setup	3-7
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup	3-7 3-11
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup	3-7 3-11 3-17
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals	3-7 3-11 3-17 3-24
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration	3-7 3-11 3-17 3-24 3-27
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals	3-7 3-11 3-17 3-24 3-27 3-30
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals 3.8 Load Setup Defaults	3-7 3-11 3-17 3-24 3-27 3-30 3-35
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals 3.8 Load Setup Defaults 3.9 Supervisor/User Password	3-7 3-11 3-17 3-24 3-27 3-30 3-35 3-36
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals 3.8 Load Setup Defaults 3.9 Supervisor/User Password 3.10 IDE HDD Auto Detection 3.11 HDD Low Level Format 3.12 Save & Exit Setup	3-7 3-11 3-17 3-24 3-27 3-30 3-35 3-36 3-37
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals 3.8 Load Setup Defaults 3.9 Supervisor/User Password 3.10 IDE HDD Auto Detection 3.11 HDD Low Level Format	3-7 3-11 3-17 3-24 3-27 3-30 3-35 3-36 3-37
3.1 Entering Setup 3.2 Standard CMOS Setup 3.3 BIOS Features Setup 3.4 Chipset Feature Setup 3.5 Power Management Setup 3.6 PNP/PCI Configuration 3.7 Integrated Peripherals 3.8 Load Setup Defaults 3.9 Supervisor/User Password 3.10 IDE HDD Auto Detection 3.11 HDD Low Level Format 3.12 Save & Exit Setup	3-7 3-11 3-17 3-24 3-27 3-30 3-35 3-36 3-37 3-37

4.2 SCSI Select Utility option

4.3 Configure / View Host Adapter Settings Menu 4.4 SCSI Disk Utilities

4-2

4-3 4-8

## **CHAPTER 0 Quick Installation**

Several easy installation steps will be described in this chapter to help the experienced users for quick installation. If you are new user and need to know more about this motherboard, please start from Chapter 1.

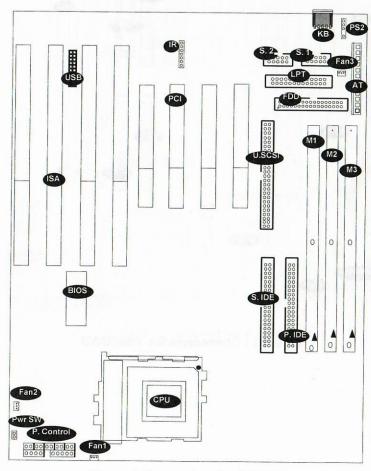


Figure 1:Connectors for P55XUB



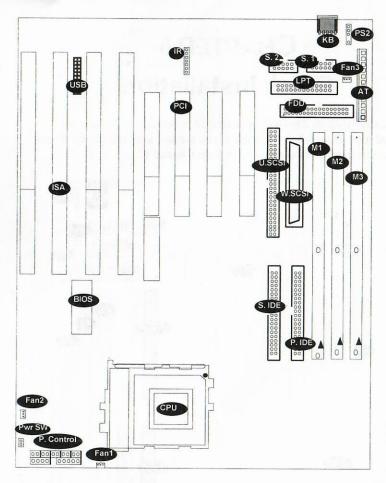


Figure 2 : Connectors for P55XUWB

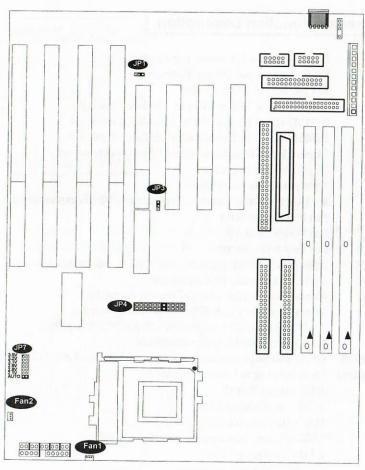


Figure 3: Jumpers location



#### PSSXUB/XUWB

#### **Connector Function Description**

AT 12 pin Standard AT power input connector BIOS (Basic Input Output System)

CPU 321 pin Socket 7 CPU socket

M1 DIMM1 (Dual In-line Memory Module ) Bank 0,1
M2 DIMM2 (Dual In-line Memory Module ) Bank 2,3
M3 DIMM3 (Dual In-line Memory Module ) Bank 4,5
FDD 34 pin Floppy Disk Drive with key protect connector

LPT 26 pin Parallel connector IR 7 pin pin-header connector

ISA 4 x ISA(Industrial Standard Architecture) 98 pin expansion slot

Fan Power connector
Fan Power connector
Fan Power connector
Fan Power connector

**Pwr SW** 2 pin power switch connector for ATX power supply

KB 5 pin AT keyboard DIN connector

P. IDE 40 pin Primary IDE with key protect connector
 S. IDE 40 pin Secondary IDE with key protect connector
 PCI 4x PCI version 2.1 compliance 120 pin PCI (Peripheral

Component Interface) expansion slot

RAIDBUS 60 pin extension slot for working with Adaptec ARO-1130 adapter

P. Control Front panel signal control connector

RST—Reset Switch SCSI—SCSI detect LED IDE—IDE detect LED

SMI-System Management Interface switch

LED—Power-on LED Speaker—Speaker connector

Keylock—Keylock switch connector

PS2 5 pin PS2 mouse connector

S. IDE 40 pin Secondary IDE with key protect connector

9 pin Serial 1 D-Sub connector9 pin Serial 2 D-Sub connector

U. SCSI 50 pin Ultra SCSI with key protect connector

USB 20 pin Universal Serial Bus connector

W. SCSI 68 pin Wide SCSI with key protect connector (P55XUWB only)

#### **Jumper Setting**

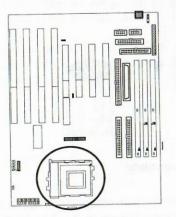
JP7 CPU working voltage select
JP3 SCSI Enabled / Disabled select
CPU clock select

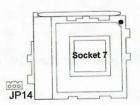
JP4 CPU clock se JP1 CMOS clear

#### **Quick Installation**

#### Step 1. Install CPU

Pull up the CPU handle bar, place the CPU into the socket in gentle/ horizontal way then pull down the handle bar back to its original place. Care must taken for the CPU's direction when was inserted.





#### Step 2. Install CPU Cooler

Hook the CPU Cooler's metal latch one side to the CPU socket's peg, then press the metal latch again to fit into another side.

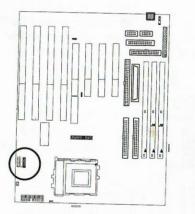
PSSXUB/XUWB 🔫

Warning: Care must be taken during the CPU Cooler installation, if not a suitable force might scrach this motherboard and damage to the motherboard.

#### Step 3. Adjust the CPU voltage (default is "Auto")

This motherboard shipped with CPU voltage default at "Auto", with this manufacture default voltuage will working with all of Intel Pentium, Pentium with MMX, AMD K5, AMD K6 166, 200, 233, 266MHz or Cyrix 6x86, 6x86L, 6x86MX, compatible processor.

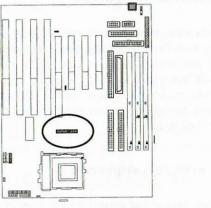
You are not required to adjust the CPU voltage unless you are using CPU that is not list above.

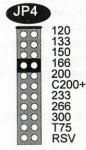




Step 4. Set the CPU frequency

The manufacturer default at 166Mhz with the special design on this motherboard. Once the jumper cap was inserted, it will automatically caculate the Internal / External Clock and CLKMUL (Clock Multiplier). Just insert the jumper cap to the CPU real frequency. There are more detail description on Chapter 2.3.4 CPU Clock Select .

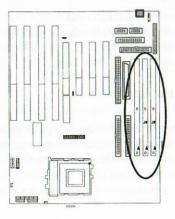




#### Step 5. Install the Memory Module

For at least one piece 168 pin DIMM (Dual In-line Memory Module) must be inserted in this motherboard.

The memory in this motherboard devided into three DIMM sockets. The left side of DIMM socket combined as Bank 0 and 1, the middle of DIMM socket combined as Bank 2 and 3, the right side of DIMM combined as Bank 4, 5.





#### Step 6. Install the internal peripherals

The internal peripherals means devices that in the computer chassis like the Floppy Disk Drive, Hard Disk Drive, CD ROM Drive ...etc. With this motherboard package, there are 34, 40, 50 and 68 (for P55XUWB only) pin flat cable. Use these cables to connect the motherboard and internal devices. All flat cable's connector housing have the key protect, this will guide you the correct direction when you insert.

#### Step 7. Install the external peripherals

The external device means devices that outside the computer chassis like the serial mouse, printer, PS/2 mouse, external modem...etc. With this motherboard's package there are two 10 pin cables convert to 9 and 25 pin D-Sub male connector cables are for high speed serial port, one 26 pin convert to 25 pin D-Sub female connector cable is for IEEE 1284 parallel port, one 5 pin housing but 4 wires into a 6 pin Mini-Din is for the PS/2 mouse.

#### Step 8. Install the reset switch

Most computer chassis provides a reset switch to front pannel control. Use the chassis provides a 2 pin housing and connect to this reset switch(any direction can function this). This will provides the system a hardware reset once the system was hang.

#### Step 9. Install the keylock

Some computer chassis will provides a keylock switch. Normally connect the marked keylock connector to the motherboard keylock. This will cause the keyboard out of function once you switch the keylock to "Lock". This will prevent any un-authorized keyin. In order to pass ESD test, the keylock function is not suggested to connect the front panel of chassis.

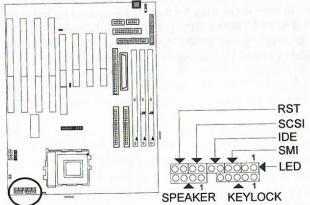
#### Step 10. Install the Speaker

Connect the speaker to the motherboard's speaker connector. (normally the red cable is +5V power)

#### Step 11. Install the power LED

Some computer chassis provides a power LED for identify if the system is on or off. Connect the power LED to the motherboard's TB LED connector. (normally the red cable is +5V power)

#### Step 12. Install the Devices LED



Some computer chassis provides a device LED(Like HDD LED) for identify if the device is under working or not. Connect the device LED to the motherboard's IDE/ SCSI LED connector. (normally the red cable is +5V power)

#### Step 13. Install the power input

This SCSI motherboard provides the standard AT power input. Carefully plug the P8 and P9 connector from your AT power supply to the motherboard.

#### Step 14. Install the keyboard

Follow the keyboard cable's key direction and connect to this motherboard.

#### Step 15. Install the display adapter

Finally, follow the display card you have and insert in horizontal direction into the PCI/ISA expansion slot.

#### Step 16. Power on the system

Once the system was power on, on the lower left corner of the screen will show "Press DEL to enter SETUP, ESC to skip memory test". Then press "DEL" to enter BIOS setup for the first time power on and choose the "LOAD SETUP DEFAULTS" then BIOS will responds "Load SETUP default(Y/N)?", press "Y" and "Enter". Final choose "SAVE to CMOS and EXIT (Y/N)?" by pressing "Y" and "Enter".

# CHAPTER 1 Overview

Thank you for purchasing this P55XUB/XUWB SCSI Motherboard. This operation manual will guide you to proper configure and install this SCSI motherboard. It has an overview of the engineer design and feature of this motherboard. Also, this manual provides useful information for your later upgrade or change the configuration. Keep this, for your future reference.

#### 1.1 Featuress

This P55XUB/XUWB SCSI Motherboard is your best choice for the optimized media rich performance desktop on the Pentium level processor with the new MMX technology. It incorported with Intel's 430TX PCIset with Adaptec 7860/7880 SCSI Controller and IO sub system that supports for Pentium processor from 120 to 300Mhz with one single jumper to set up the frequency and 512K L2 cache onboard that will prepare to run heavy application.

It incorporated with Adaptec new PCI Ultra SCSI AIC-7860(AHA-2940AU) / Ultra Wide SCSI AIC-7880(AHA-2940UW) that double/ four times the SCSI speed. In order to support fully plug and play I/O, an Ultra I/O that contain keyboard and real time clock is used for users to enjoy the plug and play function. This SCSI motherboard is designed with standard Baby AT form factor support the legacy AT for mfactor chasis.

The concurrent PCI 2.1 reduces CPU latencies for smoother video and more relistic audio and Universal Serial Bus (USB) technology makes Plus and Play peripheral connections a reality.

Use one single jumper cap to adjust the CPU speed and auto dectect CPU typr are very important functions that this SCSI Motherboard support. It supports P54C up to 233 MHz and Intel's new generation Pentium processor that support MMX technology from 166 up to 233MHz. The AMD K6 from 166 up to 266MHz and Cyrix 6x86L and 6x86MX up to PR200 MHz all are supported on this SCSI motherboard.

Run with faster system memory is another plus on this SCSI Motherboard to outperform the whole system performance. It was designed with three of the

P55XUB/XUWB

168 pin DIMM memory socket that support the new synchonous DRAM and the burst EDO DRAM, system memory can up to 256MB.

#### ■CPU:

CPU socket (socket 7) supports the flexibility of different type of current Cyrix (6x86, 6x86L, 6x86MX), AMD (K5, K6) and Intel Pentium, Pentium with MMX processor up to 233MHz. One easy jumper to set CPU speed from 120 to 300MHz.

#### ■BIOS:

2 Mega bit Award BIOS, supports bootable CD-ROM, bootable SCSI harddisk, USB,on-screen "Plug & Play" setup for Adaptec SCSI, Enhanced IDE, and Ultra Multi-IO. Support Flash ROM (This ROM provides better upgrade ability for user to update their BIOS data on the system board, user can down-load/update newer version BIOS from Internet or diskette file.

#### **■**Cache memories:

High performance write-back Level 2 external static RAM cache. This supports 512KB Synchronous Pipeline Burst Cache on-baord.

#### ■Memories:

3 X 168-pin DIMM(Dual In Line Memory Module) (Rev 1.1) Support up to 256MB (minimum of 8MB) on board. Support both the burst EDO (Extended Data Output) and Synchronous DRAM (3.3V, unbuffered) module.

#### ■Ultra / Ultra Wide SCSI interface:

- P55XUB equipe with Adaptec PCI Ultra SCSI (AIC-7860 chip) built in which is equilevent to Adaptec AHA-2940AU PCI SCSI controller. Data transfer rate up to 20MB/Sec.
- P55XUWB equipe with Adaptec PCI Ultra Wide SCSI (AIC-7880 chip) built in which is equilevent to Adaptec AHA-2940UW PCI SCSI controller. The data transfer rate up to 40MB/Sec.
- Support the Adaptec / IWILL RAIDBUS slot (only available with P55XUWB).
- Complete software driver from Adaptec Inc., include EZ-SCSI for DOS/Windows, Netware, Windows NT, OS/2, SCO Unix, Unixware.

#### **■**Expansion slots:

4 X 16-bits ISA slots, 4 X 32-bits PCI slots and 1 X RAID Port (P55XUWB only) slot.
(PCI#5 is occupied by the Adaptec SCSI.) (PCI Rev 2.1)

#### ■IDE functions:

- Support Ultra DMA/33 Bus Master IDE.
- · Support bootable CD-ROM.
- · Built in dual PCI Bus EIDE Channels.
- Support ATAPI (AT Attachment Packet Interface)

#### ■USB interface (Universal Serial Bus):

- Plug and Play devices outside the computer box
- Up to 12Mbit
- Easy of use

#### ■In-Put power:

Support the legacy standard AT power connector P8 and P9

#### ■Floppy Interface:

- Support both 3 1/2" and 5 1/4" floppy disk drives and Tape Drive (360K / 720K/ 1.2M / 1.44M / 2.88MB)
- Enabled/Disable selectable from system BIOS
- Non-Burst Mode DMA Option, 16 Byte Data FIFO

#### **■**Serial Port:

- Two high speed 16550 UART serial port
- · IRQ selectable from BIOS.
- · Address changeable form system BIOS
- NS16C550 compatible, Programmable Baud Rate Generator

#### **■**Parallel Port

- IRQ selectable for IRQ5(278h), IRQ7(378h) from system BIOS.
- Standard mode--Compatible with IBM PC/AT Printer port
- EPP Mode --Enhanced Parallel Port compatible(EPP) compatible
- ECP Mode --Microsoft & Hewlett Packard Extended Capabilities Port (ECP) Compatible
- Incorporates ChiProtect Circuit for protection against damage due to printer power-on

#### 1.2 Specifications

■Physical Dimensions:Length: 280mm / Width: 220mm

#### **■**Environmental Requirements:

Temperature 0-55 degrees C (operating or storage) 5% to 95% non-condensing relative humidity

# CHAPTER 2 Hardware Installation

#### 2.1 Preparation and Inspection

This P55XUB/XUWB SCSI Motherboard, like all electronic equipment is static sensitive. Please take the proper precautions when handling this board. You should avoid static up. If possible, You should ground yourself by touching a metal table or your computer frame. Keep the board in its conductive wrapping until it is configured and ready to be installed in your system.

#### For installation, you may need some or all of the following tools:

- · SCSI HDD drive/devices manual(s).
- IDE HDD drive/devices and floppy drive manual(s).
- · Peripherals that connect to your system's manual(s).
- · MS DOS diskettes with FDISK and FORMAT programs.
- · Medium size flat blade screwdriver.
- · Medium sized Phillips head screwdriver.
- A 3/16 inch nut driver or wrench.

#### You should find the Following components when open the box:

- P55XUB/XUWB SCSI Motherboard.
- 2. This Operation Manual
- 3. Adaptec software driver Diskette (EZ-SCSI)
- 4. Adaptec 7800 Family Manager Set software driver Diskettes
- 5. Adaptec EZ-SCSI Manual
- Adaptec 7800 Family Manager Set Manual
- 7. 68 pins Wide SCSI signal flat cable (P55XUWB only)
- 8. 50 pins internal SCSI signal flat cable
- 9. 40 pins internal IDE signal cable
- 10. 34 pins internal Floppy Disk Drive signal cable
- 11. 26 pins convert to DB25 pin for parallel port cable
- 12. 10 pins convert to DB25 pin for serial port cable
- 13. 10 pins convert to DB9 pin for serial port cable
- 14. 5 pins convert to mini-Din PS/2 mouse cable
- 15. USB Riser Card (optional)
- 16. External SCSI-2 port expansion kit (optional)
- 17. External Wide SCSI port expansion kit (optional)
- 18. SIDE Nx cooler (Optional)

#### 2.2 Placement

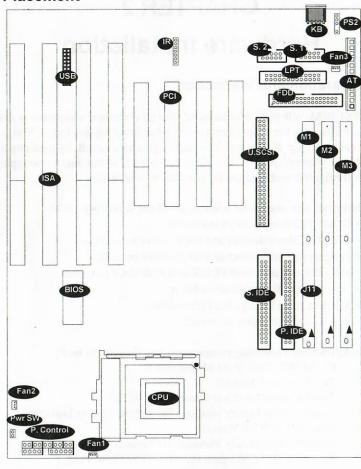


Figure 1:Connectors for P55XU

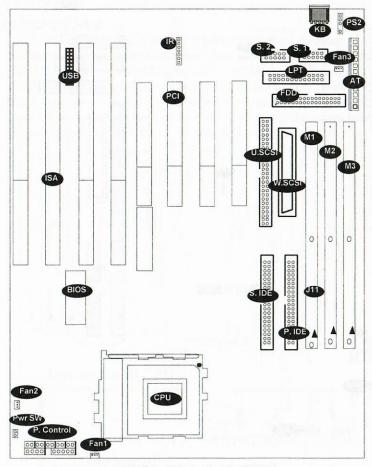


Figure 2 : Connectors for P55XUW only

## 00000 00000 0000000000000

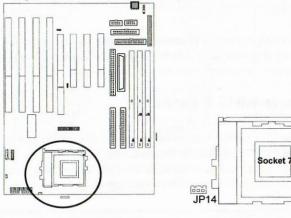
0 JP4 0000000000

Figure 3: Jumpers location

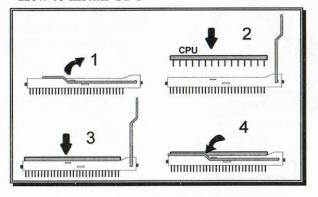
#### 2.3 CPU Group

#### 2.3.1 CPU Socket

This CPU socket provides flexibility for Intel Pentium, Pentium OverDrive, Pentium with MMX processor, AMD K5(5k86), K6 up to 266MHz and Cyrix M1(6x86), M2 processor. When installing the CPU into the Zero Insertion Force ( ZIF ) socket should be very carefully. Lift the handle bar of this 321 Pin ZIF socket up carefully and insert the CPU into ZIF socket. And make sure the CPU Pin 1 is with the square base and it goes to particular insert the CPU and press socket handle down.



#### How to install CPU



#### 2.3.2 JP14 CPU Cooler Fan Power Connector

Warning !!! Warning !!!

CPU Cooler is required to be placed on top of the CPU all the times to prevent CPU over-heat.

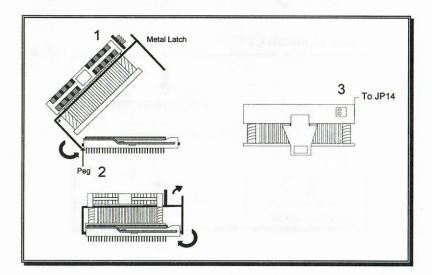
The SIDE Nx high efficency cooler is available at option to support all the new CPU that require up to 35W power consumption with very low EMI.

JP14 is a three pin power connector reserve for the cooler that have this output connector to reduce the length of connect the power source to your power connector.

#### How to install CPU Cooler

Hook the CPU Cooler's metal latch one side to the CPU socket's peg, then press the metal latch again to fit into another side.

Warning: Care must be taken when the CPU Cooler was installed, if not a suitable force might scrach this motherboard.



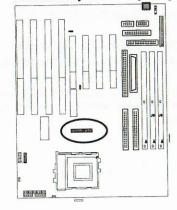


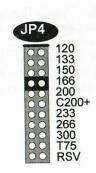
#### 2.3.3 CPU Voltage

CPU Type	Voltage Assigned	
Pentium P55C (MMX)	2.83V	
Cyrix 6x86L	2.83V	
AMD K6-166 / 200	2.9V	
Cyrix 6x86MX-166 / 200 / 233	2.9V	
AMD K6-233 / 266	3.2V	Mar 1
Pentium P54C/ AMD K5 / Cyrix 6x86	3.5V	

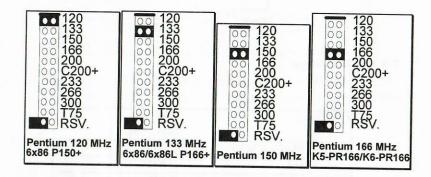
#### 2.3.4 JP4 CPU Clock Select

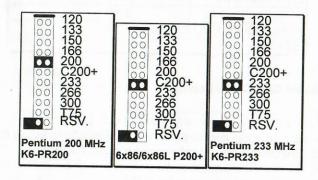
JP4 is a 2 X 11 pin jumper, with one "colored" jumper cap on it. One and only one jumper cap need to put on this connector for selecting the CPU clock. Just put this colored yellow jumper cap to the speed your CPU really is (the CPU internal/external clock ratio and ISA clock will be automatically selected by setup the JP12)





#### Following figure are for reference:

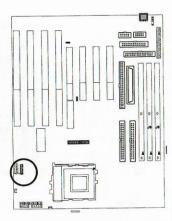


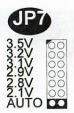


#### 2.3.5 JP7 CPU Voltage

The default is "Auto". This motherboard will detect the CPU working voltage automatically. In most of the cases, you are not required to change any of the voltage jumper.

If the future new processor comes out later than this motherboard's design that did not include the new processor's working voltage, then check the processor vendor for correct jumper setting.





#### 2.4 L2 Cache Memory

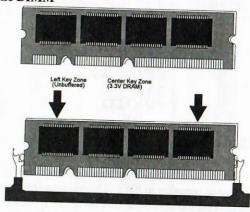
The cache memory support pipilined burst SRAM, this will perform much higher performance compare to the traditional asynchronous SRAM. This motherboard is using the new pipelined burst cache technology with 512K in size.

#### 2-5 D-RAM Configuration

This motherboard provides three (3) DIMM (Dual In-Line Memory Module) for memory expansion. These expansion memory sockets devided into six memory bank. At least one piece of 168 pin DIMM must inserted in this 64 bit Pentium base motherboard.



#### Install the DIMM



The P55XUB/XUWB supports different type of system memory. It will auto detect the memory size, no jumper needed for this function or configure.

#### NOTE:

- 1.P55XU/XUW supports both SDRAM and 3.3V EDO DRAM DIMMs.
- 2 The 70 ns Fast Page Mode or 60 ns EDO DRAM is recommended.
- 3. DIMM specification: 3.3V/ Unbuffered
- 4 This motherboard supports 4 Clock for each DIMM Socket

Make sure the DIMM module is 3.3V/ Unbuffered and can support the Intel Pentium processor while you insert into the DIMM socket.

Note: This motherboard do not support the +5V or Buffered DIMM.

#### 2.6 SCSI Interface

SCSI (Small Computer System Interface) is one of the standard interface controller. The higher level syetm always control most its peripherals in one singles controller like HDD, CD-ROM, Scanner, Tape Drive, CD-R...etc. For using the Multi-Tasking Protocol can drive several devices to work in the same time and this can increase the peripherals performance. Devices not in the same speed can be connected together and this will not postpone system overall performance, even more devices that connect to the same controller will perform better.

IWILL offer two version of these AT based SCSI motherboards.

P55XUB-- is a narrow SCSI and use the 8 bit Bus, this can connect the maximum 7 devices. And perform the 20MB/Sec transfer speed by this Ultra SCSI.

P55XUWB-- is a Ultra Wide SCSI and use the 16 bit Bus, this can connect the maximum 15 devices. And perform the 40MB/Sec transfer speed by this Ultra Wide SCSI.

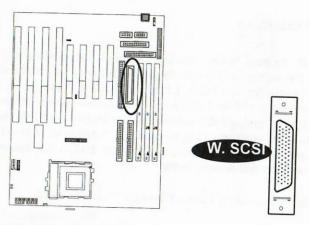
Like every house got its own address number, the SCSI Controller/Peripherals also got their own IRQ and SCSI ID numbers. This onboard SCSI controller default is using IRQ11 and ID7. Others SCSI devices also got their own ID number. The lower ID will be get higher precedential, the bootable SCSI HDD normally set to ID0.

SCSI Bus is a series/ non-stop and cabled together. And they use Terminators in both ends. So remove away all terminators that between the two ends.(reference to each devices operation manual for detail)

Care must taken for the terminators and each devices ID number.

#### 2.6.1 Ultra Wide SCSI Connector(P55XUW only)

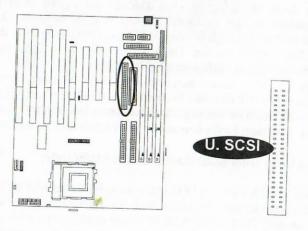
Wide SCSI is a 68 pins 16 bit Ultra Wide SCSI connector (SCSI-3). It attaches the SCSI cable(s) from the P55XUWB controller to the SCSI peripherals. The external Wide SCSI port expansion kit is an optional for your connecting external devices. Be-sure the cable's colored side should align to pin #1 of this connector.



#### 2.6.2 Ultra SCSI Connector

Ultra SCSI is a 50 pins 16 bit Ultra SCSI connector. It attaches the SCSI cable(s) from the P55XU/XUW SCSI controller to the SCSI peripherals.

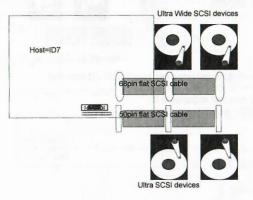
The external SCSI port expansion kit is a optional for your connecting external devices. Be-sure the cable's colored side should align to pin #1 of this connector.



#### 2.6.3 Link Internal Ultra & Ultra Wide SCSI devices

The SCSI devices are cabled together in a single, connected series. This SCSI cable must run sequentially from one device to the next, with no branches.

#### Internal SCSI Connection



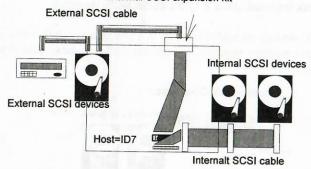
Note: The 50 pin SCSI is an 8 bit SCSI bus, the 68 pin SCSI is a 16 bit SCSI bus which has a pin-to- pin connected to the 50 pins SCSI connector.

Note: The SCSI termination must be on for the both side. The beginer and end must set the terminator on - otherwise the SCSI bus will not operate properly. For linking both the 50 & 68 pin SCSI together, the High byte terminator must set to on and Low byte set to off.

#### 2.6.4 Link Internal & External SCSI devices

The concept of linking the internal and external SCSI devices is still the same--- cabled together in a single, connected series. This SCSI cable must run sequentially from one device to the next, with no branches. The manufacture provides two kinds of option --- Fast SCSI and Wide SCSI external expansion kit for user's multiple choices.

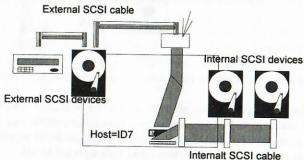
#### External SCSI expansion kit



The manufacture provides two kinds of SCSI external expansion kit for user's choices.

- \*SCSI-2 external expansion kit
- \*Wide SCSI external expansion kit

#### External SCSI expansion kit



The manufacture provides two kinds of SCSI external expansion kit for user's choices.
\*SCSI-2 external expansion kit

\*Wide SCSI external expansion kit

\*Reference to Chapter 5. SCSI Select Utility for software setup.

#### 2.6.5 Set SCSI Ids

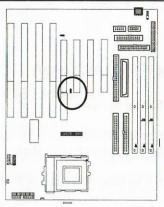
You must assign a different SCSI ID to each device on the SCSI bus connected to this motherboard. See your SCSI device documentation for directions on how to determine the ID and change it.

- Ultra/ Fast SCSI devices that connect to this mainboard's SCSI connector can be assigned ID from 0 to 7. Normally, the host will use ID7. Or reference to Chapter 5 for more information.
- Ultra Wide SCSI devices that connect to this mainboard's SCSI connector can be assigned ID from 0 to 15. Normally, the host will use ID7. Or reference to Chapter 5 for more information.
- The SCSI ID0 is best use for SCSI hard disk to be used as your computer's boot device, ID1 is best reserved for a secondary SCSI hard disk.(This is only when you use the SCSI hard disks and devices.)

#### 2.6.6 SCSI Chip Select

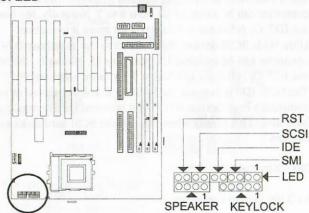
JP3 is a 3 pin jumper connector.

Function	JP3	Note
Enabled	1-2	*Default
Disabled	2-3	N 6 08





#### 2.6.6 SCSI LED



A 2 pins SCSI device detect LED connector.

Pin	Assignment
1	LED anode (+)
2	LED cathode (-)

#### 2.7 RAIDBUS slot (P55XUWB only)

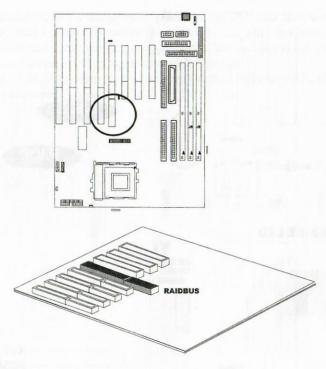
#### 2.7.1 Adaptec RAIDPort option

Turn this SCSI embedded motherboard to be hardware assist RAID ready by adding the Adaptec RAIDport 1130 adapter.

This RAIDBUS slot by adding the Adaptec RAIDport 1130 adapter can support:

- Bus Master DMA
- Up to 133 Mbyte/Sec Burst rate
- RAID level :5,1,0 and 0/1
- Fully Netware 3.11, 3.12, 4.x & WinNT 3.51 supported by Adaptec

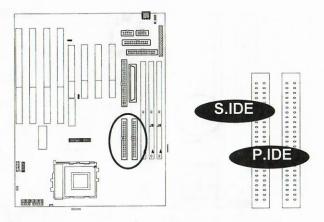
The PCI solt #1 uses the 120 pin for PCI Local Bus with 60 pin RAIDBUS slot option.



#### 2.8 Ultra DMA 33 (IDE) Interface

#### 2.8.1 Primary, Secondary IDE Connectors

Primary / Secondary IDE are 40 pins internal IDE port connectors. Use a 40 pins flat cable to connect between this connector and the IDE devices. Normally put the boot-up hard disk at the primary IDE channel and other IDE devices at the secondary IDE channel (like CD-ROM). Each IDE connector can connect for two IDE devices. For easy installation, set the first IDE device to "Master" and second IDE device to "Slave" when you connect two IDE devices in one connector.



# 2.8.2 IDE LED RST SCSI IDE SMI SPEAKER KEYLOCK

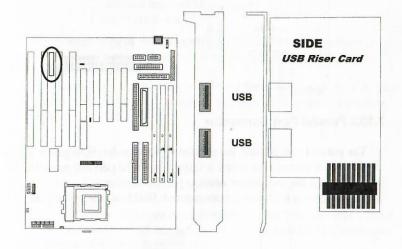
A 2 pins IDE detect LED connector.

Pin	Assignment
1	LED anode (+)
2	LED cathode (-)

#### 2.9 USB (Universal Serial Bus)

Basically the USB is suitable for middle low speed devices like Mouse, Keyboard, Joystick..etc. In before, all these were using different connectors, and it is complicated for end user in installing the system. A 4 pin standard USB connector was designed on the USB riser card, user just plug all their USB peripherals on these kinds of connector and just run. This USB riser card transfer rate up to 12Mbit per second and will provide better performance that compare to a cable type USB.

J6 is a 2 x 10 pin connector, connect this with the USB riser card and fix on the computer chassis's backplane.



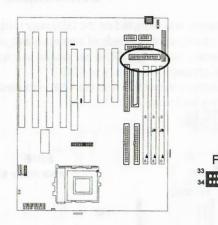
#### 2.10 Enhanced Multi-IO

#### 2.10.1 FDC Connector

The IBM compatible floppy disk drive has 360KB, 720KB, 1.2KB, 1.44KB and 2.88KB. The most popular is 1.44MB in 3.5 inch. There is also one kind of 2.88MB FDD is using on the Japan NEC PC98 series computer.

This motherboard can support for up to two different kind FDD in same connector and also support QIC-80 Tape Driver under floppy interface. FDC is a 34 pins internal Floppy port connector. Use 34 pins flat cable to connect between this connector and floppy drives.

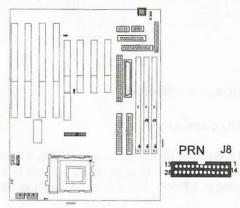




#### 2.10.2 Parallel Port Connector

The parallel port is using the parallel signal transfer, Byte per unit is the data input/ output. The speed is faster than serial port and most this was used on the parallel printer or other parallel devices.

Parallel port is a 25 pins female external DB25 connector for parallel port.



Following selection is all controlled by the BIOS: ECP Mode DMA Channel Select by BIOS printer Port Address and IRQ Select by BIOS (378h/3BC with IRQ7, 278h with IRQ5.)

#### Printer and IEEE 1284 cable

The IEEE 1284 compliant cables have better features on the following:

Twisted pairs of conductors Full foil shield Wire braid Controlled impedance -- 62 ohm Limited cross-walk

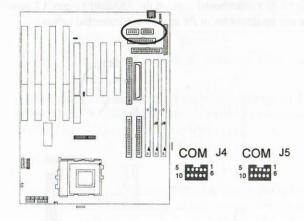
With these features will guarantee the IEEE 1284 cable perform at much higher bandwidth rates that the fast Centronics, EPP and ECP modes perform at.

If you are using the ordinary parallel cables running at the EPP or ECP mode that this controller provided, you may experience that the data efforts.

#### 2.10.3 Serial Port

The serial port is using the serial transfer. Because it transfer data input/output by bit per unit, the speed is slower than a parallel port. This ofen use in serial mouse, serial printer, fax modem...etc. This provides a 16550 compatible serial port and this is faster in transfer speed than a traditional 16450 compatible serial port.

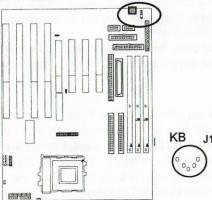
Serial 1 & Serial 2 are 9 pins pins male external DB9 port..



#### 2.11 Others

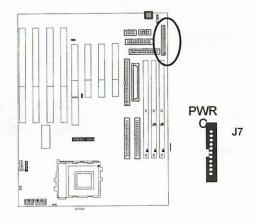
#### 2.11.1 Keyboard Connector

The keyboard connector is a 5-pin, circular-type Mini-DIN socket. It is used to connect this SCSImotherboard keyboard interface to any standard AT-compatible keyboard (84 or 101key type keyboards). The pin assignment are as follows:

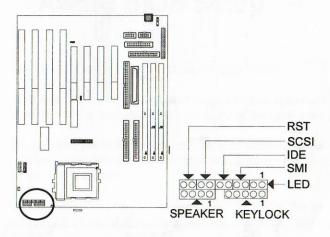


#### 2.11.2 Power Supply Connector

This SCSI motherboard support the standard 12-pin AT power connector. The pin assignments of P8 and P9 are indicated below:



### 2.11.3 Reset, SCSI LED, IDE LED, SMI Switch, LED, Speaker, KeylockConnectors

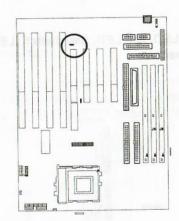


	Left pin	Right pin
Reset Switch	On = Reset	Off = Normal
SCSI LED	Cathode	Anode
IDE LED	Cathode	Anode
LED	Cathode	Anode
Speaker	VCC, GND, NC, So	und
Keylock	LED Anode, NC, GND	D, Keylock, GND

**Note**: This motherboard has no Turbo function, it will not support Turbo function. The Power LED will light-on when you connect a computer case that mounted Turbo LED and power on this system.

#### 2.11.4 CMOS clear

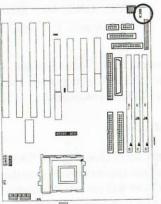
The JP1 supports CMOS clear funcion. Once the data in the BIOS was wrong or conflict and require to recover to the manufacture default, then the JP1 pin 2&3 short will provide this function. (Remember to remove this jumper cap back to it's manufacture default address JP1 pin 1&2 or it will loose the memory all the time)



JP26	Function	Note
1-2	Normal operation	*Default
2-3	CMOS clear	

#### 2.11.5 PS/2 Mouse

PS/2 Mosuse is a 6 pin Mini-DIN PS/2 mouse connector, the manufacture default is IRQ12.



# CHAPTER 3 Award BIOS Setup

#### **Notice**

The information in this manual is subject to change without notice.

The software described in this guide is furnished under a license agreement and may be used or copied only in accordance with the terms of the agreement.

Award Software, Inc. shall not be liable for technical or editorial omissions made herein; nor for incidental or consequential damages resulting from the furnishing, performance, or use of this material.

This guide contains information protected by copyright. No part of this guide may be photocopied or reproduced in any form without prior written consent from Award Software, Inc.

#### Introduction

This section discusses Award's Setup program built into the ROM BIOS. The Setup program allows users to modify the basic system configuration. This special information is then stored in battery-backed RAM so that it retains the Setup information when the power is turned off.

The Award BIOS installed in your computer system ROM (Read Only Memory) is a custom version of an industry standard BIOS. This means that it supports Intel/ Cyrix/ AMD processors in a standard IBM-AT compatible input/output system. The BIOS provides critical low-level support for standard devices such as disk drives and serial and parallel ports.

The Award BIOS has been customized by adding important, but non-standard, features such as virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

The rest of this manual is intended to guide you through the process of configuring your system using Setup.

#### PSSXUB/XUWB

#### Starting Setup

The Award BIOS is immediately activated when you first power on the computer. The BIOS reads the system information contained in the CMOS and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

- 1. By pressing <Del> immediately after switching the system on, or
- 2. by pressing the <Del> key when the following message appears briefly at the bottom of the screen during the POST (Power On Self Test).

#### Press DEL to enter SETUP.

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to...

Press <F1> to continue, <DEL> to enter SETUP

#### **Using Setup**

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Up arrow	Move to previous item
Down arrow	Move to next item
Left arrow	Move to the item in the left hand
Right arrow	Move to the item in the right hand
Esc key	Main Menu Quit and not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu Exit current page and return to Main Menu
PgUp key	Increase the numeric value or make changes
PgDn key	Decrease the numeric value or make changes

+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
(Shift)F2 key	Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
F3 key	Calendar, only for Status Page Setup Menu
F4 key	Reserved
F5 key	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
F6 key	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
F7 key	Load the default
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

#### **Getting Help**

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

#### In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the Award BIOS supports an override to the CMOS settings which resets your system to its defaults.

The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.



#### 3.1 Main Menu

Once you enter the Award BIOS CMOS Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and two exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

CMOS SETI AWARD SOF	
STANDARD CMOS SETUP	SUPERVISOR PASSWORD
BIOS FEATURES SETUP	USER PASSWORD
CHIPSET FEATURES SETUP	IDE HDD AUTO DETECTION
POWER MANAGEMENT SETUP	HDD LOW LEVEL FORMAT
PNP/ PCI CONFIGURATIONI/O SETUP	SAVE & EXIT SETUP
INTEGRATED PHERALS	EXIT WITHOUT SAVING
LOAD SETUP DEFAULTS	
Esc : Quit	: Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color

Note that a brief description of each highlighted selection appears at the bottom of the screen.

#### Setup Items

The main menu includes the following main setup categories. Recall that some systems may not include all entries.

#### Standard CMOS Setup

This setup page includes all the items in a standard, AT-compatible BIOS.

#### **BIOS Features Setup**

This setup page includes all the items of Award special enhanced features.



Super / User Password Setting

Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.

Chipset Features Setup

This setup page includes all the items of chipset special features.

Power Management Setup

This entry only appears if your system supports Power Management, screen PC", standards.

PNP / PCI Configuration Setup

This entry appears if your system supports PNP / PCI.

Integrated Peripherals

This section page includes all the items of IDE hard drive and Programmed Input / Output features.

Load Setup Default

P55XUB/XUWB 🔫

The chipset defaults are settings which provide for maximum system performance. While Award has designed the custom BIOS to maximize performance, the manufacturer has the right to change these defaults to meet their needs.

#### IDE HDD Auto Detection

Automatically detect and configure hard disk parameters. The Award BIOS includes this ability in the event you are uncertain of your hard disk parameters.

#### **HDD Low Level Format**

If supported by your system, this provides a hard disk low level format utility.

#### Save & Exit Setup

Save CMOS value changes to CMOS and exit setup.

#### **Exit Without Save**

Abandon all CMOS value changes and exit setup.

#### 3.2 Standard CMOS Setup

The items in Standard CMOS Setup Menu are divided into 10 categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

					IOS SETUF ARE, INC.	,		
Date (mm:dd:yy): I Time (hh:mm:ss):		1996					And I	
HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOR	SECTOR
Primary Master	: Auto	0	0	0	0	0	0	Auto
Primary Slave Secondary Master			0	0	0	0	0	Auto
Secondary Master	: Auto	0	0	0	0	0	0	Auto
Secondary Slave	: Auto	0	0	0	0	0	0	Auto
Drive A: 1.44M, 3 Drive B: None Floppy 3 Mode Sup Video: EGAVG	port :	Disabl	ed	Exte	Base Memo	ory : 15		
Halt On : All Errors					Total Memo	ory : 16	384 K	
sc : Quit	A.1	<b>-</b>	Select	Item	Р	U/PD/+/	- : Modif	fy

#### Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	The day, from Sun to Sat, determined by the BIOS and is display-only
date	The date, from 1 to 31 (or the maximum allowed in the month)
month	The month, Jan through Dec.
year	The year, from 1900 through 2099

#### Time

The time format is <nour> <minute> <second>. The time is calculated based on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

#### Daylight saving

The category adds one hour to the clock when daylight-saving time begins. It also subtracts one hour when standard time returns.

Enabled	Enable daylight-saving	
Disabled	Disable daylight-saving	

Primary Master/

Primary Slave/

Secondary Master/

Secondary Slave

The categories identify the types of 2 channels that have been installed in the computer. There are 45 predefined types and 4 user definable types are for Enhanced IDE BIOS. Type 1 to Type 45 are predefined. Type user is user-definable.

Press PgUp or PgDn to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Type "User" to define your own drive type manually. If you select Type "User", you will need to know the information listed below. Enter the information directly from the keyboard and press <Enter>. This information should be included in the documentation from your hard disk vendor or the system manufacturer.

If the controller of HDD interface is ESDI, the selection shall be "Type 1". If the controller of HDD interface is SCSI, the selection shall be "None". If you select Type"Auto", BIOS will Auto-Detect the HDD & CD-ROM Drive at the POST stage and showing the IDE for HDD & CD-ROM Drive.

TYPE	drive type	
CYLS.	number of cylinders	
HEADS	number of heads	
PRECOMP	write precom	00000
LANDZONE	landing zone	
SECTORS	number of sectors	
MODE	mode type	

#### Drive A Type / Drive B Type

If a hard disk has not been installed Select NONE and press <Enter>.

The category identifies the types of floppy disk drive A or drive B that have been installed in the computer.

None	No floppy drive installed
360K, 5.25 in	5-1/4 inch PC-type standard drive; 360 kilobyte capacity
1.2M, 5.25 in	5-1/4 inch AT-type high-density drive; 1.2 megabyte capacity
720K, 3.5 in	3-1/2 inch double-sided drive; 720 kilobyte capacity
1.44M, 3.5 in	3-1/2 inch double-sided drive; 1.44 megabyte capacity
2.88M, 3.5 in	3-1/2 inch double-sided drive; 2.88 megabyte capacity

#### Video

The category selects the type of video adapter used for the primary system monitor. Although secondary monitors are supported, you do not have to select the type in Setup.

LGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA, SVGA or PGA monitor adapters.
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	Monochrome adapter, includes high resolution monochrome adapters

#### Error Halt

The category determines whether the computer will stop if an error is detected during power up.

No errors Whenever the BIOS detects a non-fatal error the system will be s and you will be prompted.	
All errors	The system boot will not be stopped for any error that may be detected.
All, But Keyboard	The system boot will not stop for a keyboard error; it will stop for all other errors.
All, But Diskette	The system boot will not stop for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error; it will stop for all other errors.

#### Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

#### **Base Memory**

The POST will determine the amount of base (or conventional) memory installed in the system. The value of the base memory is typically 512K for systems with 512K memory installed on the motherboard, or 640K for systems with 640K or more memory installed on the motherboard.

#### **Extended Memory**

The BIOS determines how much extended memory is present during the POST. This is the amount of memory located above 1MB in the CPU's memory address map.

#### **Other Memory**

This refers to the memory located in the 640K to 1024K address space. This is memory that can be used for different applications. DOS uses this area to load device drivers in an effort to keep as much base memory free for application programs. The BIOS is the most frequent user of this RAM area since this is where it shadows RAM.

#### 3.3 BIOS Features Setup

This section allows you to configure your system for basic operation. You have the opportunity to select the system default speed, boot-up sequence, keyboard operation, shadowing and security.

R		IOS (XXXXXXXX) URES SETUP TWARE, INC.
Virus Warning CPU Internal Cache External Cache Quick Power On Self Test Boot Sequence Swap Floppy Drive Boot Up Floppy Seek Boot Up NumLock Boot Up System Speed Typematic Rate Setting Typematic Rate (Char/Sec) Typematic Delay (Msec) Security Option PCI/VGA palette Snoop	Disabled Enabled Enabled Enabled A, C, SCSI Disabled Enabled Con High Disabled 6 250 Setup Disabled	Video BIOS Shadow : Enabled C800-CBFFF Shadow : Disabled CC00-CFFFF Shadow : Disabled D000-D3FFF Shadow : Disabled D400-D7FFF Shadow : Disabled D800-DBFFF Shadow : Disabled DC00-DFFFF Shadow : Disabled
OS Select For DRAM > 64MI Report No FDD For Win95	B:Non-OS2 :No	Esc: Quit  F1: Help  PU/PD/+/-: Modify  F5: Old Values (Shift)F2: Change Color  F7: Load Setup Defaults

#### Virus Warning

When this item is enabled, the Award BIOS will monitor the boot sector and partition table of the hard disk drive for any attempt at modification. If an attempt is made, the BIOS will halt the system and the following error message will appear. Afterwards, if necessary, you will be able to run an anti-virus program to locate and remove the problem before any damage is done.

#### ! WARNING!

Disk boot sector is to be modified

Type "Y" to accept write or "N" to abort write

Award Software, Inc.

Enabled	Activates automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector or hard disk partition table.
Disabled	No warning message will appear when anything attempts to access the boot sector or hard disk partition table.

NOTE: Many disk diagnostic programs which attempt to access the boot sector table can cause the above warning message. If you will



be running such a program, we recommend that you first disable Virus Protection beforehand. CPU Internal Cache/ External Cache

These two categories speed up memory access. However, it depends on CPU/chipset design. The default value is enable.

Enabled	Enable cache	
Disabled	Disable cache	

Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power up the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST.

Enabled	Enable quick POST	treal government Ar
Disabled	Normal POST	SERVICE AND SERVICE

**Boot Sequence** 

This category determines which drive to search first for the disk operating system (i.e., DOS). Default value is A,C.

A,C,SCSI	System will first search for floppy disk drive, IDE hard disk drive then SCSI boot device.	
C,A,SCSI	System will first search for IDE hard disk drive, floppy then SCSI boot device.	
C,CDROM,A	System will first search for IDE hard disk drive, IDE CDROM then floppy disk drive.	
CDROM,C,A	System will first search for IDE CDROM, IDE hard disk then floppy disk drive.	
D,A,SCSI	System will first search for Primary IDE Slave device, floppy, then SCSI boo device.	
E,A,SCSI	System will first search for Secondary IDE Master device, floppy, then SCS boot device.	
F,A,SCSI	System will first search for Secondary IDE Slave device, floppy, then SCSI boot device.	
SCSI,A,C	System will first search for SCSI boot device, floppy disk drive, then IDE hard disk drive.	
SCSI,C,A	System will first search for SCSI boot device, IDE hard disk drive, then floppy disk drive.	

Conly	System will search for IDE hard disk drive only.	
-------	--	--

#### Swap Floppy Drive

This item allows you to determine whether enable the swap floppy drive or not. The choice: Enabled/Disabled.

#### **Boot Up Floppy Seek**

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360K type is 40 tracks while 760K, 1.2M and 1.44M are all 80 tracks.

Enabled	BIOS searches for floppy disk drive to determine if it is 40 or 80 tracks. Note that BIOS can not tell from 720K, 1.2M or 1.44M drive type as they are all 80 tracks.
Disabled	BIOS will not search for the type of floppy disk drive by track number. Note that there will not be any warning message if the drive installed is 360K.

#### Boot Up NumLock Status

This allows you to determine the default state of the numeric keypad. By default, the system boots up with NumLock on.

On	Keypad is number keys	- Interest page 1 statement
Off	Keypad is arrow keys	THE PROPERTY OF THE PER

#### Boot Up System Speed

selects the default system speed -- the normal operating speed at power up.

High	Set the speed to high	
Low	Set the speed to low	

Gate A20 Option

This entry allows you to select how the gate A20 is handled. The gate A20 is a device used to address memory above 1 Mbytes. Initially, the gate A20 was handled via a pin on the keyboard. Today, while keyboards still provide this support, it is more common, and much faster, for the system chipset to provide support for gate A20.

Normal	keyboard	
Fast	chipset	

#### Typematic Rate Setting

This determines if the typematic rate is to be used. When disabled, continually holding down a key on your keyboard will generate only one instance. In other words, the BIOS will only report that the key is down. When the typematic rate is enabled, the BIOS will report as before, but it will then wait a moment, and, if the key is still down, it will begin the report that the key has been depressed repeatedly. For example, you would use such a feature to accelerate cursor movements with the arrow keys.

Enabled	Enable typematic rate	
Disabled	Disable typematic rate	

#### Typematic Rate (Chars/Sec)

When the typematic rate is enabled, this selection allows you select the rate at which the keys are accelerated.

6	6 characters per second	
8	8 characters per second	
10	10 characters per second	
12	12 characters per second	
15	15 characters per second	
20	20 characters per second	
24	24 characters per second	
30	30 characters per second	

## Typematic Delay (Msec)

When the typematic rate is enabled, this selection allows you to select the delay between when the key was first depressed and when the acceleration begins.

250	250 msec	
500	500 msec	
750	750 msec	1 1/1/18
1000	1000 msec	

#### Security Option

This category allows you to limit access to the system and Setup, or just to Setup.

System	The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt.
Setup	The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt.

Note: To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.

#### OS Select for DRAM >

This item allows you to access the memory that over 64MB in OS/2. The choice: Non-OS2, OS2.

#### Report No FDD for WIN 95

This item allows you to pass the NTSL Window 95 when your system has no FDD connected.

PCI / VGA Palette Snoop



#### PSSXUB/XUWB

It determines whether the MPEG ISA/VESA VGA Cards can work with PCI/VGA or not.

Enabled	When PCI/VGA working with MPEG ISA/VESA VGA Card.	
Disabled	When PCI/VGA not working with MPEG ISA/VESA VGA Card.	1.11.211.7

#### Video BIOS Shadow

Determines whether video BIOS will be copied to RAM. However, it is optional depending on chipset design. Video Shadow will increase the video speed.

Enabled	Video shadow is enabled
Disabled	Video shadow is disabled

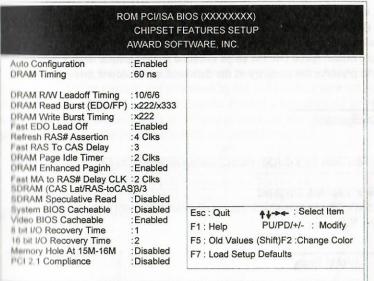
C8000 - CBFFF DC000 - DFFFF

These categories determine whether option ROMs will be copied to RAM. An example of such option ROM would be support of on-board SCSI.

Enabled	Optional shadow is enabled	
Disabled	Optional shadow is disabled	



#### 3.4 Chipset Features Setup



This section allows you to configure the system based on the specific features of the installed chipset. This chipset manages bus speeds and access to system memory resources, such as DRAM and the external cache. It also coordinates munications between the conventional ISA bus and the PCI bus. It must be stated that these items should never need to be altered. The default settings have been chosen because they provide the best operating conditions for your system. The only time you might consider making any changes would be if you discovered that data was being lost while using your system.

#### **DRAM Settings**

The first chipset settings deal with CPU access to dynamic random access memory (DRAM). The default timings have been carefully chosen and should only be altered if data is being lost. Such a scenario might well occur if your system had mixed speed DRAM chips installed so that greater delays may be required to preserve the integrity of the data held in the slower memory chips.

#### **Auto Configuration**

Pre-defined values for DRAM, cache.. timing according to CPU type & system clock.

The Choice: Enabled, Disabled.

Note: When this item is enabled, the pre-defined items will become SHOW-ONLY.

#### **DRAM Timing**

The DRAM timing is controlled by the DRAM Timing Registers. The timings programmed into this register are dependent on the system design. Slower rates may be required in certain system designs to support loose layouts or slower memory.

60ns	DRAM Timing Type.	
70ns	DRAM Timing Type.	

60ns is the default.

#### DRAM R/W Leadoff Timing

This sets the number of CPU clocks allowed before reads and writes to DRAM are performed.

11/7/3	Read Leadoff=11 clocks, Write Leadoff=7 clocks, RAS# Precharge=3 clocks.
10/6/3	Read Leadoff=10 clocks, Write Leadoff=6 clocks, RAS# Precharge=3 clocks.
11/7/4	Read Leadoff=11 clocks, Write Leadoff=7 clocks, RAS# Precharge=4 clocks.
10/6/4	Read Leadoff=10 clocks, Write Leadoff=6 clocks, RAS# Precharge=4 clocks.

10/6/3 Leadoff timing is the default.

#### DRAM Read Burst <EDO/FP>

This sets the timing for burst mode reads from two different DRAM(EDO/FPM). Hurst read and write requests are generated by the CPU in four separate parts. The first part provides the location within the DRAM where the read or write is to take place while the remaining three parts provide the actual data. The lower the timing numbers, the faster the system will address memory.

x444/x444	Read Burst (EDO/FPM) timings are x-4-4-4/x-4-4	
x333/x444	Read Burst (EDO/FPM) timings are x-3-3-3/x-4-4-4	
x222/x333	Read Burst (EDO/FPM) timings are x-2-2-2/x-3-3-3	

122/x333 timings is the default.

#### DRAM Write Burst Timing

This sets the timing for burst mode writes from DRAM. Burst read and write requests are generated by the CPU in four separate parts. The first part provides the location within the DRAM where the read or write is to take place while the remaining three parts provide the actual data. The lower the timing numbers, the faster the system will address memory.

x444	Write Burst timings are x-4-4-4	
x333	Write Burst timings are x-3-3-3	
x222	Write Burst timings are x-2-2-2	

(222 timings is the default.

#### Fast EDO Lead Off

This setting enables fast timing EDO read cycles. This result in a 1 HCLK pull-in for all read leadoff latencies for EDO DRAMs. This setting must be set to disalbed, if any of the DRAM rows is populated with FPM DRAMs.

Disabled is the default.

#### P55XUB/XUWB

#### Refresh RAS# Assertion

This setting controls th number of clocks RAS# is asserted for Refresh cycles. 4 clks is the default.

Fast RAS# to CAS# Delay

When DRAM is refreshed, both rows and columns are addressed separately. This setup item allows you to determine the timing of the transition from Row Address Strobe (RAS) to Column Address Strobe (CAS).

3	Three CPU clock delay.	
2	Two CPU clock delay.	

3 CPU clocks is the default.

DRAM Page Idle Timer

Reserved for description.

2/4/6/8 Three clocks.

Two clocks is the default.

**DRAM Enhanced** Paging

Enabled	The chipset will keep the page open until a page/row miss.
Disabled	The chipset will use additional information to keep the DRAM page open when
	host may be "right back".

Disabled is the default.

Fast MA to RAS# Delay

When DRAM is accessed, This setup item allows you to determine the timing of the transition from Memory Address (Setup time) to Row Address Strobe (RAS).

2	Two CPU clock delay.	
1	One CPU clock delay.	

2 CPU clocks is the default.

SDRAM(CAS Lat/RASto-CAS)

This setup item allows you to determine the CAS# Latency, RAS# to CAS#, active programable timings.

2/2	Two CPU clock for CAS Latency and RAS# to CAS# avtive timing.
3/3	Three CPU clock for CAS Latency and RAS# to CAS# avtive timing.

1/1 CPU clocks is the default.

**SRAM Speculative** 

This setup item allows you to determine the SDRAM speculative read logic.

Disabled is the default.

#### Cache Features

System BIOS Cacheable

When enabled, accesses to the system BIOS ROM addressed at F0000H-FFFFFH are cached, provided that the cache controller is enabled.

Enabled	BIOS access cached	
Disabled	BIOS access not cached	

Disabled is the default.

#### Video BIOS Cacheable

As with caching the System BIOS above, enabling the Video BIOS cache will cause access to video BIOS addressed at C0000H to C7FFFH to be cached, if the cache controller is also enabled

Enabled	Video BIOS access cached
Disabled	Video BIOS access not cached

Enabled is the default.

#### PCI and ISA Configuration

8 Bit I/O Recovery Time

The recovery time is the length of time, measured in CPU clocks, which the system will delay after the completion of an input/output request. This delay takes place because the CPU is operating so much faster than the input/output bus that the CPU must be delayed to allow for the completion of the I/O.

This item allows you to determine the recovery time allowed for 8 bit I/O. choices are from NA, 1 to 8 CPU clocks.

l clock is the default.

16 Bit I/O Recovery

This item allows you to determine the recovery time allowed for 16 bit I/O. Choices are from NA, 1 to 4 CPU clocks.

l clock is the default.

Memory Hole At 15M-16M

In order to improve performance, certain space in memory can be reserved for ISA cards. This memory must be mapped into the memory space below 16 MB.

Unabled	Memory hole supported.
Disabled	Memory hole not supported.

Divabled is the default.

Peer 2.1 Compliance

Divabled is the default.



#### 3.5 Power Management Setup

The Power Management Setup allows you to configure you system to most effectively save energy while operating in a manner consistent with your own style of computer use.

	AWARD SOFT	WARE, INC.	
Power Management PM Control by APM Video Off Method Video Off Method Video Off after Doze Mode Standard Mode Suspend Mode HDD Power Down Throttle Duty Cycle ZZ Active in Suspend VGA Active Monitor Soft-off by PWR-BTTN Resume by Ring *** Break Event From Sus IRQ 8 Clock Event	:User Define :Yes :V/H :Standby :Disabled :Disabled :Disabled :Disabled :62.5% :Disabled :Enabled :Delay 4 sec :Enabled :pend *** :Disabled	** Reload Global Tir IRQ[3-7, 9-15],NMI Primary IDE 0 Primary IDE 1 Secondary IDE 0 Secondary IDE 1 Floppy Disk Serial Port Parallel Port	: Enabled : Disablec : Disablec : Disalbec : Disalbec : Disalbec : Enalbed : Disalbec

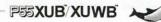
#### **Power Management**

This category allows you to select the type (or degree) of power saving and is directly related to the following modes:

- Doze Mode
- 2. Standby Mode
- 3. Suspend Mode
- 4. HDD Power Down

There are four selections for Power Management, three of which have fixed mode settings.

Disable (default)	No power management. Disables all four modes	
-------------------	--	--



Min. Power Saving	Minimum power management. Doze Mode = 1 hr. Standby Mode = 1 hr., Suspend Mode = 1 hr., and HDD Power Down = 15 min.
Max. Power Saving	Maximum power management ONLY AVAILABLE FOR SL CPU. Doze Mode = 1 min., Standby Mode = 1 min., Suspend Mode = 1 min., and HDD Power Down = 1 min.
User Defined	Allows you to set each mode individually. When not disabled, each of the ranges are from 1 min. to 1 hr. except for HDD Power Down which ranges from 1 min. to 15 min. and disable.

#### PM Control APM

When enabled, an Advanced Power Management device will be activated to enhance the Max. Power Saving mode and stop the CPU internal clock.

If the Max. Power Saving is not enabled, this will be preset to No.

#### Video Off Method

This determines the manner in which the monitor is blanked.

V/H SYNC+Blank	This selection will cause the system to turn off the vertical and horizontal synchronization ports and write blanks to the video buffer.
Blank Screen	This option only writes blanks to the video buffer.
DPMS	Initial display power management singaling.

#### PM Timers

The following four modes are Green PC power saving functions which are only configurable when *User Defined* Power Management has been selected. See above for available selections.

#### Doze Mode

When enabled and after the set time of system inactivity, the CPU clock will run at slower speed while all other devices still operate at full speed.

#### Standby Mode

PSSXUB/XUWB

When enabled and after the set time of system inactivity, the fixed disk drive and the video would be shut off while all other devices still operate at full speed.

#### Suspend Mode

When enabled and after the set time of system inactivity, all devices except the CPU will be shut off.

#### HDD Power Down

When enabled and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

#### Power Down & Resume Events

Power Down and Resume events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything which occurs to a device which is configured as On, even when the system is in a power down mode.

The following is a list of IRQ, Interrupt ReQuests, which can be exempted much as the COM ports and LPT ports above can. When an I/O device wants to gain the attention of the operating system, it signals this by causing an IRQ to occur. When the operating system is ready to respond to the request, it interrupts itself and performs the service.

As above, the choices are On and Off. Off is the default.

When set On, activity will neither prevent the system from going into a power management mode nor awaken it.

#### 3.6 PnP/ PCI Configuration Setup

This section describes configuring the PCI bus system. PCI, or Personal Computer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed the CPU itself uses when communicating with its own special components. This section covers some very technical items and it is strongly recommended that only experienced users should make any changes to the default settings.

		PNP/PCI C	BIOS (XXXXXXXX) ONFIGURATION FTWARE, INC.
PNP OS Installed Resources Controlled By Resources Controlled By		:No :Auto :Disabled	PCI IRQ Map To :PCI-AUTO Primary IDE Int# :A Secondary IDE Int# :B
IRQ-3 IRQ-4 IRQ-5 IRQ-7 IRQ-9 IRQ-10 IRQ-11 IRQ-12 IRQ-14 IRQ-15 DMA-0	Assigned to	PCI/ISA PnP PCI/ISA PnP	Used MEM base addr : N/A
DMA-1 DMA-3 DMA-5 DMA-6 DMA-7	Assigned to :	PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP PCI/ISA PnP	Esc : Quit F1 : Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Change Color F7 : Load Setup Defaults

PNP OS Installed

The Award Play BIOS has the capacity to support PNP OS. such as Windows 95®

Choices are Yes and No (default).

Resource Controlled

The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows® 95.

Choices are Auto and Manual (default).

Reset Configuration Data

This item allows you to determine reset the configuration data or not.

Choices are Enabled and Disabled (default).

IRQ3/4/5/7/9/10/11/12/ 14/15, DMA0/1/3/5/6/7 assigned to

This item allows you to determine the IRQ / DMA assigned to the ISA bus and is not available to any PCI slot.

Choices are Legacy ISA and PCI/ISA PnP.

PCI IDE IRQ Map to

This allows you to configure your system to the type of IDE disk controller in use. By default, Setup assumes that your controller is an ISA (Industry Standard Architecture) device rather than a PCI controller. The more apparent difference is the type of slot being used.

If you have equipped your system with a PCI controller, changing this allows you to specify which slot has the controller and which PCI interrupt (A, B,C or D) is associated with the connected hard drives. Remember that this setting refers to the hard disk drive itself, rather than individual partitions. Since each IDE controller supports two separate hard drives, you can select the INT# for each.

Again, you will note that the primary has a lower interrupt than the secondary as described in *lot x Using INT#*" above.

Selecting "PCI Auto" allows the system to automatically determine how your HIH disk system is configured.

Award's ROM BIOS provides a built-in Setup program which allows user modify the system configuration and hardware parameters. The modified data will be found in a battery-backed CMOS RAM, so data will be retained even the system power is off. In general, you are not required to change any data. Unless there is a multiet or you re-configuring system, this will need to enter new setup information. Following section describes how to use this program and propriate

The "LOAD SETUP DEFAULT" is recommended in your first time setup this your or you change the system's configuration. You will need "LOAD SETUP HEAULT" first and re-confirure your system. This will be described in later

And it is possible that battery failed which might cause data lose in CMOS RAM, then you need to re-enter the system's CMOS RAM and re-configure to get the middle parameters.



# 3.7 Integrated Peripherals

	M PCI/ISA BIC INTEGRATED WARD SOFT	PERIPHERA	K) LS
IDE Primary Master PIO IDE Primary Slave PIO IDE Secondary Master PIO IDE Secondary Master PIO IDE Secondary Slave PIO IDE Primary Master UDMA IDE Primary Slave UDMA IDE Secondary Master UDMA IDE Secondary Master UDMA IDE Secondary PCI IDE On-Chip Primary PCI IDE On-Chip Secondary PCI IDE Onboard FDC Controller Onboard Serial Port 1 Onboard Serial Port 2	:Auto :Enabled :Enabled :Enabled :3F8/IRQ4 :2F8/IRQ3	Onboard PC	
Onboard Parallel Port Parallel Port Mode ECP Mode Use DMA	:378/IRQ7 :ECPEPP1.9 :3	Esc : Quit F1 : Help F5 : Old Valu F7 : Load Se	↑↓→← : Select Item PU/PD/+/- : Modify es (Shift)F2 :Change Co tup Defaults

On-Chip Secondary PCI IDE

This allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive (HDD).

Enabled	IDE controller uses block mode.	
Disabled	IDE controller uses standard mode.	

#### IDE PIO

IDE hard drive controllers can support up to two separate hard drives. These drives have a master/slave relationship which are determined by the cabling configuration used to attach them to the controller. Your system supports two IDE controllers--a primary and a secondary--so you have to ability to install up to four separate hard disks.

PIO means Programmed Input/ Output. Rather than have the BIOS issue a series of commands to effect a transfer to or from the disk drive, PIO allows the BIOS

to tell the controller what it wants and then let the controller and the CPU perform the complete task by themselves. This simpler and more efficient (and faster).

Your system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When *Auto* is selected, the BIOS will select the best available mode. This is true for the next four setup items:

- 1. IDE Primary Master PIO
- 2. IDE Primary Slave PIO
- 3. IDE Secondary Master PIO
- 4. IDE Secondary Slave PIO

#### IDE Ultra DMA-33

UDMA means Ultra Direct Memory Access. It's a new function available with this motherboard's chipset.

Your system supports two modes, enabled or disabled.

- 1. IDE Primary Master UDMA
- 2. IDE Primary Slave UDMA
- 3. IDE Secondary Master UDMA
- 4. IDE Secondary Slave UDMA

On-Chip Primary PCI IDE

As stated above, your system includes two built-in IDE controller, both a primary controller. You might choose to disable the controller if you want to higher performance or specialized controller.

Enabled	Primary HDD controller used Default
Disabled	Primary HDD controller not used.



## On-Chip Secondary PCI IDE

As above for the Primary controller, this setup item you either to enable or disable the secondary controller. You might choose to disable the controller if you were to add a higher performance or specialized controller.

Enabled	Primary-HDD controller used	
Disabled	Primary HDD controller not used.	

#### Enabled is the default.

### Onboard FDC Controller

Your system includes a built-in FDC controller. This setup item allows you either to enable or disable the floppy controller. You might choose to disable the controller if you were to add a higher performance or specialized controller.

Enabled	FDC controller used
Disabled	FDC controller not used.

### Enabled is the default.

## **Onboard Serial Port1**

Your system includes two built-in Serial Port. This setup item allows you to change the Serial 1 setting.

3F8/IRQ4	Serail Port Address=3F8h, interrupt=IRQ4	
2F8/IRQ3	Serail Port Address=2F8h, interrupt=IRQ3	
3E8/IRQ4	Serail Port Address=3E8h, interrupt=IRQ4	
2E8/IRQ3	Serail Port Address=2E8h, interrupt=IRQ3	
Disabled	Disable Serial 1	
Auto	Automatically select address interrupt	

## 3F8/IRQ4 is the default.

## Onboard Serial Port2

Your system includes two built-in Serial Port. This setup item allows you to change the Serial 2 setting.

3F8/IRQ4	Serail Port Address=3F8h, interrupt=IRQ4	
2F8/IRQ3	Serail Port Address=2F8h, interrupt=IRQ3	
3E8/IRQ4	Serail Port Address=3E8h, interrupt=IRQ4	
2E8/IRQ3	Serail Port Address=2E8h, interrupt=IRQ3	
Auto	Automatically select address interrupt	the Day
Disabled	Disable Serial 1	

# 2F8/IRQ3 is the default.

## Onboard Parallel Port

Your system includes a built-in Printer Port. This setup item allows you to change the Printer Port setting.

378/IRQ7	Serail Port Address=378h, interrupt=IRQ7	
278/IRQ5	Serail Port Address=278h, interrupt=IRQ5	
3BC/IRQ7	Serail Port Address=3BCh, interrupt=IRQ7	-
Disabled	Disable Serial 1	

# 178/IRQ7 is the default.

# Parallel Port Mode

Your system includes a built-in Printer Port. This setup item allows you to change the Printer Port Mode setting.

ECPEPP1.9	ECP & EPP Mode Ver1.9	
SPP	Standard Printer Port	
EPP1.7	EPP Mode Ver1.7	
ECPEPP1.7	ECP & EPP Mode Ver1.9	



# PSSXUB XUWB

PS/2	PS/2 Mode	
EPP1.9	EPP Mode Ver1.9	
ECP	ECP	No. of Street, B

#### ECPEPP1.9 is the default.

ECP Mode Use DMA

As above fro the ECP Mode, this setup item allows you to change the DMA Channel for the ECP Mode Printer Port setting.

3	DMA 3	18/38/86/8
1	DMA 1	

#### DMA3 is the default.

Onboard PCI SCSI Chips

Your system includes an PCI SCSI Chips, this setup item allows you either to enable or disable your on-onboard SCSI Controller.

Enabled	Enable on-board SCSI	BAR VICE HENCE
Disabled	Disabled on-board SCSI	

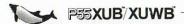
#### Enabled is the default.



# 3.8 LOAD SETUP DEFAULTS

The chipset defaults are settings which provide for maximum system performance. While Award has designed the custom BIOS to maximize performance, the manufacturer has the right to change these defaults to meet their

CMOS S	A BIOS (XXXXXXXX) ETUP UTILITY OFTWARE, INC.	
STANDARD CMOS SETUP BIOS FEATURES SETUP CHIPSET FEATURES SETUP	USER PASS	DR PASSWORD SWORD JTO DETECTION —EL FORMAT
POWER MANAGEI PNP/ PCI CONFIGI INTEGRATED PHE LOAD SETUP DEFAULTS	EFAULTS (Y/N) ? N	ETUP SAVING
Esc : Quit F10 : Save & Exit Setup	↑↓→→ : Select Item (Shift) F2 : Change Color	
Time, Date, Ha	ard disk Type	



# 3.9 Supervisor/User Password Setting

СМО	OUISA BIOS (XXXXXXXX) OS SETUP UTILITY RD SOFTWARE, INC.
STANDARD CMOS SETUP BIOS FEATURES SETUP CHIPSET FEATURES SETUP POWER MANAGEN PNP/ PCI CONFIGIL Enter Passwi	SUPERVISOR PASSWORD USER PASSWORD IDE HDD AUTO DETECTION EL FORMAT
INTEGRATED PHE LOAD SETUP DEFAULTS	SAVING
Esc : Quit F10 : Save & Exit Setup	◆↓→← : Select Item (Shift) F2 : Change Color
Time, Date	e, Hard disk Type

You can set either supervisor or user password, or both of them. The differences between are:

supervisor password: can enter and change the options of the setup menus. user password: just can enter but do not have the right to change the options of the setup menus.

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

#### ENTER PASSWORD:

Type the password, up to eight characters in length, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable a password, just press <Enter> when you are prompted to enter the password. A message will confirm the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED.

When a password has been enabled, you will be prompted to enter it every time you try to enter Setup. This prevents an unauthorized person from changing any part of your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time your system is rebooted. This would prevent unauthorized use of your computer.

#### 3.10 IDE HDD AUTO DETECTION

Automatically detect and configure hard disk parameters. The Award BIOS includes this ability in the event you are uncertain of your hard disk parameters.

#### 3.11 HDD LOW LEVEL FORMAT

If supported by your system, this provides a hard disk low level format utility.

#### 3.12 SAVE & EXIT SETUP

Save CMOS value changes to CMOS and exit setup.

#### 3.13 EXIT WITHOUT SAVING

Abandon all CMOS value changes and exit setup.

# CHAPTER 4 SCSI BIOS Setup

Like the system BIOS, the SCSI BIOS is responsible for management/ control the SCSI hardware parameters setting. These parameters include the SCSI ID, Terminator setting, and SCSI devices behavior pattern on the SCSI system. During system power on or warm reset the SCSI BIOS will scan all SCSI devices that connect to the SCSI bus and according to each setting default perform its behavior pattern.

During system power on and after boot system BIOS, the screen will show:

Adaptec AIC-78XX Ultra BIOS vx.xx

(c) Adaptec, Inc. All Rights Reserved.

\*\*\*Press <Ctrl> <A> for SCSISelect(TM) Utility !\*\*\*.

# 4.1 When to Use the SCSISelect Utility

Use the SCSISelect utility if you need to

- . Change any of the default values.
- Check and / or change SCSI device settings that may conflict with those of other devices (e.g., SCSI ID).
- Perform low level formatting on new SCSI disk devices.

## Running the SCSISelect Utility

You will see a banner similar to the one shown in b elow when you turn on or reboot your computer. The BIOS banner lists the model number and SCSI ID of each SCSI device connected to the host adapter.

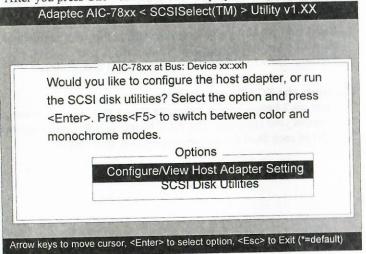
Adaptec AIC-78XX BIOS vx.xx (c) 1954 Adaptec, Inc. All Rights Reserved. Press <Ctrl><A> for SCSISelect (TM) Utility! P1-17S - MAXTOR SCSI ID #0 -Drive D: (81h) - QUANTUM P40S-94-40-04xx CD\_ROM: XX3355 - Toshiba SCSI ID #3 VIPER 150 21247 - ARCHIVE SCSI ID #4 BIOS Installed Successfully!

To start SCSI Select, press Ctrl + A when the BIOS banner first appears on the screen.

Note: If you only connect a non-bootable device, this BIOS can not be installed.

# 4.2 SCSISelect Utility Options

After you press Ctrl + A then it will displays the Options menu as below.



Use the Up and Down keys and the **Enter** key to make selections in the SCSI Select utility . Press **Esc** at any time to return to the previous menu . **Note:** You can press **F5** to toggle the display between color and monochrome modes . (This feature may not work on some kinds of monitors.)

# 4.3 Configure/View Host Adapter Settings

The Configure / View Host Adapter Settings menu lists three settings under SCSI Channel Interface Definitions, as shown in following Figure.

AIC-78xx at Bus: Device xx:xxh	
Configuration	
SCSI Channel Interface Definitions	
Host Adapter SCSI ID	7
SCSI Parity Checking	Enabled
Host Adaptec SCSI Termination	Low ON/ High O
Additional Options	
Boot Device Configuration	Press <enter></enter>
SCSI Device Configuration	Press <enter></enter>
Advanced Configuration Options	Press <enter></enter>
<f6> -Reset to Host Defaults</f6>	

Use the cursor (Up, Down) to move to your selection. Press Enter to display a pop-up menu of choices or to make selections. Press Esc at any time to return to the previous menu.

Note: Press F6 to reset all settings to the host adapter defaults. Host adapter default settings are marked with an asterisk (\*) throughout the selection submenus.

# 4.3.1 Host Adapter SCSI ID

This option allows you to change the host adapter SCSI ID. There are 8 available IDs for the P55TV/TVS. The default is ID 7, which has the highest priority on the SCSI bus. ( We recommend that you not to change this setting ).

Each SCSI device on the SCSI bus, including the host adapter, must be set to a unique SCSI ID.

The SCSI ID serves two purposed: it uniquely identifies each SCSI device on the bus, and it determines the device's priority on the bus during the Arbitration phase. The Arbitration phase determines which device controls the bus when two or more devices request use of it.

Use the cursor (û ♣) and Enter keys to select the SCSI ID, if you need to change it . Press Esc at any time to return to the previous menu.



When selecting the host adapter SCSI ID, consider the following:

- If you install more than one SCSI host adapter in the computer, each board has its own SCSI bus. This means devices can have duplicate SCSI IDs, as long as they are not on the same SCSI bus (e.g. each SCSI bus can have a device with SCSI ID 0, ets.).
- If you plan to connect two host adapters in two different computers to the same SCSI bus to they can share SCSI devices (see Appendix C, Multiple Computer Configuration), set the host adapters to different SCSI IDs. IDs 6 and 7 are preferable, since they have the highest priority on the SCSI bus.

#### 4.3.2 SCSI Parity Checking

Select this option to enable or disable SCSI Parity Checking on the host adapter. The default setting is Enable.

The host adapter always checks parity when reading from the SCSI bus to verify the correct transmission from your SCSI devices . You should disable SCSI Parity Checking if any attached SCSI devices do not support SCSI parity . (Most currently available SCSI devices do support SCSI parity.)

Use the cursor  $(\hat{U} \cup \hat{V})$  and Enter keys to make selections. Press Esc at any time to return to the previous menu.

## 4.3.3 Host Adapter SCSI Termination

This option allows you to configure host adapter SCSI termination.. The default setting for the is **Enabled**.

Use the cursor ( $\widehat{U}$   $\mathbb{Q}$ ) and **Enter** keys to make your selection . The Termination is determined by which of the SCSI connectors on the board have devices attached to them . The possible P54TV termination settings are as follows:

Host Adapter Termination	Devices Are Attached to
Enabled	Internal connector only
Enabled	External connector only
Disabled	Internal and external connectors

# 4.3.4 Boot Device Configuration

This option allows you to choose which SCSI ID device has the Boot right for system O.S.

#### 4.3.5 SCSI Device Configuration

This option allows you to configure certain parameters of each SCSI device on the SCSI bus.

Use the cursor keys ( $\hat{\mathcal{D}}$ ) to move between options . Press **Enter** to display a pop-up menu with a selection of values . Use the cursor keysx ( $\hat{\mathcal{D}}$ ) to select a value , and press **Enter** to make your selection.

#### ■ Initiate Sync Negotiation

This option determines whether the host adapter initiates synchronous negotiation with the SCSI device.

When set to yes, the host adapter initiates synchronous negotiation with the SCSI device . When set to no, the host adapter does not initiate synchronous negotiation. The host adapter , however, always responds to synchronous negotiation if the SCSI device initiates it . The default setting is yes.

Data is transferred in asynchronous mode if neither the on board SCSI nor the SCSI peripheral negotiates for synchronous data transfers.

**Note:** Some older SCSI-1 devices do not support synchronousnegotiation. This may cause your computer to operate erratically or hang if Initiate Sync Negotiation is enabled. Set Initiate Sync Negotiation to **no** for these devices.

# ■ Maximum Sync Transfer Rate

This option determines the maximum synchronous data transfer rate that the host adapter can support. The host adapter supports rates up to the Ultra SCSI maximum of 20.0 MBytes / sec.

#### **■** Enable Disconnection

This option determines whether the host adapter allows a SCSI device to disconnect from the SCSI bus (sometimes called Disconnect / Reconnect). Disconnect / Reconnect allows the host adapter to perform other operations on the SCSI bus while the SCSI device is temporarily disconnected.

When set to yes, the SCSI device may disconnect from the SCSI bus. The SCSI device, however, may choose not to disconnect, even if permitted by the host adapter (this can usually be configured on the SCSI device). When set to no, the SCSI device is not allowed to disconnect from the SCSI bus. The default setting is yes.

You should leave Enable Disconnection set to  $\mbox{yes}$  if two or more SCSI devices are connected to the on board SCSI port . This optimizes SCSI bus performance. If only one SCSI device is connected to SCSI port , set Enable Disconnection to  $\mbox{no}$  to achieve slightly better performance.

no.



#### ■ Send Start Unit Command

This option , which is supported by some SCSI devices, determines whether the Start Unit Command (SCSI command 1B) is sent to the SCSI device (most devices do not require this ). Enabling this option reduces the load on your computer's power supply by allowing the host adapter to power -up SCSI devices one - at - a-time when you boot your computer. Otherwise , the devices all power -up at the same time . Most devices require you to set a jumper before they can respond to this command .

When set to yes, the Start Unit Command is sent to the SCSI device during bootup. When set to no, each SCSI device powers -up in its normal fashion. The default setting is

**Note:** The Send Start Unit Command setting is valid only if the host adapter BIOS is enabled.

If this option is enabled for more than one SCSI device, the Start Unit Command is sent first to the device with the lowest SCSI ID. When this device responds to the host adapter, the Start Unit Command is sent to the next highest SCSI ID with a setting of **yes**. The process continues until all supported devices responds to the host adapter.

Note: If many drivers are set to **yes** for Send Start Unit Command, the boot time varies depending on how long it takes each drive to spin up.

## 4.3.6 Advanced Configuration Options

When you select Advanced Configuration Options. Do not change these five options unless absolutely necessary.

Use the cursor keys ( $\widehat{U}$   $\mathbb{Q}$ ) to move between options . Press **Enter** to display a pop-up menu with a selection of options . Use the cursor keys ( $\widehat{U}$   $\mathbb{Q}$ ) to select an options , and press **Enter** to make your selection.

# ■ Plug & Play SCAM Support

This SCSI port provides the Plug & Play specification, this is different with Microsoft Windows 95's specification.

When the SCAM function is enabled, the devices ID & Termination will be automatically done. Of course, this need both the devices and Host have the SCAM function. Otherwise, keep this function disabled. The SCAM is a Plug & Play specification

#### Host Adapter BIOS

This option enables or disables the on board SCSI BIOS . The default setting is Enabled.

The host adapter BIOS must be enabled if you want the computer to boot from a SCSI hard disk drive connected to the host adapter. Several SCSISelect options cannot be used unless the SCSI BIOS is enabled.

#### Support Removable Disk Under BIOS as Fixed Disks

This option allows you to control which removable - media drives are supported by the SCSI BIOS . It is only valid if the SCSI BIOS is enabled . The default setting is Boot Only. The following choices are available :

**Boot Only** - Only the removable - media drive designated as the boot device are treated as a hard disk drive.

All Disks - All removable - media drives supported by the BIOS are treated as hard disk drives.

Disabled - No removable - media drives are treated as hard disk drives .

In this situation, software drives are needed because the drive are not controlled by the BIOS.

Caution: Support for removable - media drives means only that the host adapter BIOS allows you to use a removable - media drive as if it were a hard disk drive; it does not mean you can remove the disk media during operation. If a removable - media SCSI device is controlled by the host adapter BIOS, do not remove the media while the drive is powered - on or you may lose data. If you want to be able to remove media while the power is on, install the removable - media device driver and set this option to Disabled.

# • Extended BIOS Translation for DOS Drives > 1 GByte

This option allows you to enable or disable extended translation for SCSI hard disks with a capacity greater than 1 GByte. It is only valid if the host adapter BIOS is enabled . The default setting is Enabled.

If this option is enabled , the following translation schemes are used: SCSI hard disks 1 GByte use a translation schema of 64 heads , 32 sectors per track

SCSI hard disks 1 GByte use a translation schema of 255 heads , 63 sectors per track



Display <Ctrl-A> Message Durig BIOS initialization.
 This option allows you to enable or disable the BIOS prompt for the SCSI utility.

Multiple Lun Support
 This option allows you to enable or disable the SCSI Lun support

■ BIOS Support for Int13 Extensions

This option allows you to Enabled or Disabled the Int13 Extensions. The SCSI BIOS need to change the Int13h to make the SCSI been recognized by O.S.

Support for Ultra SCSI speed Enabled the Ultra SCSI functin by change this option. The maximum Data Transfer Rate of this SCSI Bus is 20MB/Sec. For support this function, check your SCSI device vendor to make sure your SCSI device support the Ultra function.

BIOS Support for More Than 2 Drives
 This option allows you to enable or disable BIOS support for more than two, and up to eight, SCSI hard disk drives. It is only valid if the host adapter BIOS is enabled. This feature is supported by BIOS 5.0 and above. The default setting is Enabled.

4.4 SCSI Disk Utilities
When you select SCSI Disk Utilities from the Options menu the SCSI Select utility scans the SCSI bus and lists all SCSI devices installed on the SCSI bus. You can easily determine from this screen which SCSI ID is assigned to each device on the SCSI bus.

When you highlight a disk drive by moving to it with the cursor keys and press Enter, a small menu window appears. You then select Format Disk or Verify Media from this menu.

Use the cursor keys ( $\hat{U}$   $\hat{U}$ ) to move between options. Press **Enter** to display a pop- up menu with a selection of values. Use the cursor keys ( $\hat{U}$   $\hat{U}$ ) to select a value, and press **Enter** to make your selection.

Format Disk

The Format Disk utility performs a low - level format on disk devices . Your fixed disk media must be low-level formatted before you can use your operating system's partitioning and file preparation utilities, such as MS-DOS fdisk and format.

Most SCSI disk devices are pre-formatted and do not need to be formatted again. The Adapter Format Disk utility is compatible with the vast majority of SCSI disk drives.Run it on hard disk drives or removable - media drives that were previously used with a non - Adaptec host adapter.

Caution: A low - level format destroy all data on the drive. Be sure to back up your data before performing this operation. You cannot abort a low - level format once it is started.

### Verify Disk Media

The Verify Disk Media utility scans the selected device's media for defects. If the utility finds bad blocks, it prompts you to reassign them; if you select yes, those blocks will no longer be used.

Note: You can press Esc at any time to abort the Verity Disk Media utility.

Memory Choices
128 MB CT 16 M 64 S4 D10
SDRAM, P66 CL=2 Non-Parity
\$ 62.99 Crucial.com



Rev:1.1A

PMXUWB1000E

One jumper setting design is patented by Iwill Corp.

Intel and the Intel logo are trademarks of Intel Inc.

All other brand names and trademarks are the property of their respective owner.