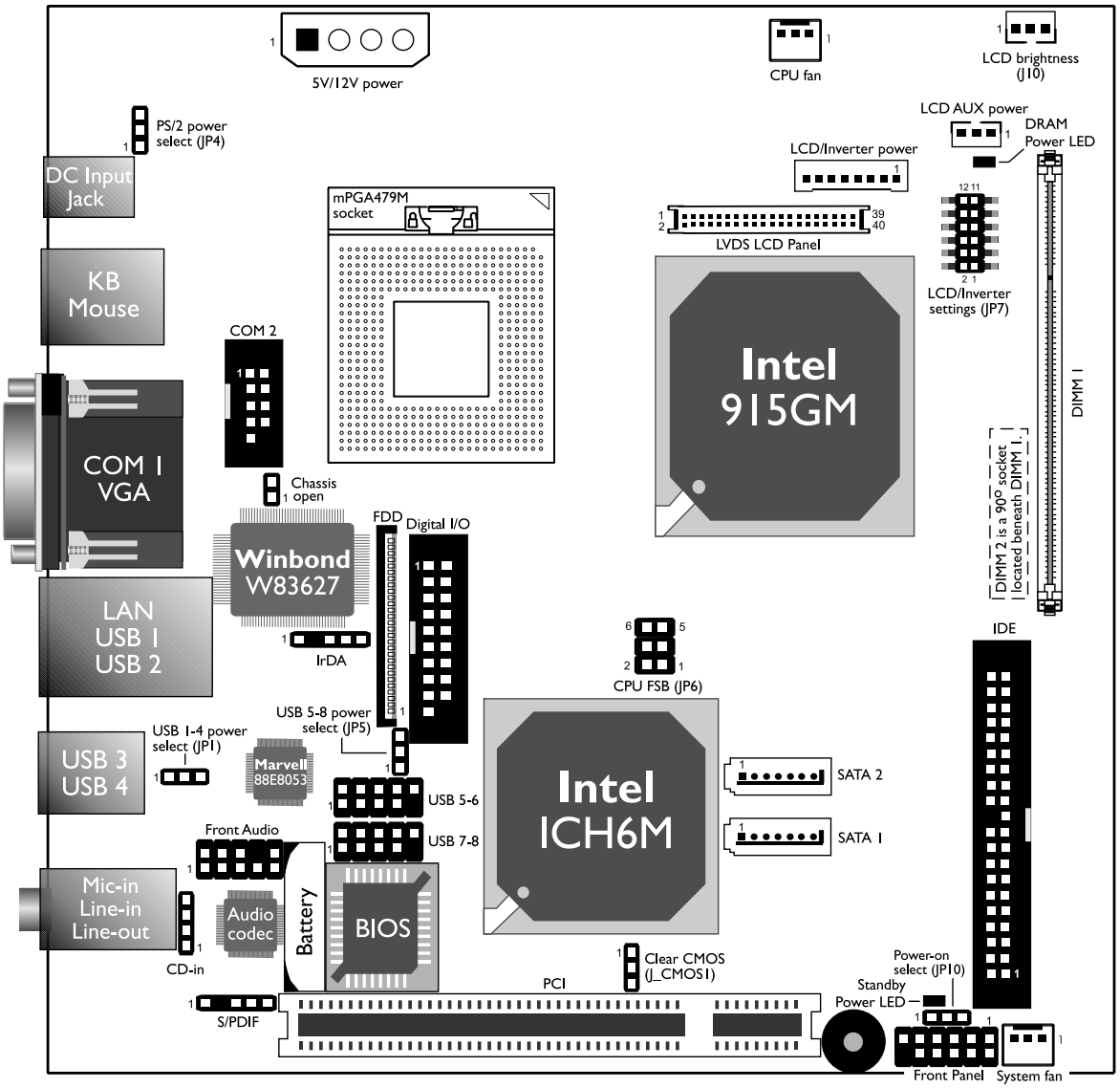
G5G101-B



Clear CMOS Data	J_CMOS1
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

PS/2 Power	JP4
5V (default)	1-2 On
5V_standby	2-3 On

USB Power: 1-4 (JP1), 5-8 (JP5)	
5V (default)	1-2 On
5V_standby	2-3 On

LCD Brightness Control (Voltage Level Adjust)	J10
Increases the voltage level	1-2 On
Decreases the voltage level	2-3 On

Note: Increasing or decreasing the voltage to control the LCD panel's brightness varies among Inverters. You must refer to the Inverter's specification to make the appropriate adjustment to the brightness of the LCD panel.

LCD/Inverter Settings - JP7					
Panel Power		Inverter On Level		Inverter On/Off Select	
5V	1-3 On	5V	2-4 On	Active Low	8-10 On
3.3V	3-5 On	12V	4-6 On	Active High	10-12 On
12V	7-9 On				

Note: Before powering-on the system, make sure JP7's setting matches the LCD panel's specification. Selecting the incorrect voltage will seriously damage the LCD panel.

※ The following Intel Celeron® M 65nm processors are NOT supported: 410, 420, 423, 430, 440 and 450.

Pentium M Processor FSB Select	JP6
FSB400	4-6 On
FSB533	3-5, 4-6 On

Power-on Select	JP10
Power-on via AC power	1-2 On
Power-on via power button	2-3 On

※ The CompactFlash™ socket is located on the solder side of the board.

934-G5G111-000G
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