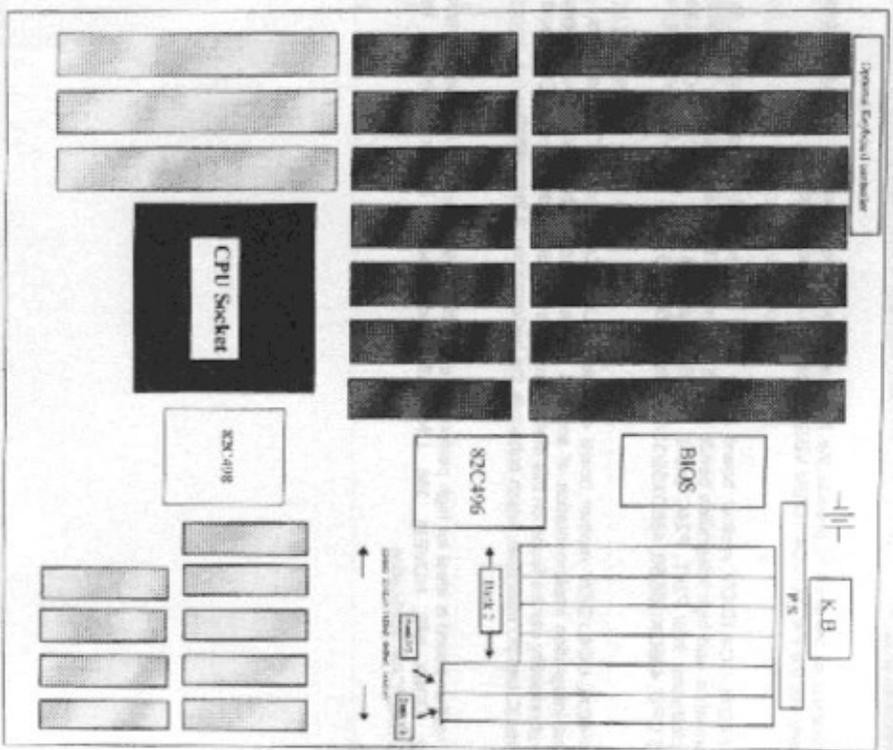# II GENERAL SPECIFICATIONS

- UM82C498 and UM82C496 Chipsets

- Flexible integrated DRAM Controller

- Support Multi-Type DRAM

- Support up to 192MB DRAM, 64 MB on 30 pins SIMM and 128 MB on 72 pins SIMM

- Built-in Cache Controller:
  - Support L1 write back or write through cache
  - 8 KB Internal cache
  - Support up to 256 KB External cache
  - Multi-Type SRAM: 8K8, 32K8, 64K8

- Built-in Keyboard Controller with outside option

- Provide Green PC power management:
  - Programmable CPU Clock down to 0 MHz
  - 4 power management mode: ON, STANDBY, INACTIVE, OFF MODES
  - Programmable CPU clock down to 0 MHz
  - Fully Support Microsoft Auto Power Management

- Seven 16-bit Input/Output (I/O) slots

- Three VESA Local Bus slots

F4DXL-UC4.1D/DV mother board lay-out

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VESA CARD INSTALL OPERATION

The mother board consists of Three (3) VESA Local Bus slots, each of them can be served as Master/Slave. The following jumper selection is needed for installing the VESA Local Bus Add-On cards.

VL BUS OPTION

	CLOSE	OPEN
JP21	*For VESA clock > 33 MHz	*For VESA clock <= 33 MHz
JP22	For VESA 1 WS	*For VESA 0 WS

* Factory Default Setting

CPU (MICROPROCESSOR) INSTALL SELECTION

CPU	JP11	JP12	JP13	JP17	JP18	JP19
486SX	OPEN	2-3	2-3	OPEN	OPEN	OPEN
*486DX	OPEN	2-3	1-2, 3-4	1-2	OPEN	OPEN
486DX-SL	1-2	1-2	1-2, 3-4	1-2	5-6	1-2, 3-4
P24D	1-2, 4-5	1-2, 4-5	1-2, 3-4	1-2	3-4, 5-6	1-2, 3-4
P24T	1-2	1-2	1-2, 3-4	2-3	5-6	1-2, 3-4
M6	1-2, 3-4, 5-6	1-2, 3-4, 5-6	2-3	OPEN	2-3, 4-5	2-3, 4-5
M7	1-2, 3-4, 5-6	1-2, 3-4, 5-6	1-2, 3-4	1-2	2-3, 4-5	2-3
UMC 486	2-3	2-3	2-3	3-4	1-2	OPEN
AMD 486 DXL/DX2	2-3	2-3	1-2, 3-4	1-2, 3-4	1-2	OPEN
#AMD 486 DX2-80	2-3	2-3	1-2, 3-4	1-2, 3-4	1-2	OPEN

Remark: For using AMD486 DX2-80 CPU, a wire jumper is needed between pin 4 of JP12 and pin 3 of JP20. There is no wire jumper needed when using DX2-50 or DX2-66 MHz CPU.

CPU POWER SETTING

	JP14	JP15	JP16	JP23
3.3 V	1-2	1-2	1-2	CLOSE
3.6 V	1-2	1-2	1-2	OPEN
*5.0 V	2-3	2-3	2-3	OPEN

WARNING FOR 3.3 V and 3.6V CPU

1) For using 3.3 V OR 3.6V CPU, a Voltage Regulator LT1086 should be used on U24

2) Adjust Jumpers JP14 - JP16, & JP23 as shown in CPU Power Setting of INSTALLATION Section.

CPU SPEED SELECTION (CLOCK SPEED SELECTION)

CPU CLOCK	JP4	JP5	JP6
25 MHz	OFF	OFF	ON
*33 MHz	ON	ON	ON
40 MHz	OFF	ON	ON
50 MHz	ON	OFF	OFF

*Factory default setting.

7

V JUMPER/CONNECTOR SELECTION

Please kindly take note about this session for we will discuss all the jumpers/connectors which are on the F4DXL-UC4.1D/DV mother board.

5.1 CONNECTOR SUMMARY

CONNECTOR	FUNCTION
KB1	KEYBOARD CONNECTOR
JP1	CONNECTOR FOR EXTERNAL BATTERY
JP26/RESET	HARDWARE RESET SWITCH CONNECTOR
JP27/TBSW	TURBO SWITCH CONNECTOR
D1/TBLED	TURBO LED CONNECTOR
JP28/SPEAKER	SPEAKER CONNECTOR
JP29/KEYLOCK	KEYBOARD LOCK CONNECTOR

5.2 CONNECTOR DEFINITION

JP1 CONNECTOR FOR EXTERNAL BATTERY

PIN	ASSIGNMENT
*ON BOARD BATTERY 2-3	NORMAL
ON BOARD BATTERY : 3-4	RESET CMOS
EXTEND BATTERY : 1	POSITIVE TERMINAL
EXTEND BATTERY : 4	NEGATIVE TERMINAL

JP26/RESET HARDWARE RESET SWITCH CONNECTOR

PIN	ASSIGNMENT
CLOSE	RESET
OPEN	NORMAL

JP27/TBSW TURBO SWITCH CONNECTOR

PIN	ASSIGNMENT
OFF (OPEN)	NON-TURBO MODE
ON (CLOSE)	TURBO MODE

D1/TBLED   TURBO LED CONNECTOR

PIN	ASSIGNMENT
1	LED+ (ANODE)
2	LED- (CATHODE)

JP28/SPEAKER SPEAKER CONNECTOR

PIN	ASSIGNMENT
1	SPEAKER DATA
2	NOT USED
3	GROUND
4	+5V

JP29/KEYLOCK   KEYBOARD LOCK CONNECTOR

PIN	ASSIGNMENT
1	+5V
2	NOT USED
3	GROUND
4	LOCK
5	GROUND

5.3   JUMPER SETTING

LOCAL BUS DEVICE CONTROL JP21, JP22

JP21	CLOSE,	=	*VESA CLOCK > 33MHz
	OPEN	=	VESA CLOCK ≤ 33MHz
JP22	OPEN,	=	*0WS
	CLOSE,	=	1WS

* Factory default setting